



# Multimedia Applications Processors – Arm11™ Core, Vector Graphics Hardware Accelerator

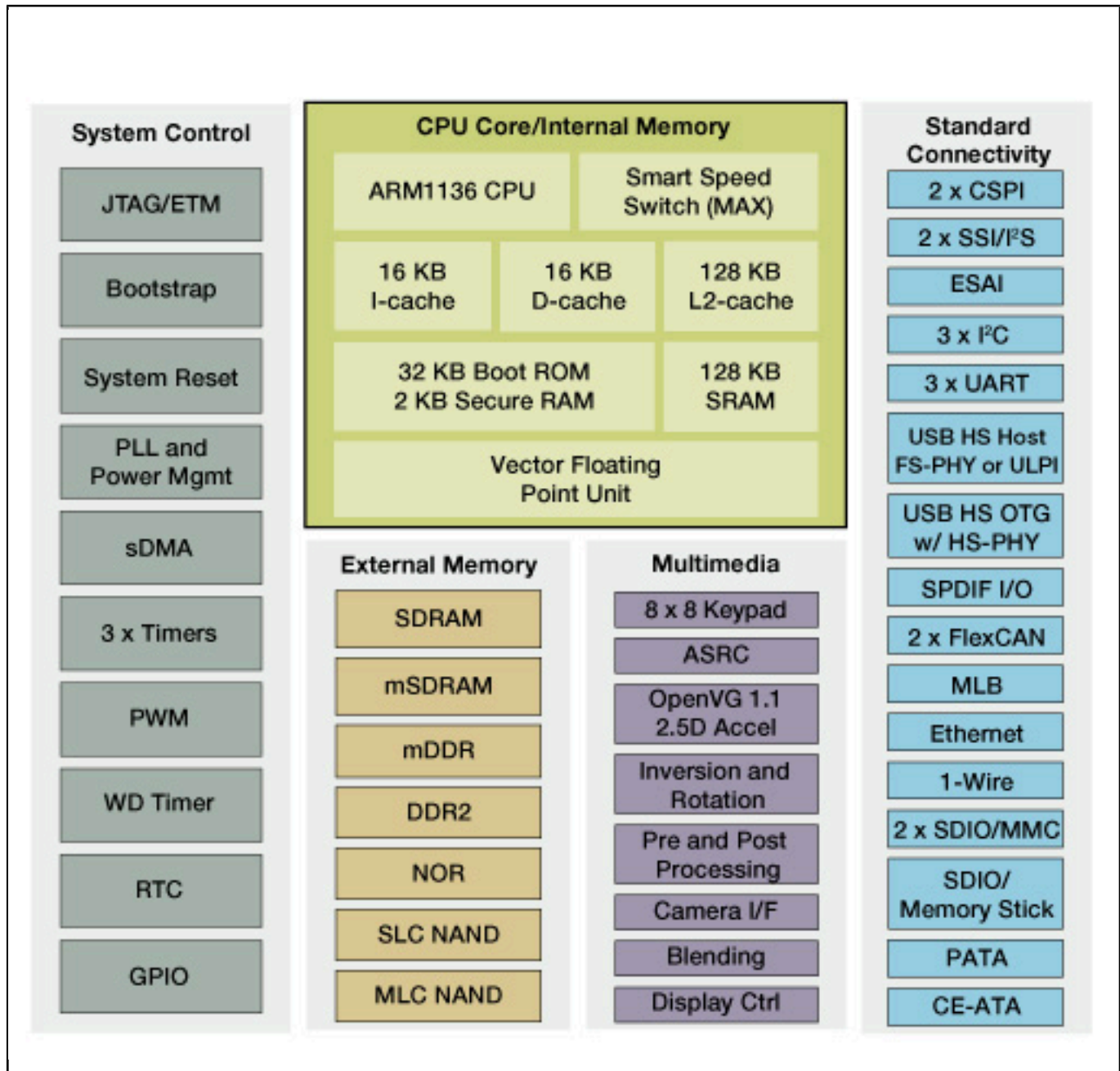
## i.MX356

Last Updated: Dec 29, 2025

The i.MX356 provides all of the functionality required in today's automotive market. With an Arm11™ core running at 532 MHz the i.MX356 integrates an OpenVG™ 1.1 vector graphics hardware accelerator. The OpenVG was integrated to deliver smooth textural visuals required in today's automotive infotainment systems. The processor features an advanced Image Processing Unit (IPU) with updated resizing capabilities to handle wide-VGA and increased the interface to 24-bit for higher color display capabilities. To reduce BOM cost flexible connectivity options were integrated, such as 2-CAN modules, Media Local Bus (MLB), Enhanced Serial Audio Interface (ESAI), Ethernet, and two USB ports with integrated PHYs. The i.MX356 can connect too many different peripherals like Wi-Fi via SDIO or USB, Bluetooth via UART or SSI/I2S and external storage via CE-ATA or PATA. Support for lower cost memories like 3.3V SDRAM, DDR2, and multi-level cell NAND were integrated.

The i.MX356 is supported by companion NXP® power management ICs (PMIC), [MC13892](#) and [MMPF0100](#).

# Freescale i.MX356 Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Arm11™ Core, Vector Graphics Hardware Accelerator](#).

Note: The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.