



Multimedia Applications Processors - HD Video, High-Performance, Low-Power, Arm® Cortex®-A8 Core

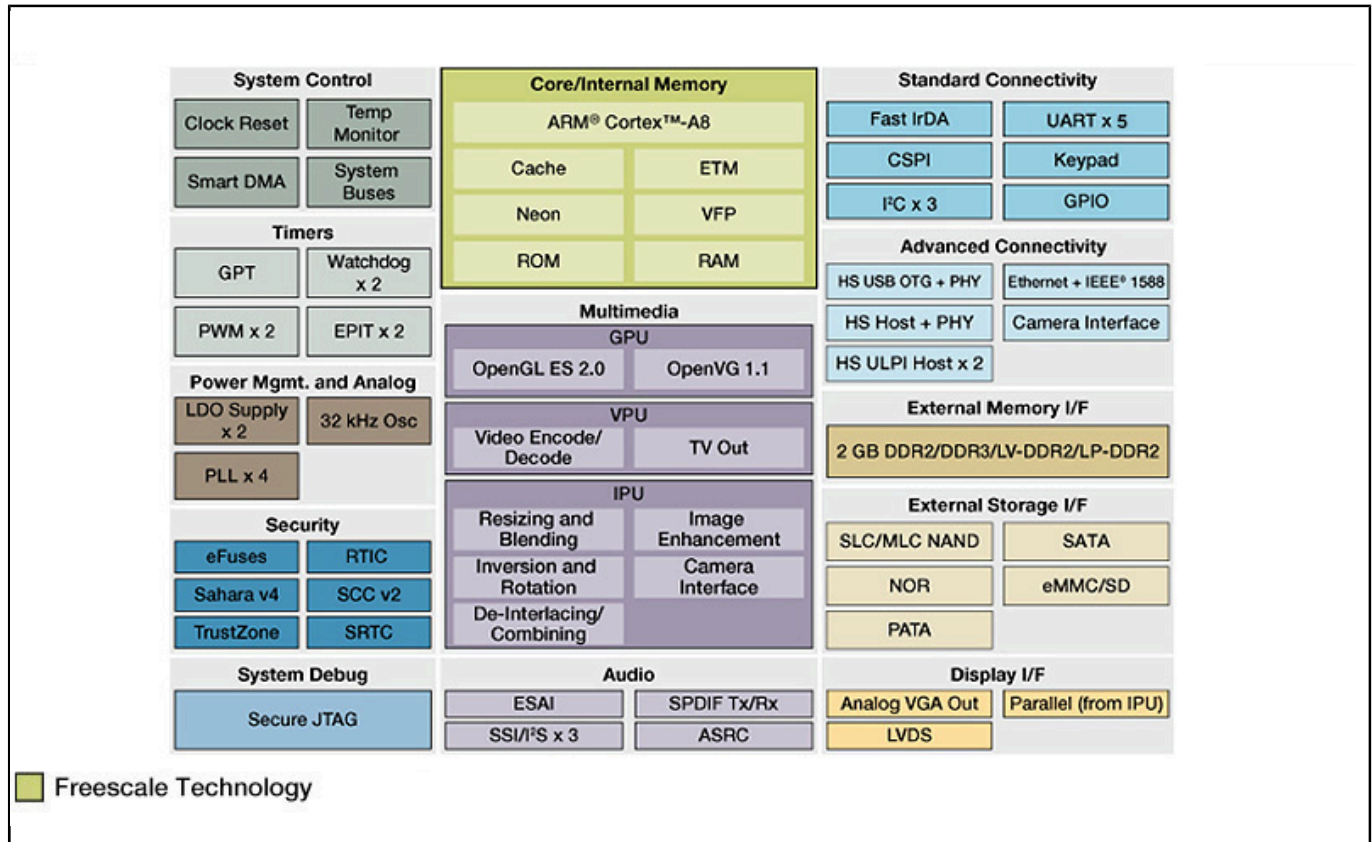
i.MX535

Last Updated: Dec 15, 2024

The i.MX53 family of processors represents our next generation of advanced multimedia and power-efficient implementation of the Arm® Cortex®-A8 core. With core processing speeds up to 1 GHz, the i.MX535 is optimized for both performance and power to meet the demands of high-end, advanced applications requiring mobility and a long battery life. 1080p HD video decode and 720p video encode, two dedicated graphics cores, multiple display and connectivity options, and high level of integration make the i.MX535 a choice platform for smart mobile devices.

The i.MX535 is supported by a companion power management IC (PMIC), [MC34709](#).

Freescale i.MX535 Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - HD Video, High-Performance, Low-Power, Arm® Cortex®-A8 Core](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.