



# Multimedia Applications Processors - HD Video, High-End, Advanced Applications, Arm® Cortex®-A8 Core

## i.MX537

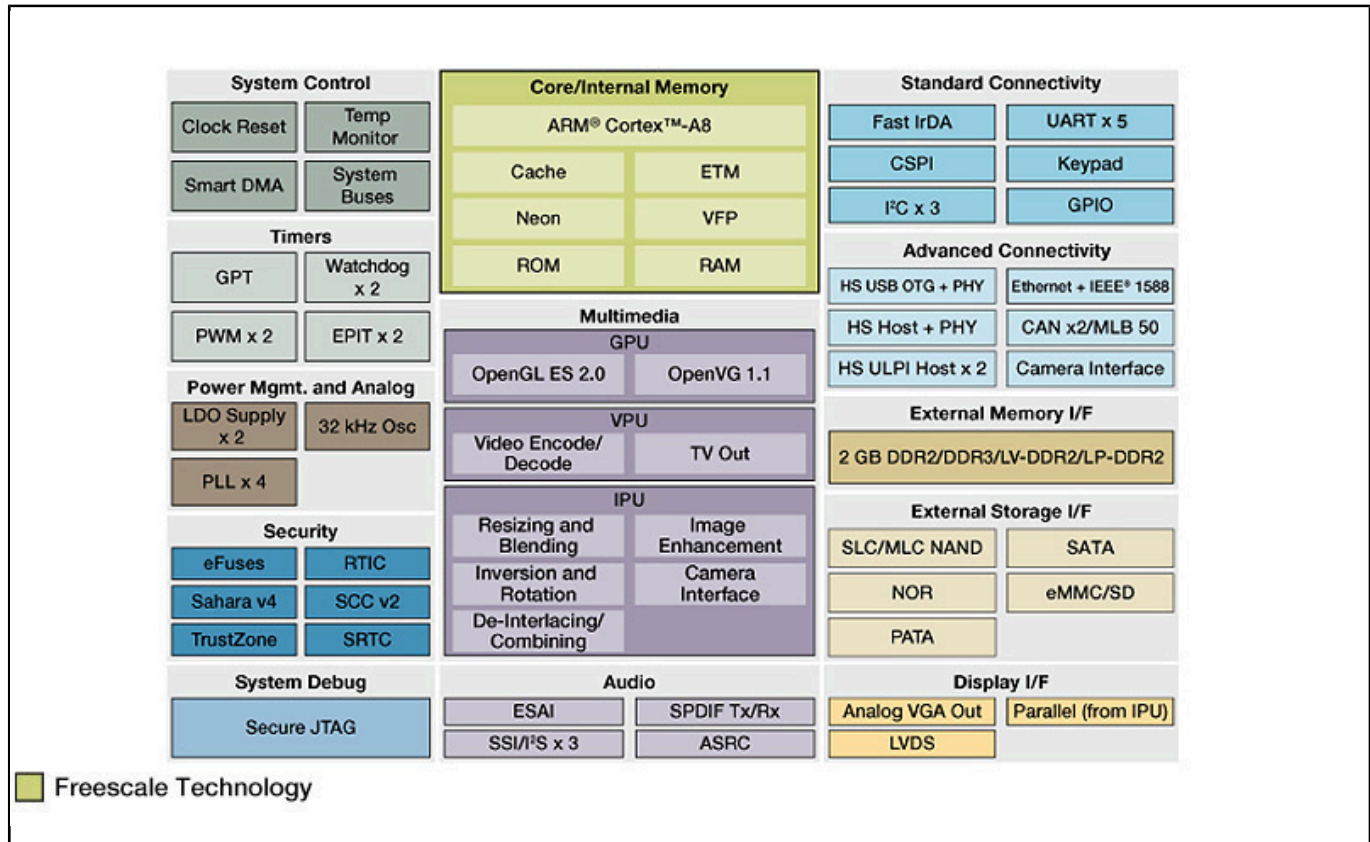
Last Updated: Jan 16, 2023

The i.MX53 family of processors represents NXP's next generation of advanced multimedia and power-efficient implementation of the Arm® Cortex®-A8 core. With core processing speeds up to 800 MHz, the i.MX537 is optimized for both performance and power to meet the demands of high-end, advanced applications. Integrated display controller, 1080p HD video decode and 720p video encode, enhanced graphics and connectivity features make the i.MX537 ideal for a wide range of applications such as Human Machine Interfaces (HMI) and patient monitors which require rich user interfaces with high color displays and user interaction.

The i.MX537 provides key environmental differentiators for the industrial market. These include 3.3V I/O support, a 0.8 mm pitch package to reduce PCB and manufacturing costs, extended temperature coverage for harsh environments, industrial qualification for extended reliability and a formal long product supply guarantee to support product life spans.

The i.MX537 is supported by a companion NXP power management IC (PMIC), [MC34709](#).

# i.MX537 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - HD Video, High-End, Advanced Applications, Arm® Cortex®-A8 Core](#).

Note: The information on this document is subject to change without notice.

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