



i.MX 94 Applications Processor — Safe and Secure Industrial and Automotive Connectivity with Real-Time Control

i.MX94

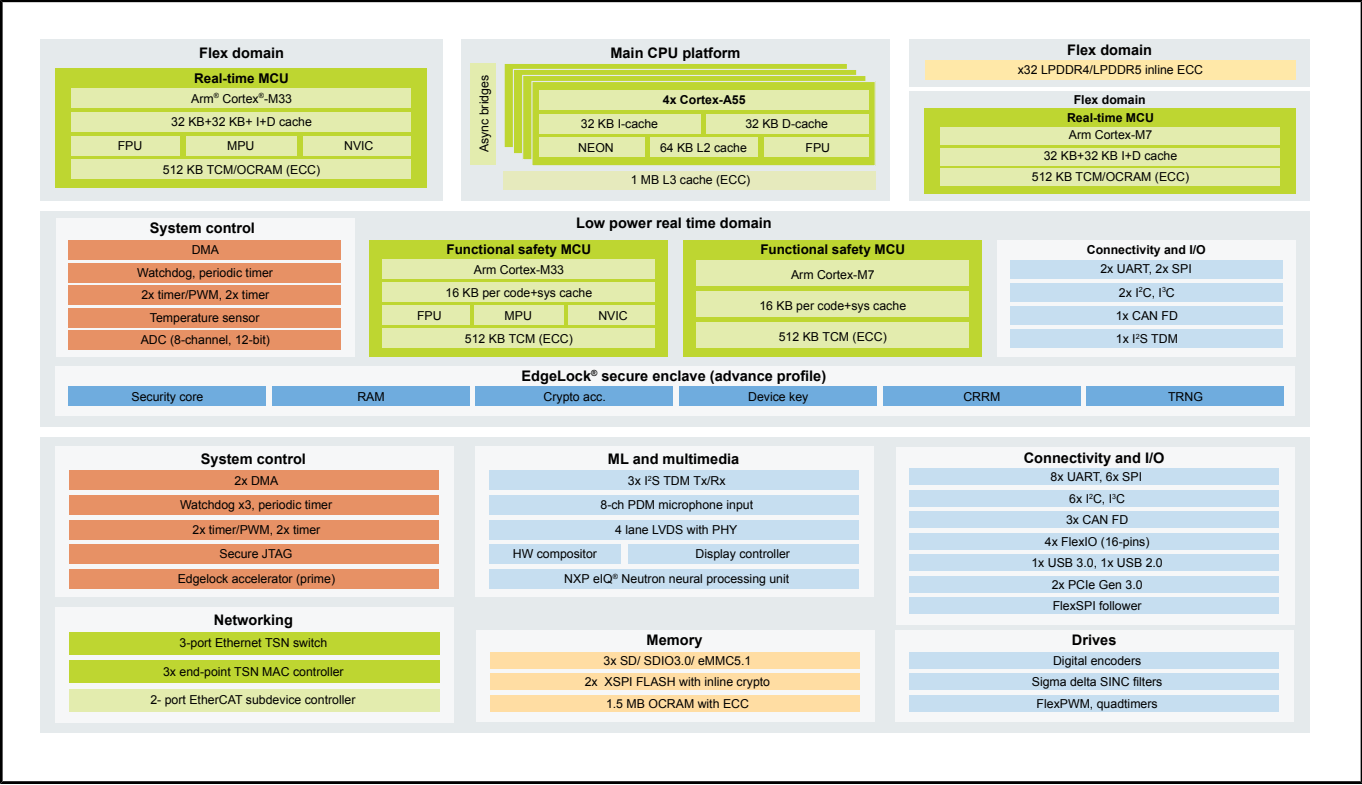
Preproduction

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information [contact support](#) or your sales representative.

Last Updated: May 30, 2025

The i.MX 94 family is an i.MX MPU with an integrated time-sensitive networking (TSN) switch, enabling configurable, secure communications with rich protocol support in industrial and automotive environments. This is also NXP's first chip supporting future-proof, postquantum cryptography. The multicore design of the i.MX 94 family delivers high-performance and low latency across both application and real-time domains, with an integrated functional safety island.

i.MX 94 Family Block Diagram Block Diagram



View additional information for [i.MX 94 Applications Processor — Safe and Secure Industrial and Automotive Connectivity with Real-Time Control](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.