i.MX 94 Applications Processor — Safe and Secure Industrial and Automotive Connectivity with Real-Time Control

i.MX94

Preproduction

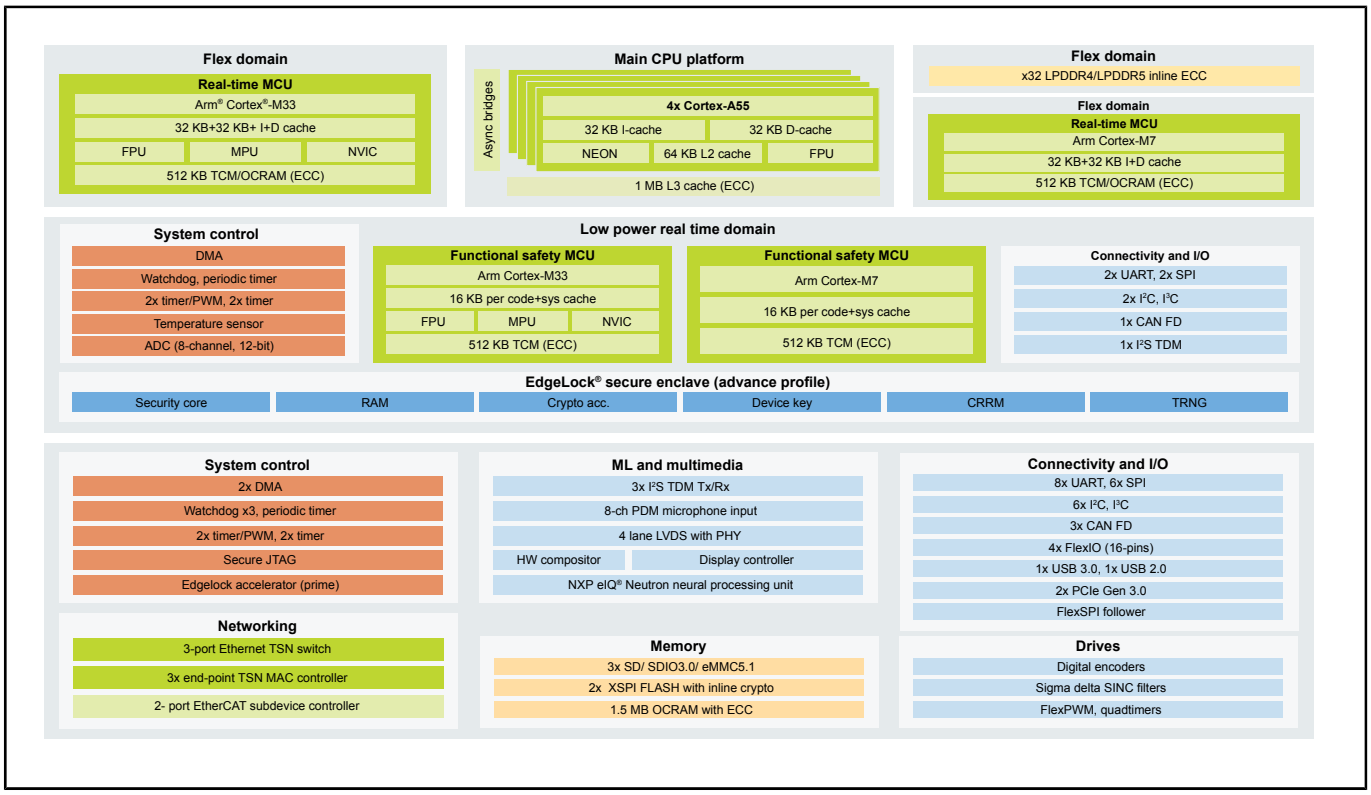
This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information please contact your sales representative.

Last Updated: Dec 22, 2025

The i.MX 94 family is an i.MX MPU with an integrated time-sensitive networking (TSN) switch, enabling configurable, secure communications with rich protocol support in industrial and automotive environments. This is also NXP's first chip supporting future-proof, postquantum cryptography. The multicore design of the i.MX 94 family delivers high-performance and low latency across both application and real-time domains, with an integrated functional safety island.

The EdgeLock® Secure Enclave (Advanced Profile) provides advanced, future-proof security capabilities, enabling a post-quantum hardware root-of-trust and supporting hybrid mode (ML-DSA, ECDSA) for NXP signed Secure Enclave Firmware. It also enables fast secure boot, secure debug and update, real-time message signing, authentication and encryption for secure communications, and more. Learn more about our post-quantum cryptography (PQC) strategy.

i.MX 94 Family Block Diagram Block Diagram



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