The i.MX 95 applications processor family enables a broad range of edge applications from Automotive connectivity domain and eCockpit, to Industry 4.0 and IoT platforms. The family offers NXP SafeAssure® functional safety compliant platform development (ASIL-B, SIL2), 6x Arm® Cortex-A55 cores, Arm Mali GPU, 4K VPU, ISP, ML acceleration NPU, and Edgelock® secure enclave security.

The i.MX 95 family energy flex architecture provides scalable real-time, safety and application domains with high-speed connectivity (10GbE, USB 3, PCIe® Gen 3) to enable a wide range of platforms.
i.MX 95 Applications Processor Block Diagram

View additional information for i.MX 95 Applications Processor Family: High-Performance, Safety Enabled Platform with eIQ® Neutron NPU.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.