

## Robust and Reliable 32-bit Microcontroller (MCU) based on Arm® Cortex®-M0 Core

## LPC12D27FBD100

Last Updated: Apr 5, 2024

The LPC12D27FBD100 is an Arm Cortex-M0 based microcontroller for embedded applications featuring a high level of integration and low power consumption. The Arm Cortex-M0 is a next generation core that offers system enhancements such as enhanced debug features and a higher level of support block integration. The LPC12D27FBD100 is a dual-chip module consisting of a LPC1227 single-chip microcontroller combined with a PCF8576D Universal LCD driver in a low-cost 100-pin package. The LCD driver provides 40 segments and supports from one to four backplanes. Display overhead is minimized by an on-chip display RAM with autoincrement addressing.





View additional information for Robust and Reliable 32-bit Microcontroller (MCU) based on Arm® Cortex®-M0 Core.

Note: The information on this document is subject to change without notice.

## www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.