32-bit MCU for Powertrain Applications
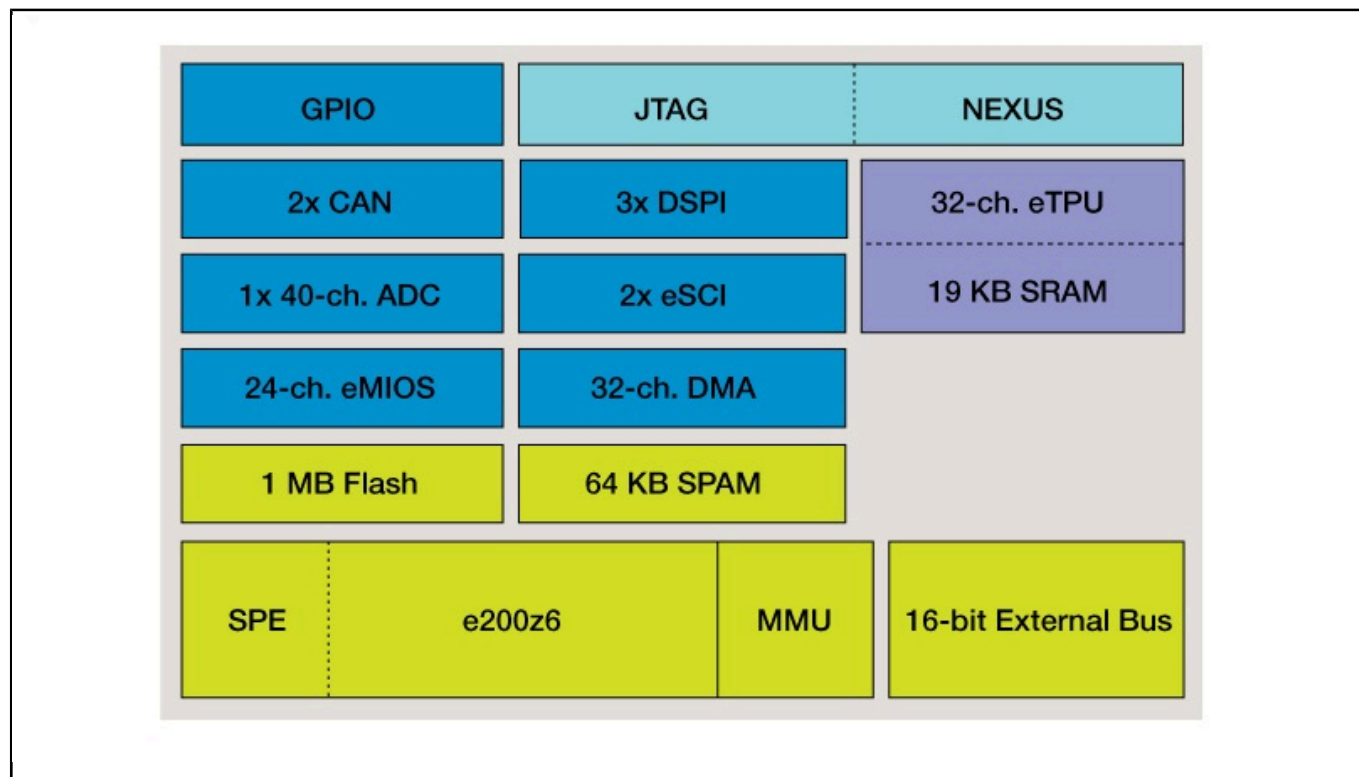
MPC5554

新規採用非推奨
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

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The MPC5554 MCU enables complex, real-time control for engine management systems. It offers system performance of up to five times that of its MPC500 predecessors along with the reliability and familiarity of proven Power Architecture® technology.

- Reduces costs by integrating more functionality on-chip
- Development support speeds time-to-market
- Supports multiple protocols and customer requirements through intelligent subsystems
- Offers a migration path from the MPC500 MCUs, facilitating reuse of legacy software architectures

MPC5554 Block Diagram Block Diagram

GPIO	JTAG	NEXUS
2x CAN	3x DSPI	32-ch. eTPU
1x 40-ch. ADC	2x eSCI	19 KB SRAM
24-ch. eMIOS	32-ch. DMA	
1 MB Flash	64 KB SPAM	
SPE e200z6	MMU	16-bit External Bus

View additional information for 32-bit MCU for Powertrain Applications.

Note: The information on this document is subject to change without notice.