

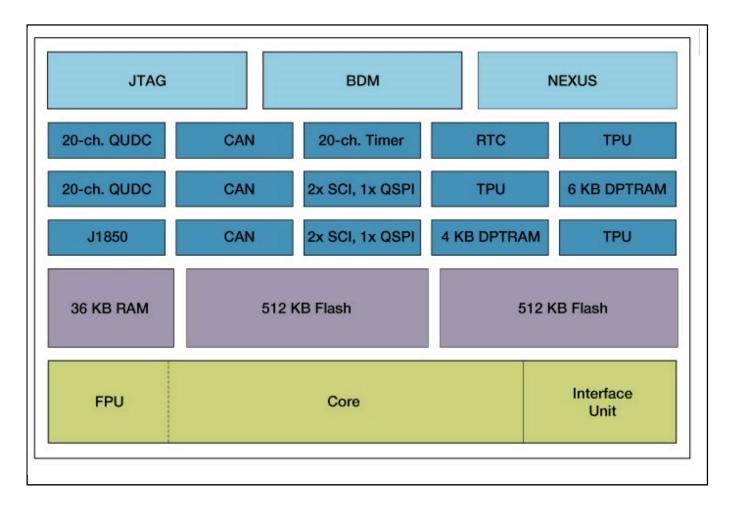
32 Bit Microcontroller

MPC565

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The MPC565 is a member of the MPC500 family of microprocessors that implements the Power Architecture® instruction standard architecture. The MPC565 is integrated with a floating point unit, an advanced peripheral set and 1 MB of flash memory on a single chip. This combination is ideal for high-performance automotive applications as well as other control-intensive applications.

MPC565 Block Diagram Block Diagram



View additional information for 32 Bit Microcontroller.

Note: The information on this document is subject to change without notice.

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