Integrated Host Processor

MPC8610

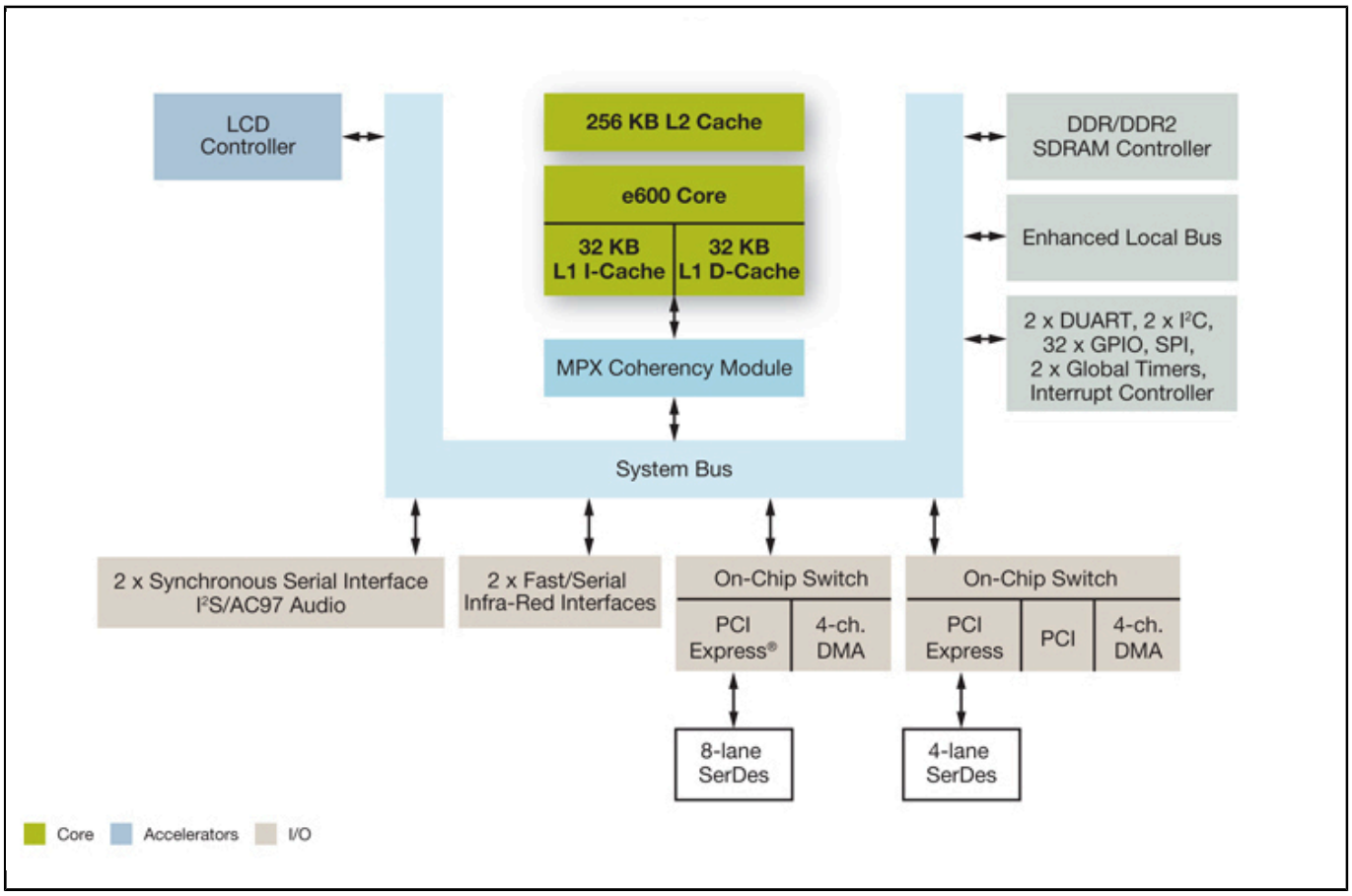
新規採用非推奨
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

Last Updated: Apr 9, 2022

The MPC8610 processor is engineered to deliver breakthrough performance, connectivity and integration for embedded applications that process or display graphical images, such as kiosks, robotics, in-vehicle infotainment, cockpit displays, single-board computers and multi-function printers and scanners.

The MPC8610’s strength is its integration—the high-performance e600 core built on Power Architecture® technology—combined with the PowerQUICC® system-on-chip (SoC) platform and an LCD controller. With e600 core performance and integrated northbridge and southbridge functionality, the single chip replaces what could take up to four chips using other solutions. Moving all core-to-peripheral connections inside the device greatly reduces the number of high-speed parallel busses to be routed on the circuit board. This translates into smaller boards with fewer layers and higher processing density.

Freescale MPC8610 Host Processor Block Diagram Block Diagram



View additional information for Integrated Host Processor.

Note: The information on this document is subject to change without notice.