

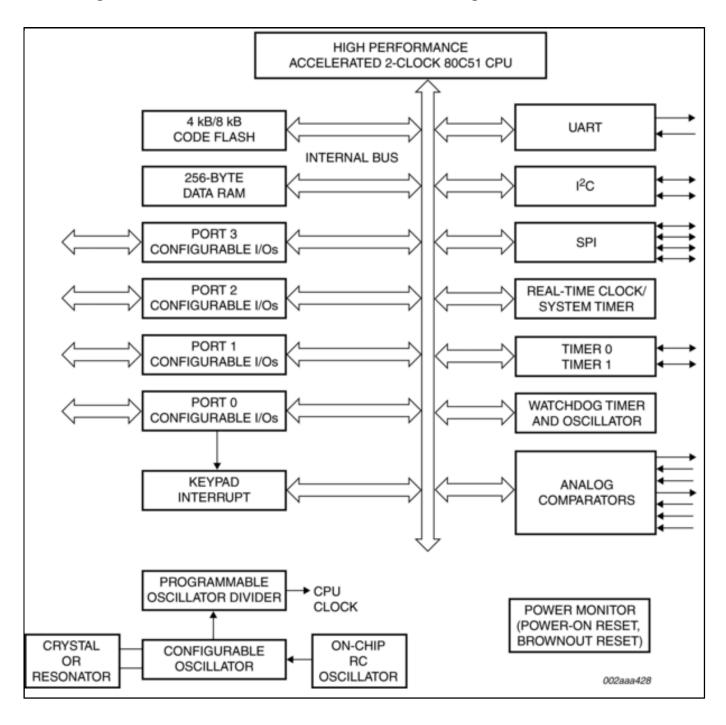
8-Bit Microcontrollers with Two Clock 80C51 Core 4 KB/8 KB 3 V Flash with 256 B Data RAM

P89LPC930FDH

Last Updated: Mar 8, 2023

The P89LPC930/931 are single-chip microcontrollers designed for applications demanding high-integration, low cost solutions over a wide range of performance requirements. The P89LPC930/931 is based on a high performance processor architecture that executes instructions in two to four clocks, six times the rate of standard 80C51 devices. Many system-level functions have been incorporated into the P89LPC930/931 in order to reduce component count, board space, and system cost.

Block diagram: P89LPC930FDH, P89LPC931FDH Block Diagram



View additional information for 8-Bit Microcontrollers with Two Clock 80C51 Core 4 KB/8 KB 3 V Flash with 256 B Data RAM.

Note: The information on this document is subject to change without notice.

www.nxp.comNXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.