AN11209 Set-Top Box LNAs BGU703X and BGU704X Rev. 2 – 20 March 2017

Application note

Document information

Info	Content
Keywords	Set-Top Box, STB, LNA, BGU703X, BGU704X
Abstract	This document provides circuit, layout, BOM, and performance information of Set-Top Box LNA BGU703X and BGU704X



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Set-Top Box LNAs BGU703X and BGU704X

Revision history

Rev	Date	Description
1	20121005	Initial document
2	20170320	Add stability improvement circuit

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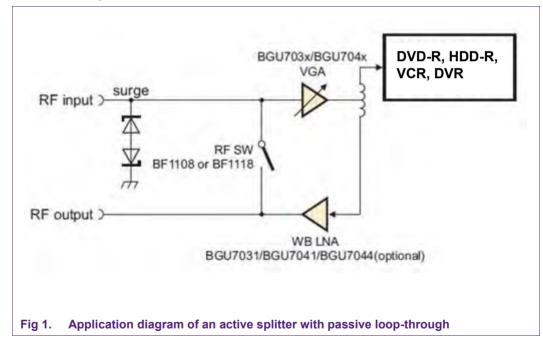
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1. Introduction

In Set-Top Boxes (STBs) that use multiple or network-interfaced module (NIM) tuners, the RF signal usually needs to be distributed or split. Very often, a low noise amplifier (LNA) is used to compensate for signal loss when the signal is split with a balun core. In addition to that, due to its low noise, this LNA is used to improve the sensitivity of the tuner.

This STB LNA family of 5V and 3.3V wideband, low noise amplifiers is specifically designed for high linearity, low-noise performance for TV, DVR/PVR, set-top box tuner applications from 40 MHz to 1 GHz. They are used in discrete or Si CAN tuners, as well as on board tuners. Fig 1 shows the application diagram of an active splitter with passive loop-through. It shows that at the moment the power of the recording device (DVD-R, HDD-R, VCR, DVR) is on, the RF switch is open, so the RF signal travels via the recording device to the TV tuner. At the moment the power of the recording device is completely off, the RF switch closes and this ensures that the RF signal is looped through directly to the TV tuner. Built in NXPs own QUBiC4+ Si BiCMOS process these low noise amplifiers provide programmable gain (-2dB, 5dB and 10dB), have integrated biasing, 75 Ω matching (saving up to 15 external components compared to discrete solutions). These low noise amplifiers are very ESD robust (>2kV HBM and >1.5kV CDM) compared to GaAs solutions. Table 1 gives an overview of this STB LNA family.

In this document, the application diagram, board layout, bill of materials, and performance information are given.



Tabl	le 1.	Overview	product	types

Table I. Overvie	w product types		
Type Number	Supply voltage [V]	Number of modes	Description
BGU7031	5.0	1	Fixed Gain 10dB
BGU7032	5.0	2	Gain 10dB

Type Number	Supply voltage [V]	Number of modes	Description
			Bypass mode
BGU7033	5.0	3	Gain 10dB Gain 5dB Bypass mode
BGU7041	3.3	1	Fixed Gain 10dB
BGU7042	3.3	2	Gain 10dB Bypass mode
BGU7044	3.3	1	Fixed Gain 14dB
BGU7045	3.3	2	Gain 14dB Bypass mode

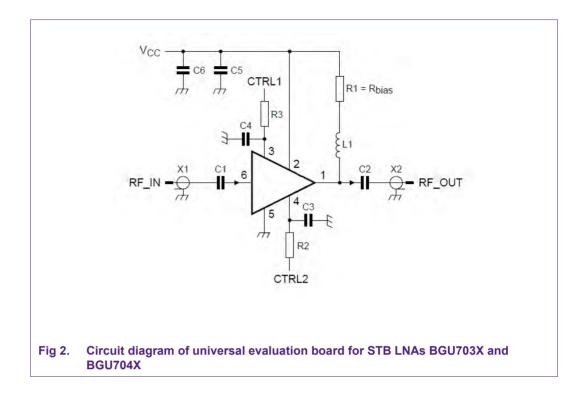
2. Application Circuit

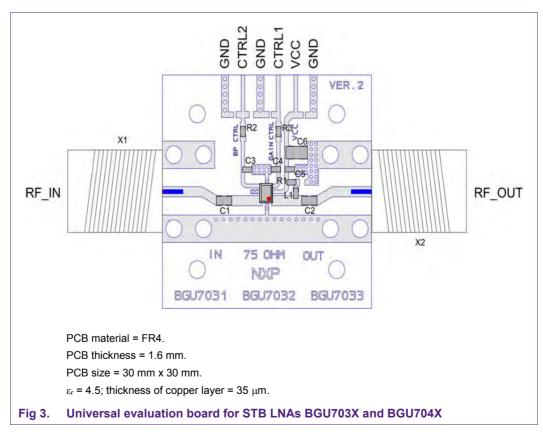
A universal evaluation board is used to test the RF performance of the whole NXP STB LNA family BGU703X and BGU704X. For all the types, it needs the same input and output DC block capacitors, supply decoupling capacitors, and RF choke. The difference between the types is mainly the external resistor used to set an optimum biasing current, and depending on how many modes the type has, the resistor and decoupling capacitor are used for each control line (bypass and gain control). The resistor for the control line is used to protect the control pin of the STB LNA MMIC by limiting the current.

The circuit diagram of the universal evaluation board and the board itself are shown in Fig 2 and Fig 3 respectively. Table 2, Table 3, Table 4, Table 5, Table 6, Table 7, and Table 8 show the bills of materials for BGU7031, BGU7032, BGU7033, BGU7041, BGU7042, BGU7044, and BGU7045 respectively.

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Table 2. Bill of materials BGU7031				
Component	Value	Туре	Remark	
C1	10 nF	C0805	DC blocking	
C2	10 nF	C0805	DC blocking	
C3	NC		Not connected	
C4	NC		Not connected	
C5	10 nF	C0603	RF decoupling	
C6	10 µF	C1206	RF decoupling	
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF	
R1	43 Ω	R0603	Bias setting	
R2	NC		Not connected	
R3	NC		Not connected	
X1	75 Ω	F-connector	input	
X2	75 Ω	F-connector	output	

Table 3.Bill of materials BGU7032

Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	10 nF	C0603	RF decoupling
C4	NC		Not connected
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF
R1	43 Ω	R0603	Bias setting
R2	1.8 KΩ	R0603	Current limiting
R3	NC		Not connected
X1	75 Ω	F-connector	input
X2	75 Ω	F-connector	output

Table 4. Bill of materials BGU7033

Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	10 nF	C0603	RF decoupling
C4	10 nF	C0603	RF decoupling
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF

Component	Value	Туре	Remark
R1	43 Ω	R0603	Bias setting
R2	1.8 KΩ	R0603	Current limiting
R3	1.8 KΩ	R0603	Current limiting
X1	75Ω	F-connector	input
X2	75 Ω	F-connector	output

Table 5. Bill of materials BGU7041

Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	NC		Not connected
C4	NC		Not connected
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF
R1	7.5 Ω	R0603	Bias setting
R2	NC		Not connected
R3	NC		Not connected
X1	75 Ω	F-connector	input
X2	75 Ω	F-connector	output

Table 6.Bill of materials BGU7042

Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	10 nF	C0603	RF decoupling
C4	NC		Not connected
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF
R1	7.5 Ω	R0603	Bias setting
R2	1.8 KΩ	R0603	Current limiting
R3	NC		Not connected
X1	75 Ω	F-connector	input
X2	75 Ω	F-connector	output

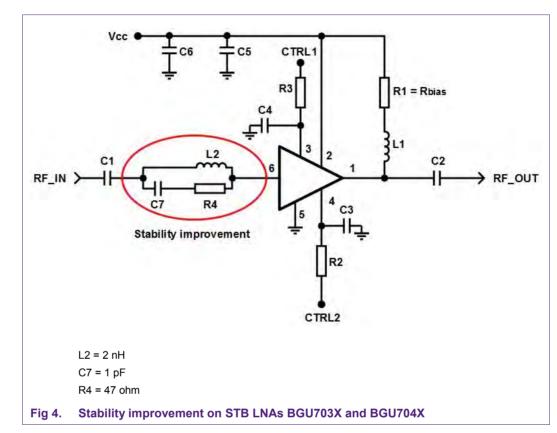
Table 7. Bill	of materia	als BGU7044	
Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	NC		Not connected
C4	NC		Not connected
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF
R1	18 Ω	R0603	Bias setting
R2	NC		Not connected
R3	NC		Not connected
X1	75 Ω	F-connector	input
X2	75 Ω	F-connector	output

Table 8.Bill of materials BGU7045

Component	Value	Туре	Remark
C1	10 nF	C0805	DC blocking
C2	10 nF	C0805	DC blocking
C3	10 nF	C0603	RF decoupling
C4	NC		Not connected
C5	10 nF	C0603	RF decoupling
C6	10 µF	C1206	RF decoupling
L1	1.5 KΩ	L0603	RF Choke: Chip ferrite bead BLM18HE152SN1DF
R1	18 Ω	R0603	Bias setting
R2	1.8 KΩ	R0603	Current limiting
R3	NC		Not connected
X1	75 Ω	F-connector	input
X2	75 Ω	F-connector	output

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3. Stability



In some capacitive load cases at RF input the BGU70xx LNA's tends to oscillate. To avoid oscillation additional components (see Fig 4.) should be placed at RF input.

The stability improvement circuit has no influence on the RF-parameter! Place the stability circuit closed to the LNA's input, keep distance to GND and remove the GND layers below the L2, C7 and R4 up to LNA input to avoid capacitive load at LNA's input.

4. RF Performance for Different Bias Currents including Default Current

Because there are trade-offs between bias current, linearity, and NF, in this chapter the RF performance of all STB LNA types is given for different bias currents, including the default current. The bias current is controlled by the bias resistor and Table 9 shows an overview of the resistor values for different bias currents in gain mode of different types.

Table 9. Overview resistor values for different bias currents in gain mode of different types

Туре	R _{bias} [Ω]								
	lcc≈35mA	lcc≈39mA	lcc≈43mA	lcc≈46mA					
BGU7031/2/3	N/A	N/A	43 (default)	39					
BGU7041/2	7.5 (default)	5.6	N/A	N/A					
BGU7044/5	18 (default)	N/A	10	N/A					

4.1 RF Test Setup

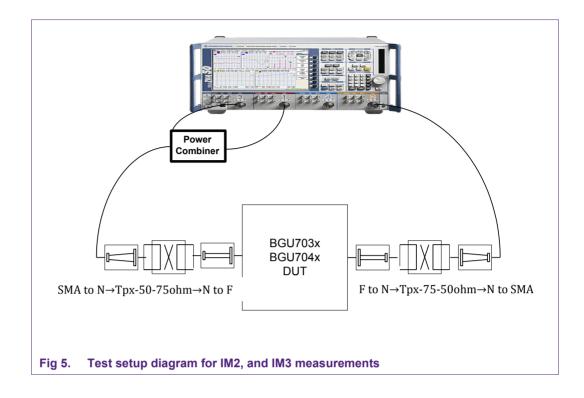
4.1.1 IM2, and IM3 measurement setup

Table 10. Equipment list for P1dB, IM2, and IM3 measurements

For the IM2, and IM3 measurements in this report, the equipment list in Table 10 has been used and Fig 5 shows the test setup diagram.

Manufacturer Type Instrument Rohde & Schwarz ZVA24 (1x) 4- Port Vector Network Analyzer 10MHz – 24GHz TTi QL355TP (2x) Dual DC Power Supply (1x) USB Powermeter Rohde & Schwarz NRP - Z21 (1x) Multimeter Keithley 2000 (1x) Power Combiner Agilent 11667B Macom TPX-75-4 (2x) Impedance Matching Transformer 75Ω/50Ω, Nconnectors 3x Cables from ZVA Rohde & Schwarz Test cables PC2.9/PC3.5 1x Cable to input Suhner Sucoflex104E, appr.50cm Suhner Sucoflex104PE, appr.20cm 1x Cable from output Additional connectors, cables Bomar, Suhner, Radiall n.a. and adapters as in drawing

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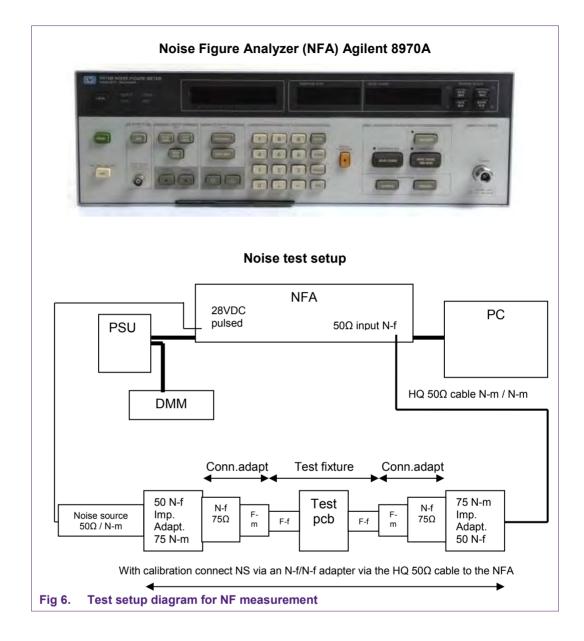
4.1.2 NF measurement setup

For the NF measurement in this report, the equipment list in Table 11 has been used and Fig 6 shows the test setup diagram.

Table 11. Equipment list for NF measurement

Description	Manufacturer	Number
Noise Figure Analyzer 10MHz – 1600MHz	Agilent	8970A
Noise source $15dB / N(m) / 50\Omega$	Agilent	346B
DC Power-supply	TTi	QL564P
Multimeter	Agilent	34401A
Impedance adapters 5.7dB Loss Pad (N-f) 50 Ω / (N-m) 75 Ω	Agilent	11852B
Connector adapters (N-f) 75Ω / (F-m) 75Ω	Bomar	

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4.2 2nd Order Intermodulation (IM2)

For IM2 measurement ZVA S-par. system calibration is not needed since it is a pure and relative power amplitude measurement. Thus only manual Power calibration is required. For this measurement, two tones are used separated by 200MHz or 6MHz, depending on the specification. Via a broadband power combiner and 50Ω to 75Ω impedance transformers the two tones with equal amplitude are fed into the DUT. The measurement has been done with f1=200MHz or f1=97.25MHz, depending on the specification, and an input power sweep from -20dBm to 5dBm per tone is applied. The pre-defined losses of the 50Ω to 75Ω impedance transformers etc. are compensated afterwards using output data processing. With Power calibration the reference plane is the SMA connector at the

 50Ω input cable just before the SMA to N adapter that is connected to the input transformer. For IM2, only f_1+f_2 product has been measured.

The IM2 measurement results for different bias currents of BGU703X (5.0V devices) and BGU704X (3.3V devices) are given in chapter 4.2.1 with f_1 =200MHz and tone spacing of 200MHz and chapter 4.2.2 with f_1 =97.25MHz and tone spacing of 6MHz.

4.2.1 IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; P_{in} per tone swept from - 20dBm to 5dBm

Table 12 shows an overview of IIP2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; and P_{in} =-15dBm per tone for BGU703x (5.0V devices) and BGU704x (3.3V devices) in different modes.

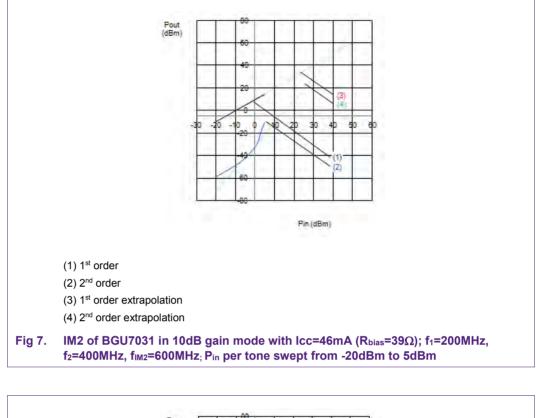
Table 12.Overview of IIP2 with f1=200MHz, f2=400MHz, f1M2=600MHz; and Pin =-15dBm per
tone for BGU703x and BGU704x in different modes

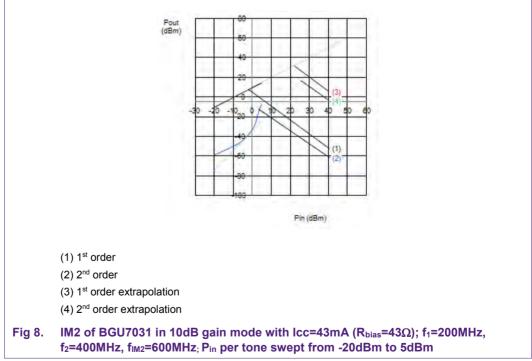
I	IIP2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz, Pin=-15dBm per tone														
Γ		Туре													
		IIP2	2	BGU7031 BGU7032		BGU7033			BGU7041	BGU7042		BGU7044	BGU7045		
				10dB Gain	10dB Gain	Bypass	10dB Gain	5dB Gain	Bypass	10dB Gain	10dB Gain	Bypass	14dB Gain	14dB Gain	Bypass
	ent	A]	35	N/A	N/A	N/A	N/A	N/A	N/A	2.95E+01	2.98E+01	3.45E+01	2.73E+01	2.68E+01	3.40E+01
	Ę -	<u> </u>	39	N/A	N/A	N/A	N/A	N/A	N/A	3.59E+01	3.93E+01	3.27E+01	N/A	N/A	N/A
	is c	ode ode	43	3.47E+01	3.10E+01	3.35E+01	3.84E+01	3.42E+01	3.46E+01	N/A	N/A	N/A	2.93E+01	3.00E+01	3.47E+01
1	olas in	Ē	46	3.35E+01	3.32E+01	3.41E+01	3.29E+01	3.23E+01	3.26E+01	N/A	N/A	N/A	N/A	N/A	N/A

4.2.1.1 BGU7031: IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz, P_{in} per tone swept from - 20dBm to 5dBm

Fig 7 to Fig 8 show 1st and 2nd order response of BGU7031 in 10dB gain mode with $f_1=200$ MHz, $f_2=400$ MHz, $f_{1M2}=600$ MHz; and P_{in} per tone swept from -20dBm to 5dBm.

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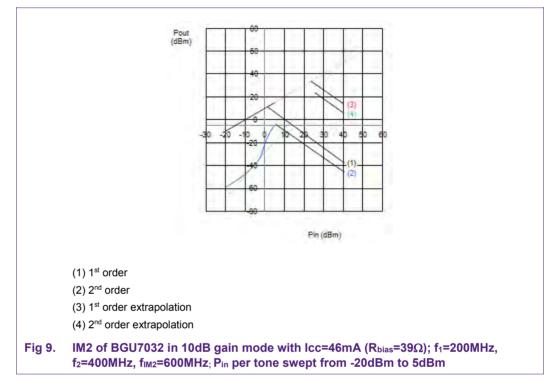




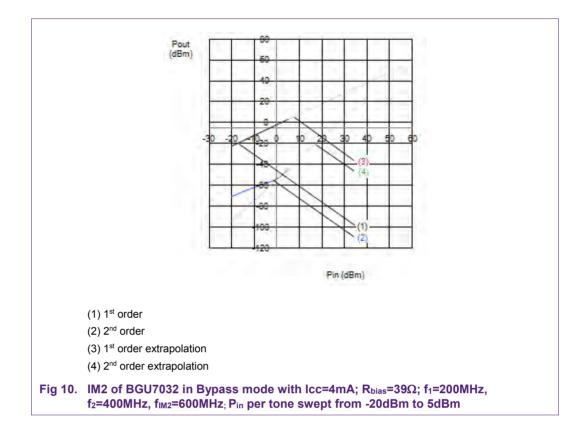
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4.2.1.2 BGU7032: IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; P_{in} per tone swept from - 20dBm to 5dBm

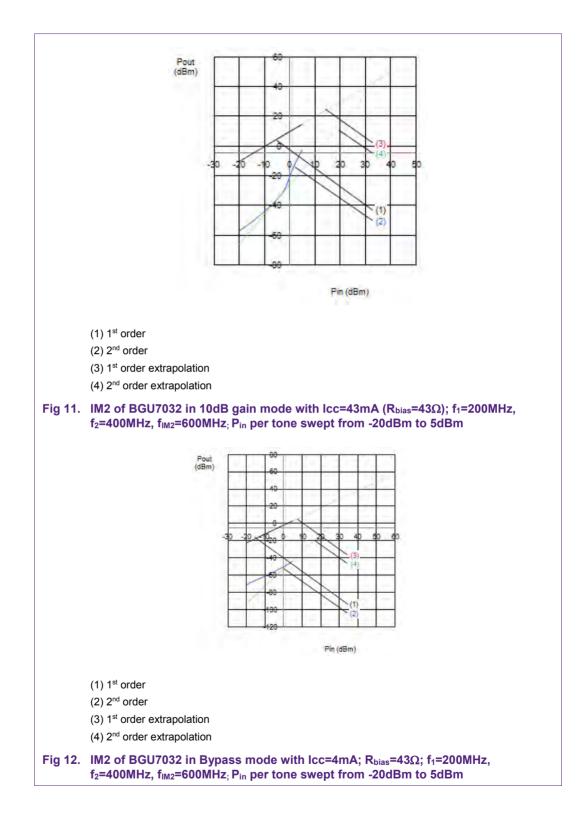
Fig 9 to Fig 12 show 1st and 2nd order response of BGU7032 in 10dB gain and bypass modes with f_1 =200MHz, f_2 =400MHz, f_{1M2} =600MHz; and P_{in} per tone swept from -20dBm to 5dBm.



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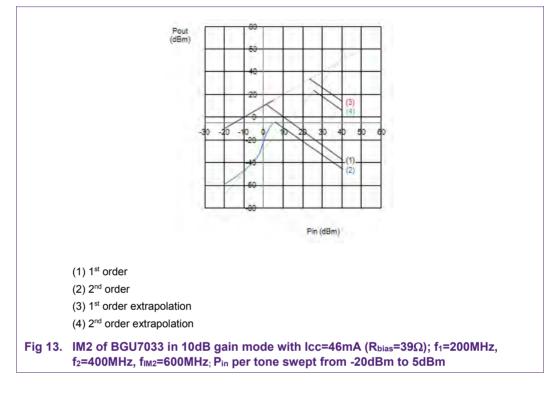


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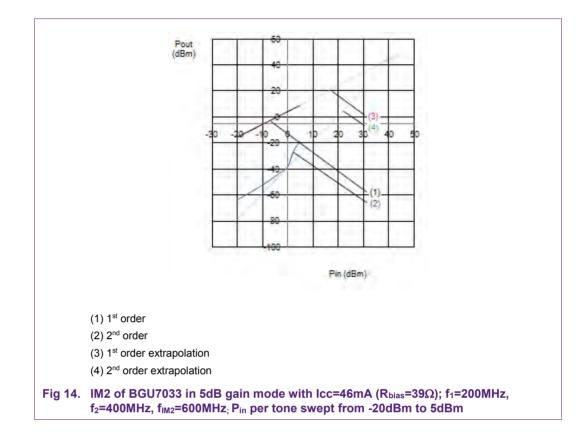


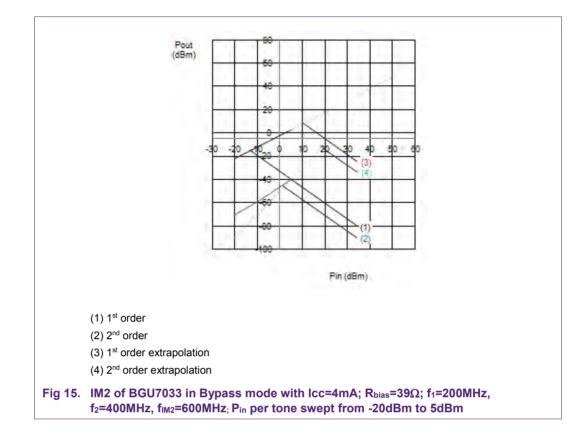
4.2.1.3 BGU7033: IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; P_{in} per tone swept from - 20dBm to 5dBm

Fig 13 to Fig 18 show 1st and 2nd order response of BGU7033 in 10dB gain, 5dB gain, and bypass modes with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; and P_{in} per tone swept from -20dBm to 5dBm.

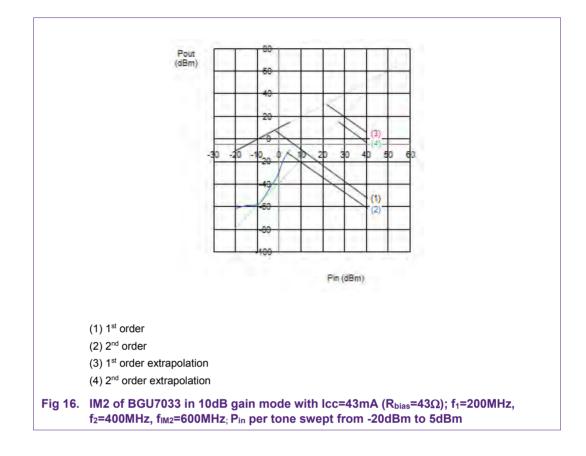


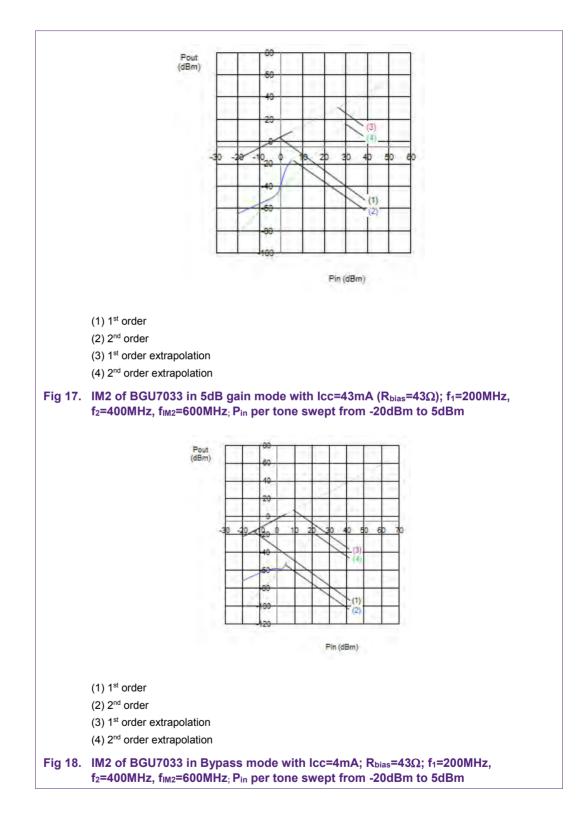
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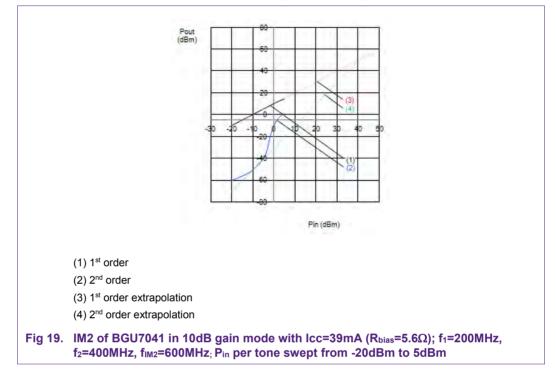
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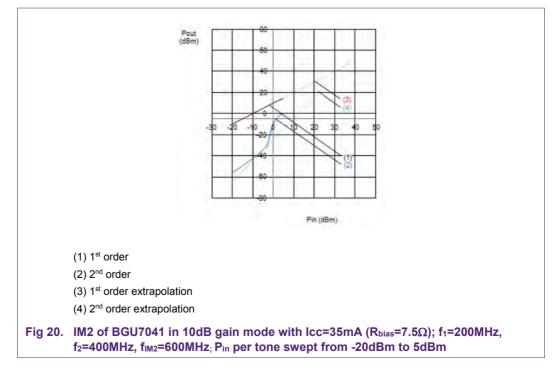




4.2.1.4 BGU7041: IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; P_{in} per tone swept from - 20dBm to 5dBm

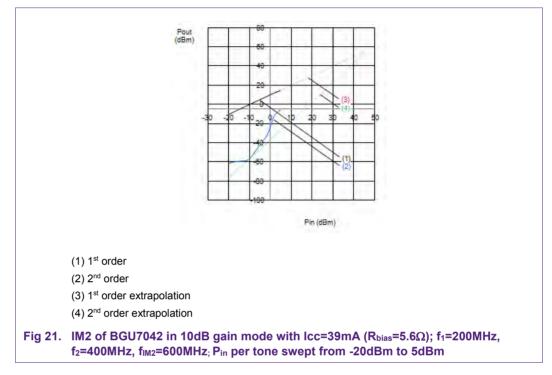
Fig 19 to Fig 20 show 1st and 2nd order response of BGU7041 in 10dB gain mode with $f_1=200$ MHz, $f_2=400$ MHz, $f_{IM2}=600$ MHz; and P_{in} per tone swept from -20dBm to 5dBm.



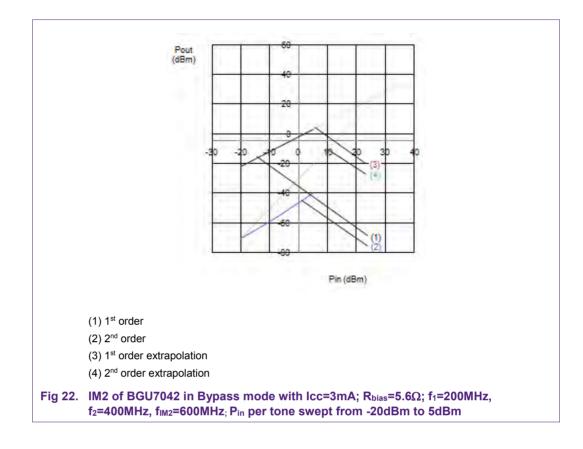


4.2.1.5 BGU7042: IM2 with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; P_{in} per tone swept from - 20dBm to 5dBm

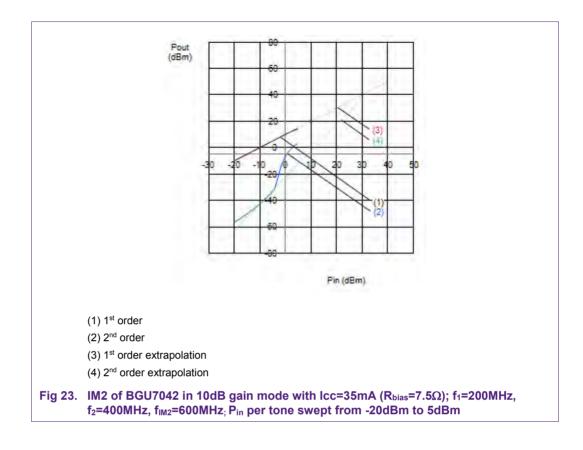
Fig 21 to Fig 24 show 1st and 2nd order response of BGU7042 in 10dB gain and bypass modes with f_1 =200MHz, f_2 =400MHz, f_{1M2} =600MHz; and P_{in} per tone swept from -20dBm to 5dBm.

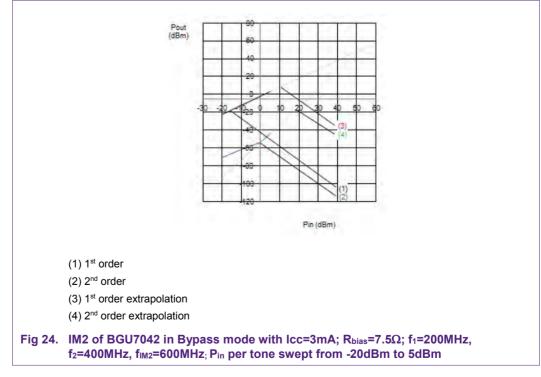


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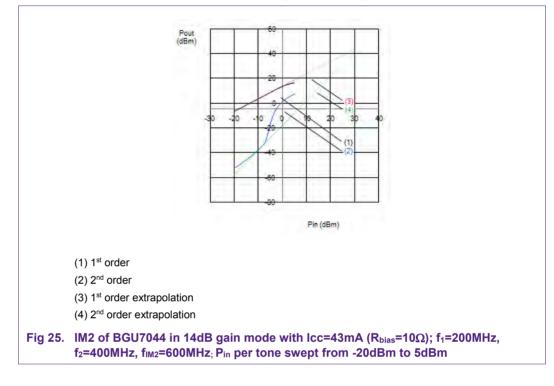




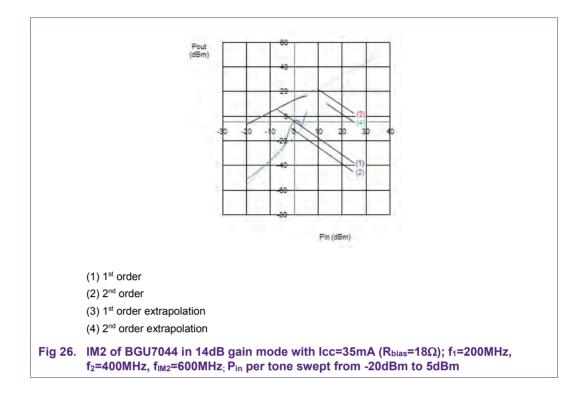
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4.2.1.6 BGU7044: IM2 with f₁=200MHz, f₂=400MHz, f_{IM2}=600MHz; P_{in} per tone swept from - 20dBm to 5dBm

Fig 25 to Fig 26 show 1st and 2nd order response of BGU7044 in 14dB gain mode with $f_1=200$ MHz, $f_2=400$ MHz, $f_{IM2}=600$ MHz; and P_{in} per tone swept from -20dBm to 5dBm.



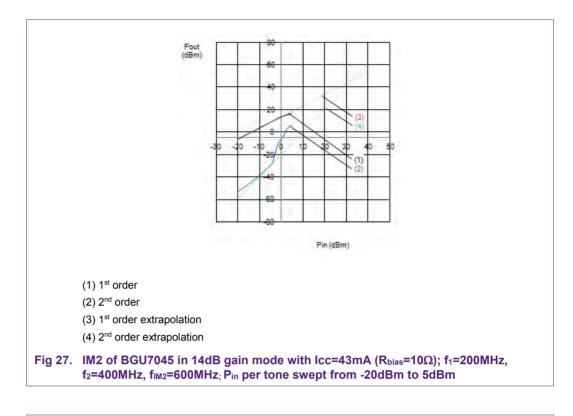
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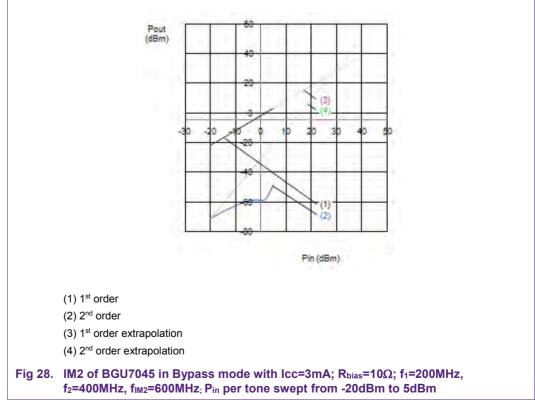


4.2.1.7 BGU7045: IM2 with f₁=200MHz, f₂=400MHz, f_{IM2}=600MHz; P_{in} per tone swept from - 20dBm to 5dBm

Fig 27 to Fig 30 show 1st and 2nd order response of BGU7044 in 14dB gain and bypass modes with f_1 =200MHz, f_2 =400MHz, f_{IM2} =600MHz; and P_{in} per tone swept from -20dBm to 5dBm.

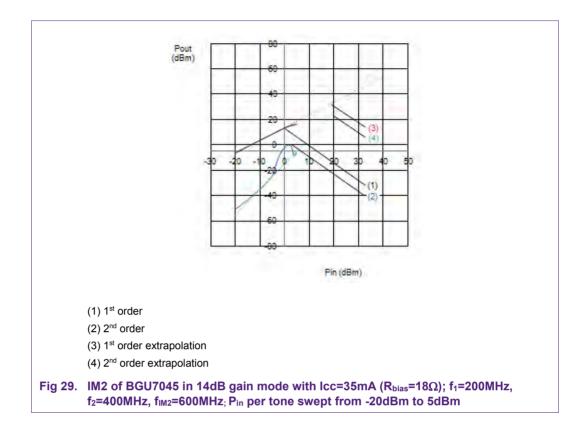
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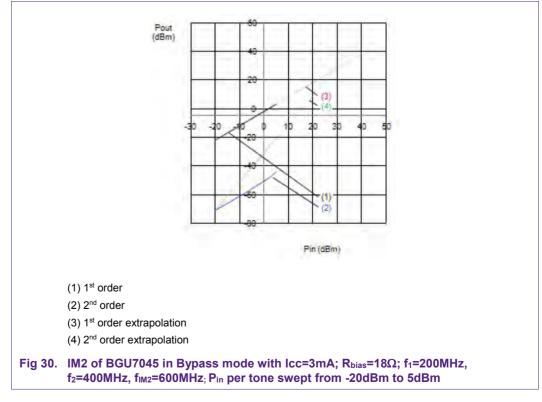




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4.2.2 IM2 with f_1 =97.25MHz, f_2 =103.25MHz, f_{IM2} =200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Table 13 shows an overview of IIP2 with $f_1=97.25$ MHz, $f_2=103.25$ MHz, $f_{IM2}=200.50$ MHz; and $P_{in} = -20$ dBm per tone for BGU703x (5.0V devices) and BGU704x (3.3V devices) in different modes.

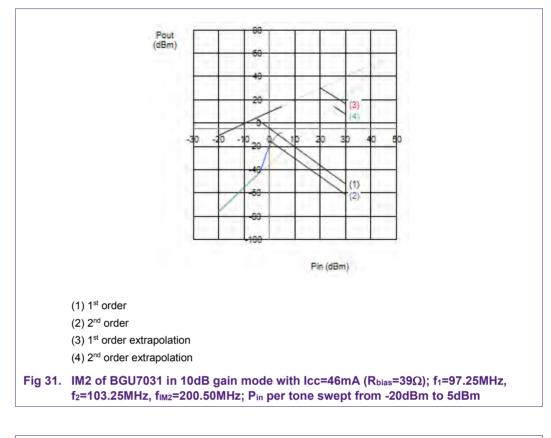
Table 13.Overview of IIP2 with f1=97.25MHz, f2=103.25MHz, f1M2=200.50MHz; and Pin = -20dBm per tone for BGU703x and BGU704x in different modes

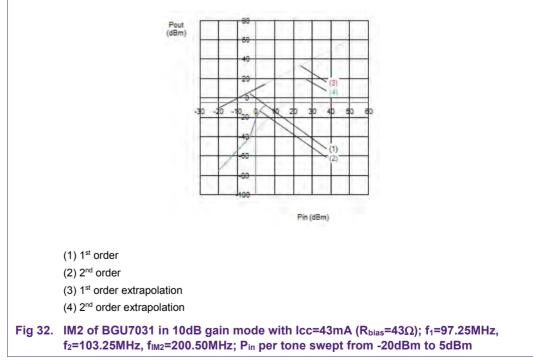
Ш	IIP2 with f_1 =97.25MHz, f_2 =103.25MHz, f_{IM2} =200.50MHz, Pin=-20dBm per tone														
	Туре														
IIP2 BGU7031 BGU7032 BGU7033 BGU7041 B							BGU	17042 BGU7044		BGU	7045				
				10dB Gain	10dB Gain	Bypass	10dB Gain	5dB Gain	Bypass	10dB Gain	10dB Gain	Bypass	14dB Gain	14dB Gain	Bypass
ť		A]	35	N/A	N/A	N/A	N/A	N/A	N/A	3.48E+01	3.35E+01	4.47E+01	2.92E+01	2.91E+01	4.56E+01
curre	ain	<u></u>	39	N/A	N/A	N/A	N/A	N/A	N/A	3.54E+01	3.43E+01	4.89E+01	N/A	N/A	N/A
		ode	43	4.44E+01	3.53E+01	4.54E+01	3.51E+01	4.51E+01	4.86E+01	N/A	N/A	N/A	3.24E+01	3.24E+01	4.55E+01
bias		É	46	4.52E+01	3.58E+01	4.77E+01	3.65E+01	4.53E+01	4.83E+01	N/A	N/A	N/A	N/A	N/A	N/A

4.2.2.1 BGU7031: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Fig 31 to Fig 32 show 1st and 2nd order response of BGU7031 in 10dB gain mode with $f_1=97.25$ MHz, $f_2=103.25$ MHz, $f_{IM2}=200.50$ MHz; P_{in} per tone swept from -20dBm to 5dBm.

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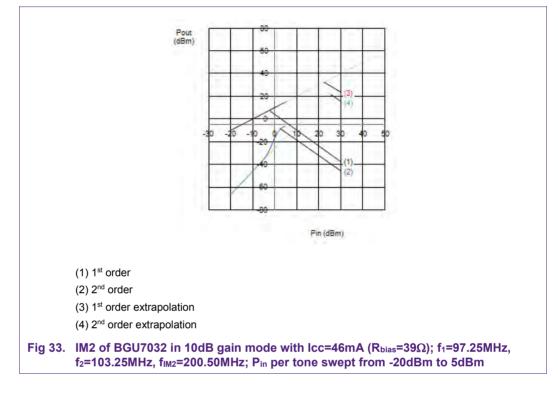


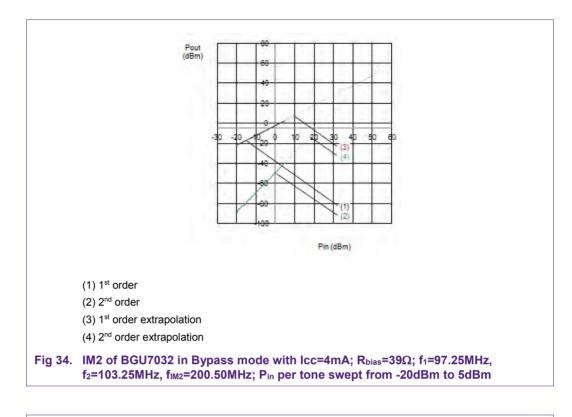


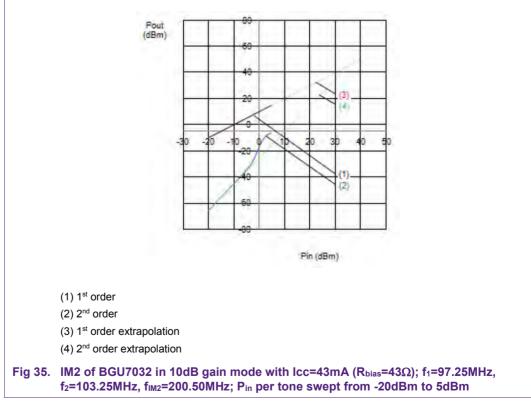
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4.2.2.2 BGU7032: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

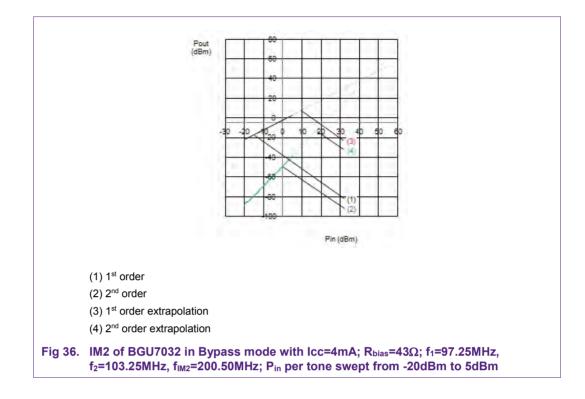
Fig 33 to Fig 36 show 1^{st} and 2^{nd} order response of BGU7032 in 10dB gain and bypass modes with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from - 20dBm to 5dBm.







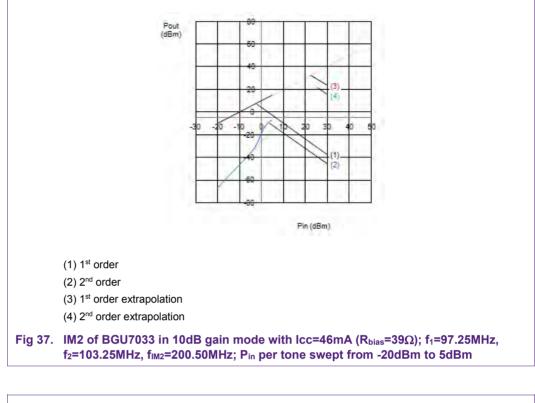
AN11209

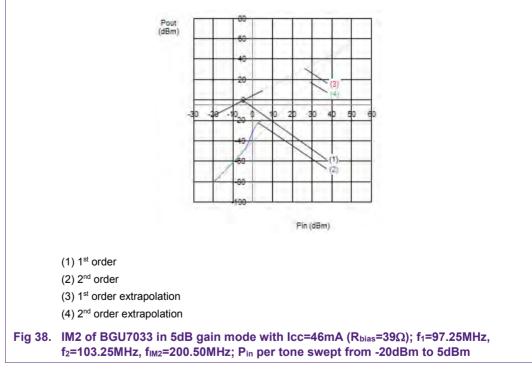


4.2.2.3 BGU7033: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Fig 37 to Fig 42 show 1st and 2nd order response of BGU7033 in 10dB gain, 5dB gain, and bypass modes with f_1 =97.25MHz, f_2 =103.25MHz, f_{IM2} =200.50MHz; P_{in} per tone swept from -20dBm to 5dBm.

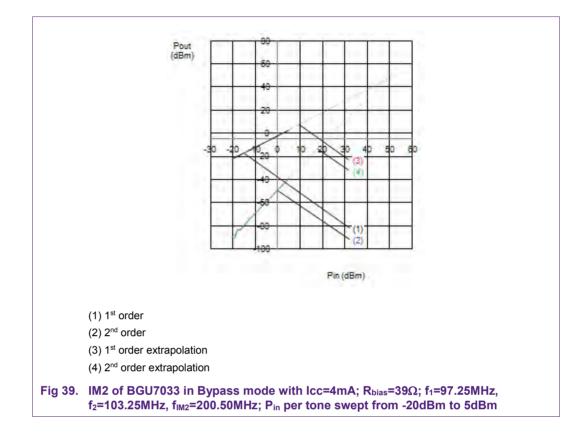
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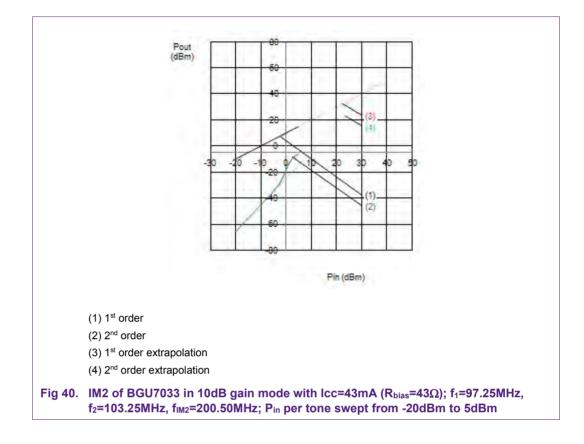


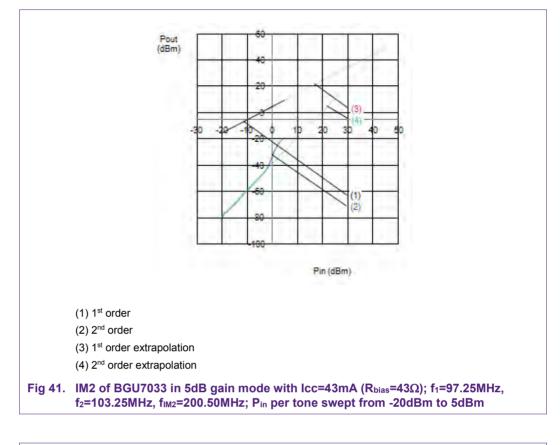
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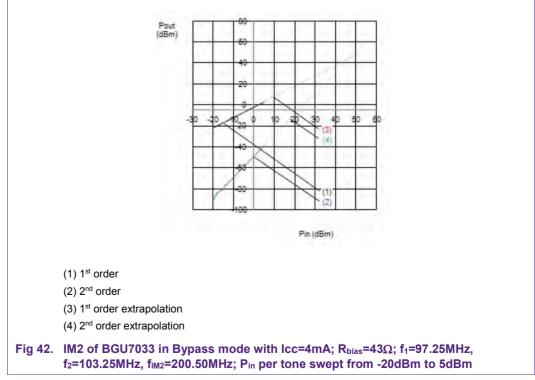
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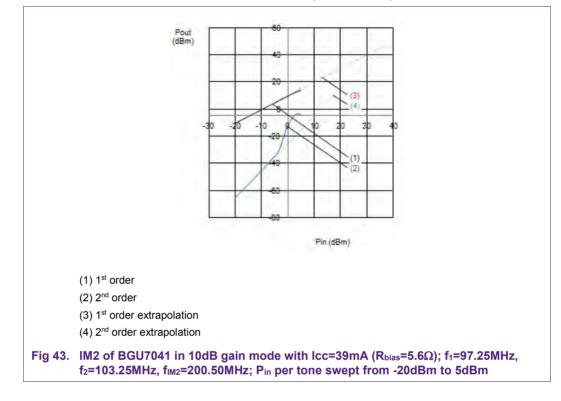




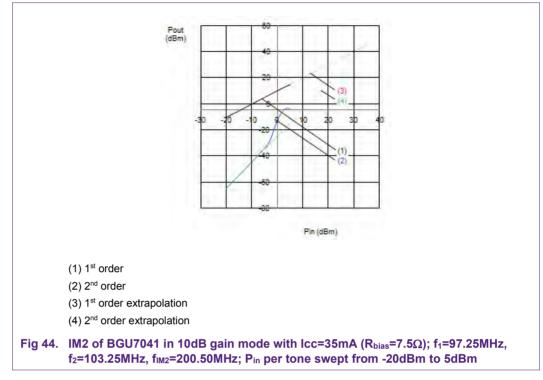


4.2.2.4 BGU7041: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Fig 43 to Fig 44 show 1st and 2nd order response of BGU7041 in 10dB gain mode with $f_1=97.25$ MHz, $f_2=103.25$ MHz, $f_{IM2}=200.50$ MHz; P_{in} per tone swept from -20dBm to 5dBm.



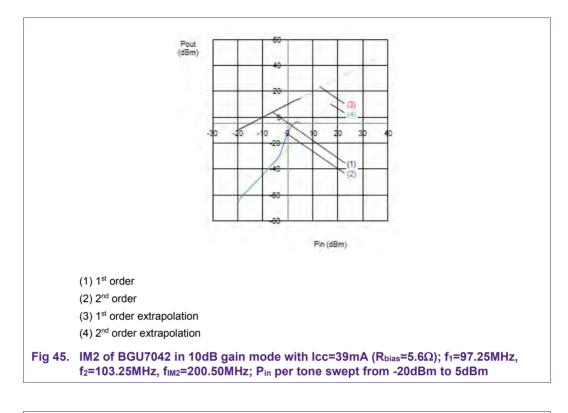
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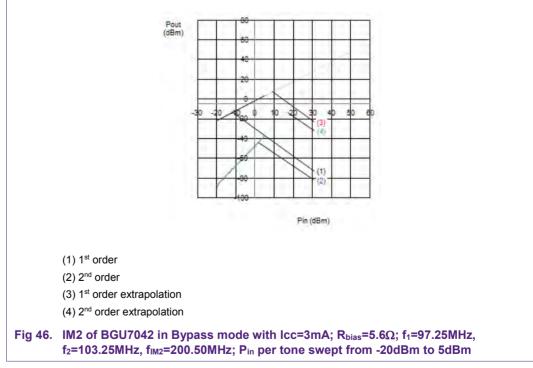


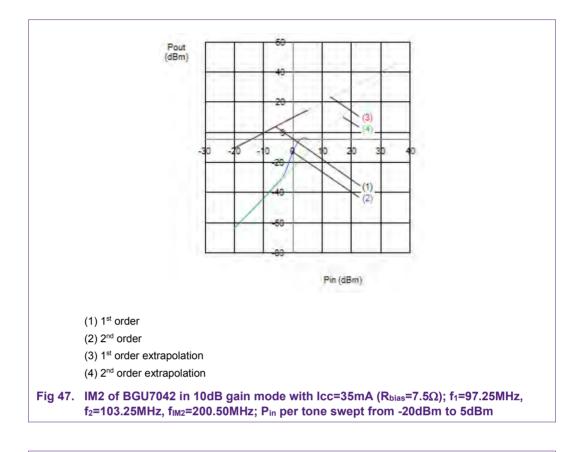
4.2.2.5 BGU7042: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

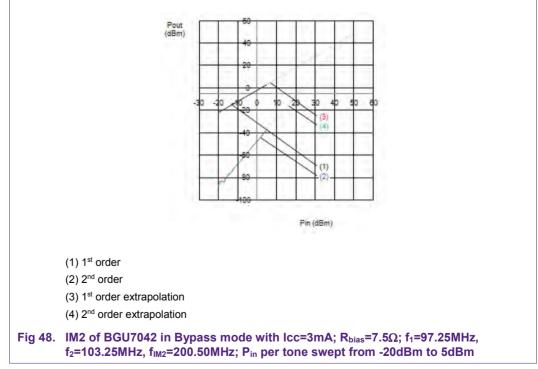
Fig 45 to Fig 48 show 1^{st} and 2^{nd} order response of BGU7042 in 10dB gain and bypass modes with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from - 20dBm to 5dBm.

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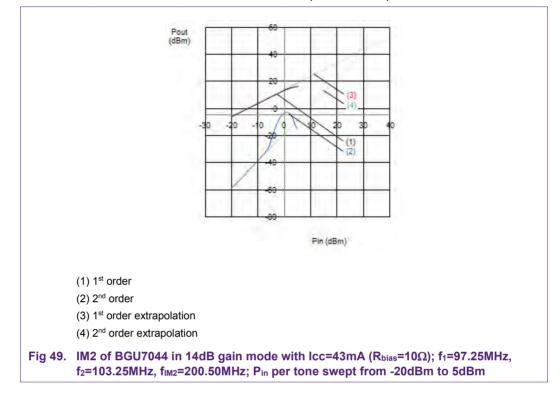




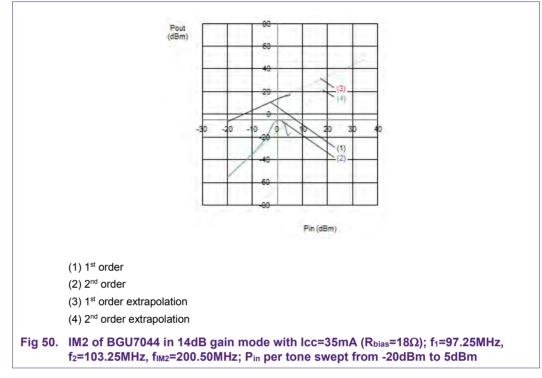


4.2.2.6 BGU7044: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Fig 49 to Fig 50 show 1st and 2nd order response of BGU7044 in 14dB gain mode with f_1 =97.25MHz, f_2 =103.25MHz, f_{IM2} =200.50MHz; P_{in} per tone swept from -20dBm to 5dBm.



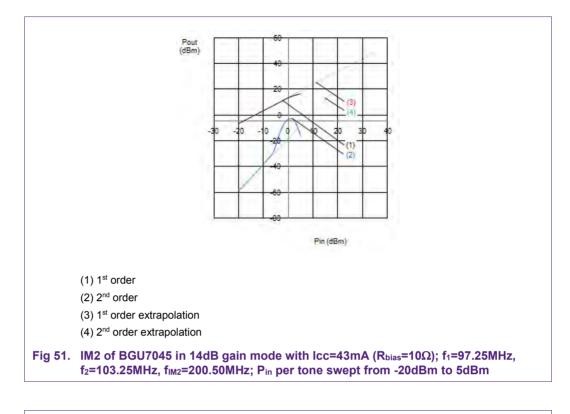
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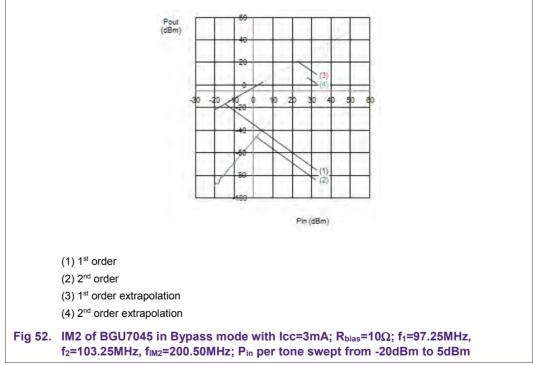


4.2.2.7 BGU7045: IM2 with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from -20dBm to 5dBm

Fig 51 to Fig 54 show 1^{st} and 2^{nd} order response of BGU7045 in 14dB gain and bypass modes with f₁=97.25MHz, f₂=103.25MHz, f_{IM2}=200.50MHz; P_{in} per tone swept from - 20dBm to 5dBm.

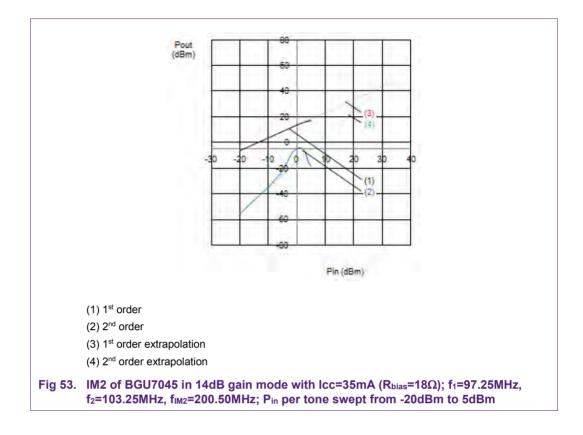
AN11209



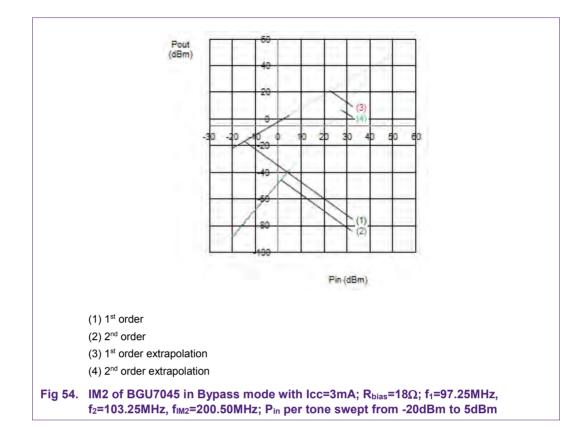


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4.3 3rd Order Intermodulation (IM3)

For IM3 measurement ZVA S-par. system calibration is not needed since it is a pure and relative power amplitude measurement. Thus only manual Power calibration is required. For this measurement, two tones are used separated by 1MHz or 10MHz, depending on the specification. Via a broadband power combiner and 50Ω to 75Ω impedance transformers the two tones with equal amplitude are fed into the DUT. The measurement has been done with f₁=1000MHz or f₁=900MHz, depending on the specification, and an input power sweep from -20dBm to 5dBm per tone is applied. The pre-defined losses of the 50Ω to 75Ω impedance transformers etc. are compensated afterwards using output data processing. With Power calibration the reference plane is the SMA connector at the 50Ω input cable just before the SMA to N adapter that is connected to the input transformer. Both IM3 products will be measured at the frequencies $2xf_1-f_2$ and $2xf_2-f_1$ Because both frequencies give similar results at these settings only frequency $2xf_2-f_1$ is used.

The IM3 measurement results for different bias currents of BGU703X (5.0V devices) and BGU704X (3.3V devices) are given in chapter 4.3.1 with f_1 =1000MHz and tone spacing of 1MHz and chapter 4.3.2 with f_1 =900MHz and tone spacing of 10MHz.

4.3.1 IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

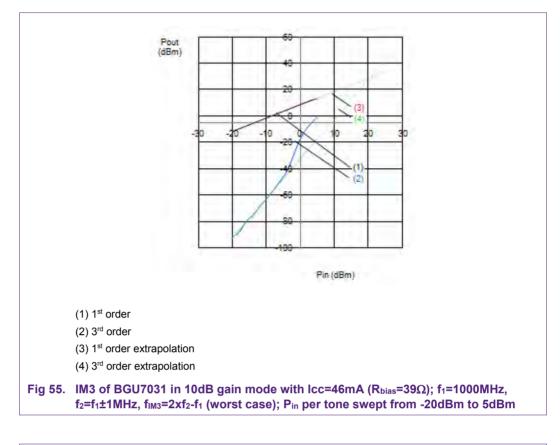
Table 14 shows an overview of IIP3 with f_1 =1000MHz, f_2 =1001MHz, f_{IM3} =1002MHz; P_{in} = -10dBm per tone for BGU703x (5.0V devices) and BGU704x (3.3V devices) in different modes.

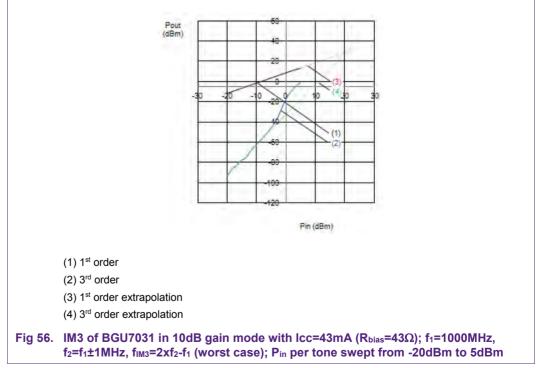
Table 14. Overview of IIP3 with f_1 =1000MHz, f_2 =1001MHz, f_{IM3} =1002MHz; P_{in} = -10dBm per tone for BGU703x and BGU704x in different modes

II	IIP3 with f_1 =1000MHz, f_2 =1001MHz, f_{IM3} =1002MHz, Pin=-10dBm per tone															
Г				Туре												
	1	IIP3		BGU7031	BGU	7032		BGU7033		BGU7041	BGU7042		BGU7044	BGU7045		
				10dB Gain	10dB Gain	Bypass	10dB Gain	5dB Gain	Bypass	10dB Gain	10dB Gain	Bypass	14dB Gain	14dB Gain	Bypass	
bias current	in gain	mode [mA]	35	N/A	N/A	N/A	N/A	N/A	N/A	2.03E+01	1.98E+01	3.06E+01	1.54E+01	1.55E+01	3.01E+01	
			39	N/A	N/A	N/A	N/A	N/A	N/A	2.01E+01	2.01E+01	3.01E+01	N/A	N/A	N/A	
			43	2.05E+01	2.08E+01	4.02E+01	2.14E+01	2.41E+01	3.05E+01	N/A	N/A	N/A	1.76E+01	1.76E+01	2.98E+01	
			46	2.08E+01	2.08E+01	2.95E+01	2.13E+01	2.33E+01	3.70E+01	N/A	N/A	N/A	N/A	N/A	N/A	

4.3.1.1 BGU7031: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

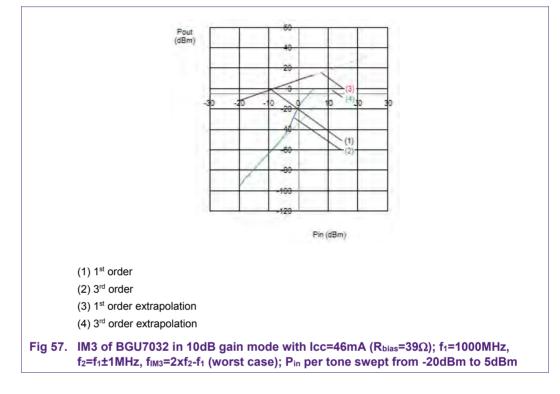
Fig 55 to Fig 56 show 1st and 3rd order response of BGU7031 in 10dB gain mode with f_1 =1000MHz, f_2 = f_1 ±1MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.



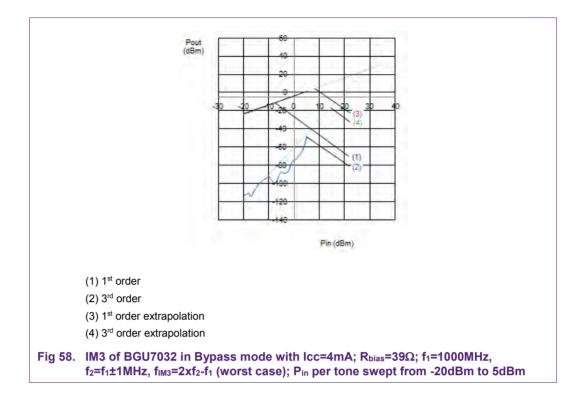


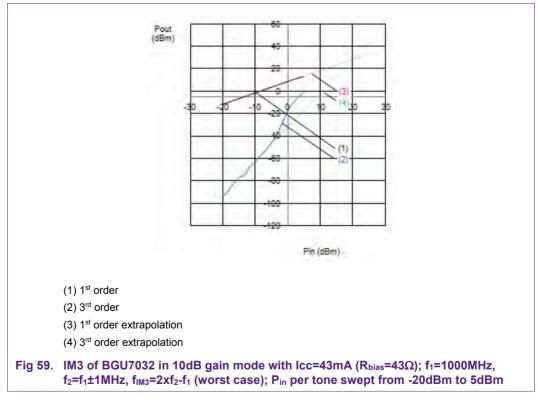
4.3.1.2 BGU7032: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 57 to Fig 60 show 1st and 3rd order response of BGU7032 in 10dB gain and bypass modes with f_1 =1000MHz, f_2 = f_1 ±1MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from - 20dBm to 5dBm.



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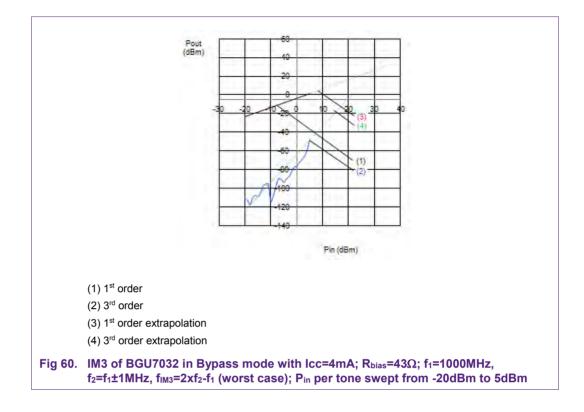




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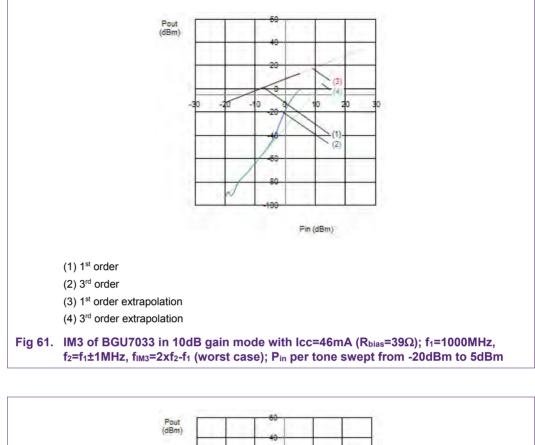
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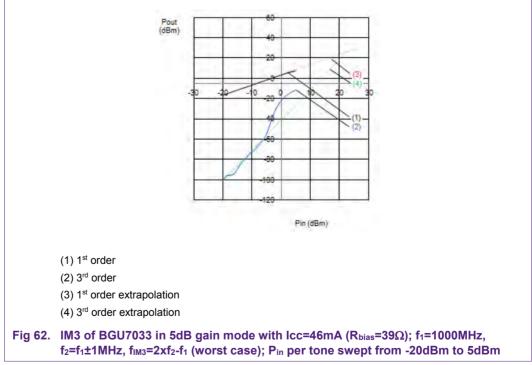


4.3.1.3 BGU7033: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 61 to Fig 66 show 1^{st} and 3^{rd} order response of BGU7033 in 10dB gain, 5dB gain, and bypass modes with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm.

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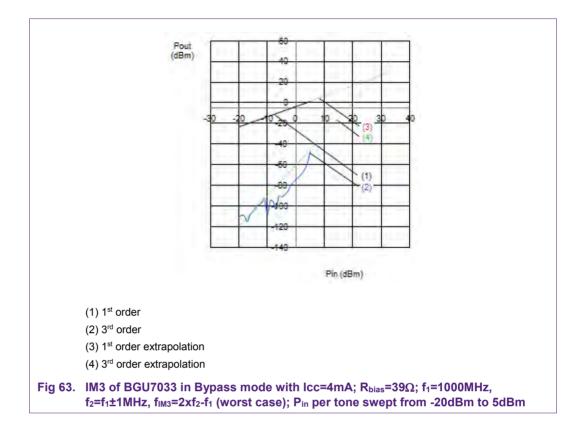




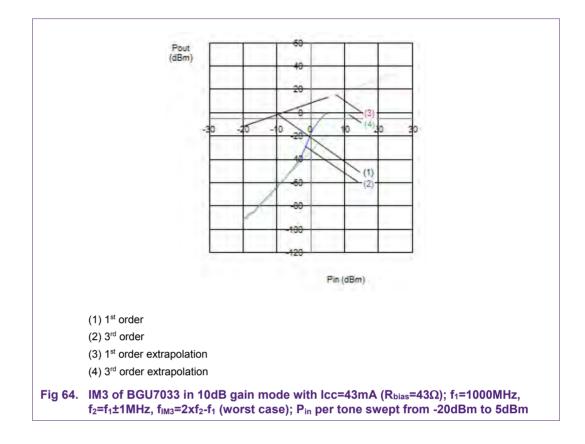
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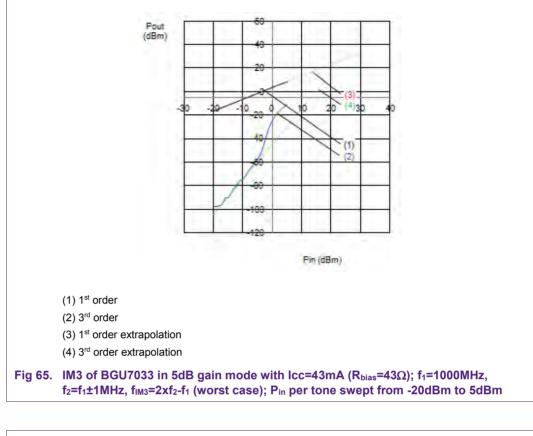
AN11209

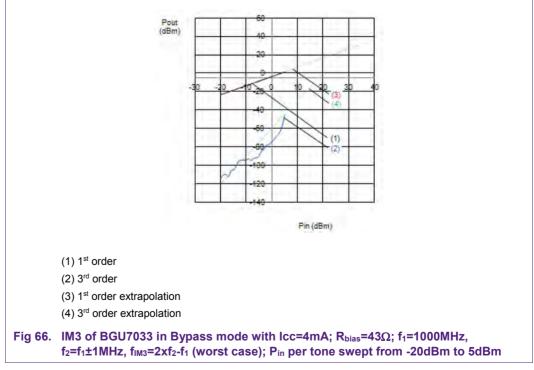


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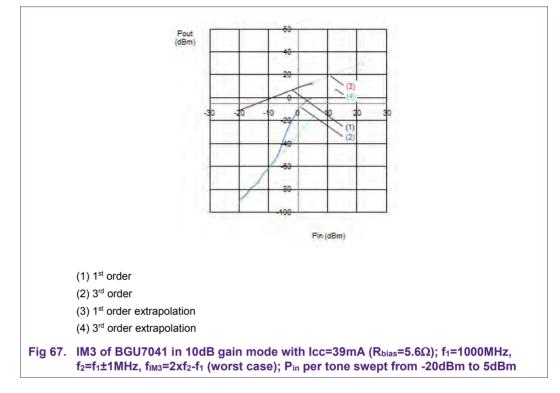




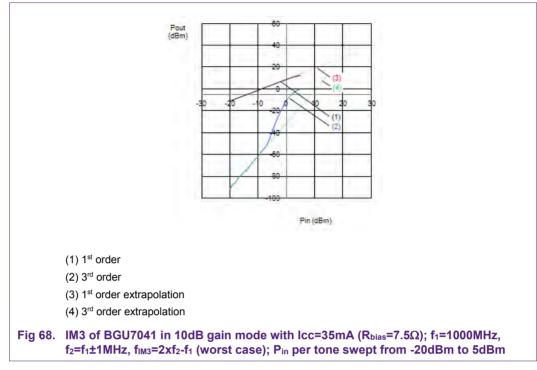
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4.3.1.4 BGU7041: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 67 to Fig 68 show 1st and 3rd order response of BGU7041 in 10dB gain mode with f_1 =1000MHz, f_2 = f_1 ±1MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.



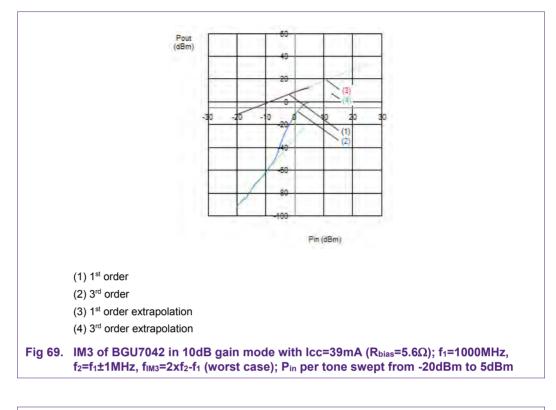
AN11209

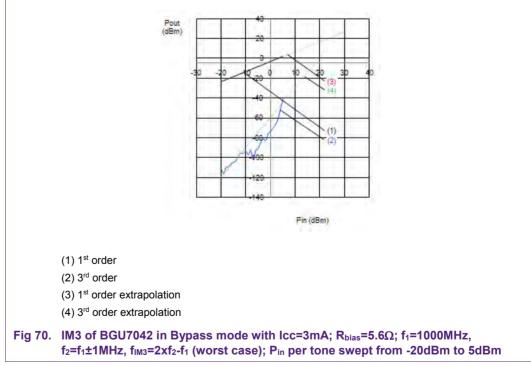


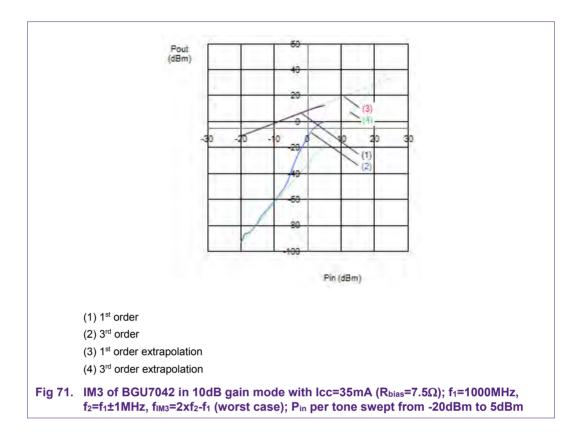
4.3.1.5 BGU7042: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

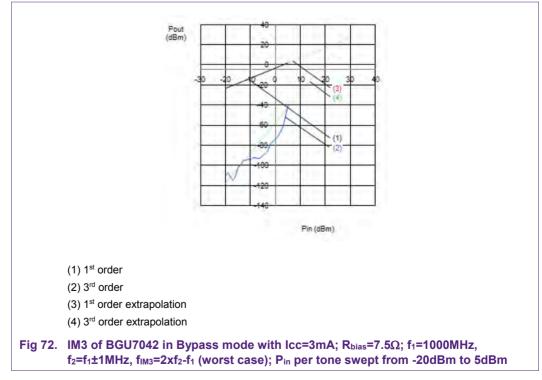
Fig 69 to Fig 72 show 1st and 3rd order response of BGU7042 in 10dB gain and bypass modes with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from - 20dBm to 5dBm.

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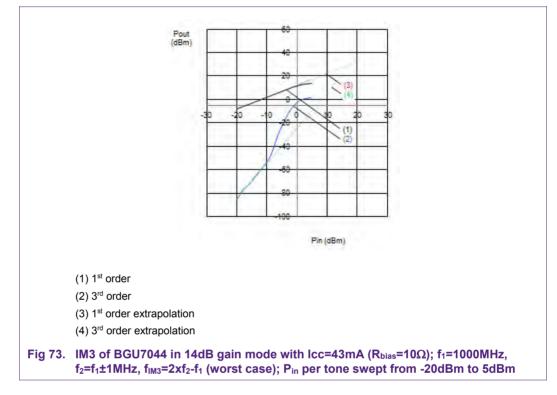




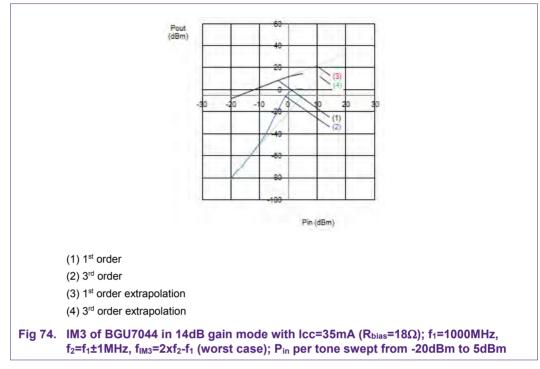


4.3.1.6 BGU7044: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 73 to Fig 74 show 1st and 3rd order response of BGU7044 in 14dB gain mode with f_1 =1000MHz, f_2 = f_1 ±1MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.



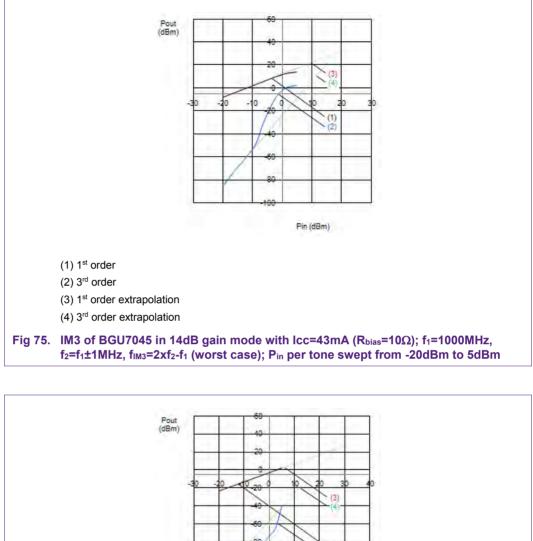
AN11209

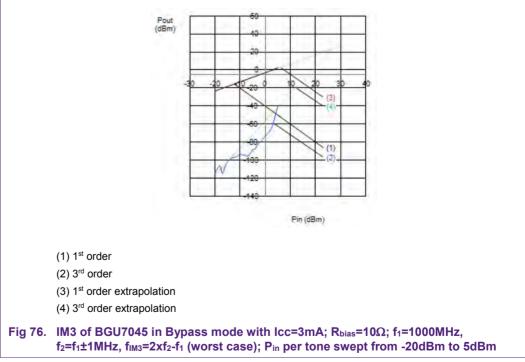


4.3.1.7 BGU7045: IM3 with f₁=1000MHz, f₂=f₁±1MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 75 to Fig 78 show 1st and 3rd order response of BGU7045 in 14dB gain and bypass mode with f_1 =1000MHz, f_2 = f_1 ±1MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from - 20dBm to 5dBm.

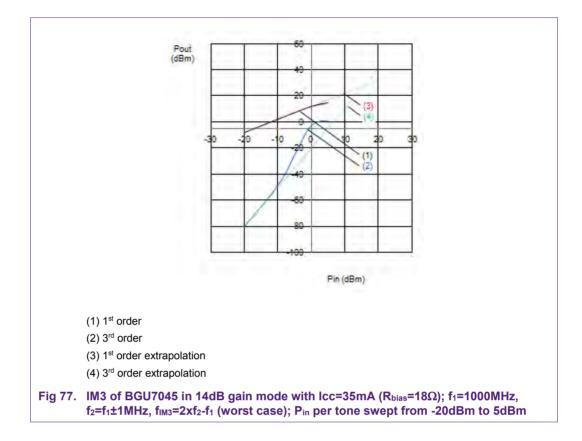
AN11209



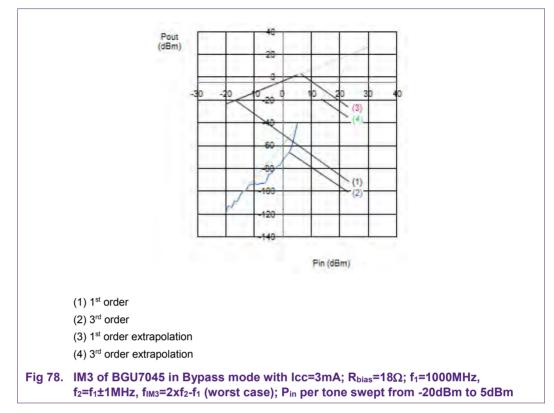


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4.3.2 IM3 with f₁=900MHz, f₂=910MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

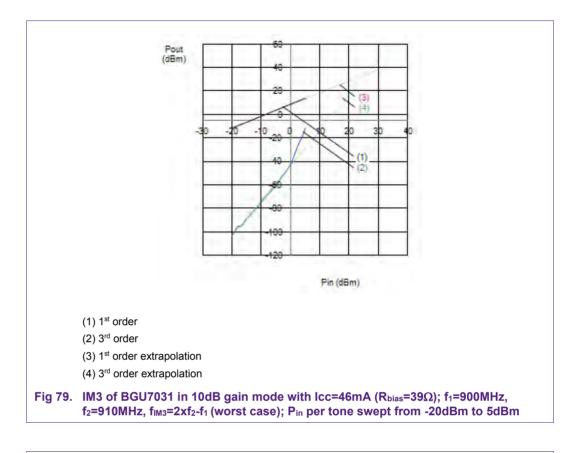
Table 15 shows an overview of IIP3 with f₁=900MHz, f₂=910MHz, f_{IM3}=920MHz; P_{in} =-20dBm per tone for BGU703x (5.0V devices) and BGU704x (3.3V devices) in different modes.

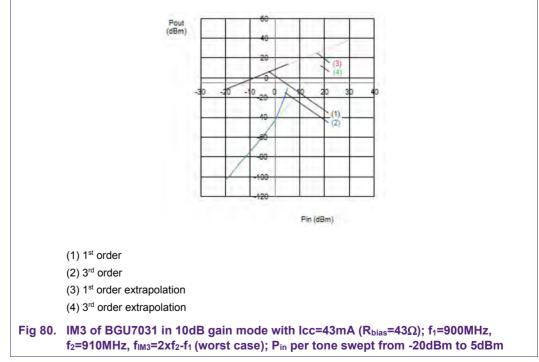
Table 15. Overview of IIP3 with f1=900MHz, f2=910MHz, f1M3=920MHz; Pin =-20dBm per tone for BGU703x and BGU704x in different modes

I	IIP3 with f_1 =900MHz, f_2 =910MHz, f_{IM3} =920MHz, Pin=-20dBm per tone															
Г	IIP3			Туре												
L				BGU7031	BGU7032		BGU7033			BGU7041	BGU7042		BGU7044	BGU7045		
				10dB Gain	10dB Gain	Bypass	10dB Gain	5dB Gain	Bypass	10dB Gain	10dB Gain	Bypass	14dB Gain	14dB Gain	Bypass	
bias current	in gain	ωo	35	N/A	N/A	N/A	N/A	N/A	N/A	2.43E+01	2.49E+01	2.07E+01	1.97E+01	2.02E+01	2.22E+01	
			39	N/A	N/A	N/A	N/A	N/A	N/A	2.27E+01	2.52E+01	2.14E+01	N/A	N/A	N/A	
			43	2.57E+01	2.43E+01	2.29E+01	2.39E+01	2.43E+01	2.34E+01	N/A	N/A	N/A	2.25E+01	2.26E+01	2.27E+01	
			46	2.56E+01	2.49E+01	1.96E+01	2.47E+01	2.52E+01	1.95E+01	N/A	N/A	N/A	N/A	N/A	N/A	

4.3.2.1 BGU7031: IM3 with f₁=900MHz, f₂=910MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

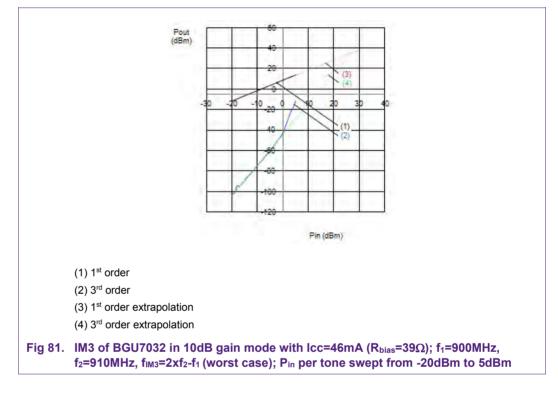
Fig 79 to Fig 80 show 1^{st} and 3^{rd} order response of BGU7031 in 10dB gain mode with f₁=900MHz, f₂=910MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm.

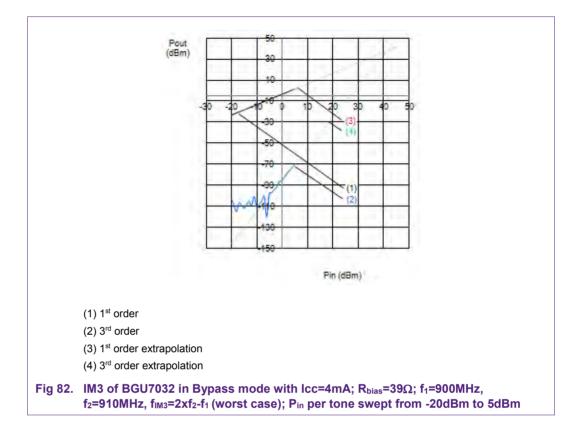


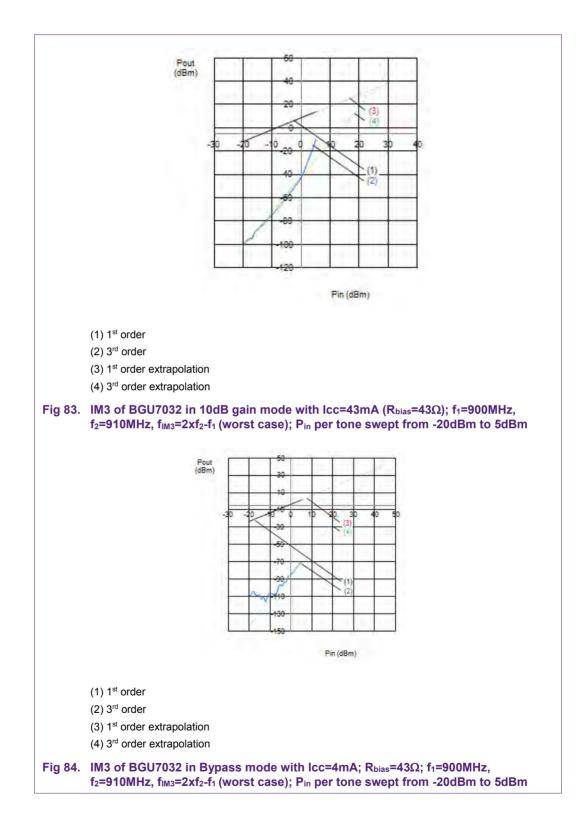


4.3.2.2 BGU7032: IM3 with f₁=900MHz, f₂=910MHz, f_{1M3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 81 to Fig 84 show 1st and 3rd order response of BGU7032 in 10dB gain and bypass modes with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from - 20dBm to 5dBm.

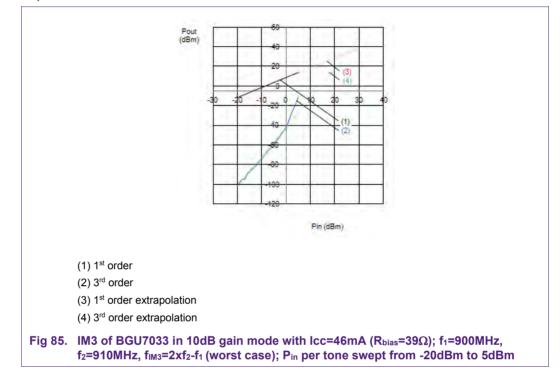




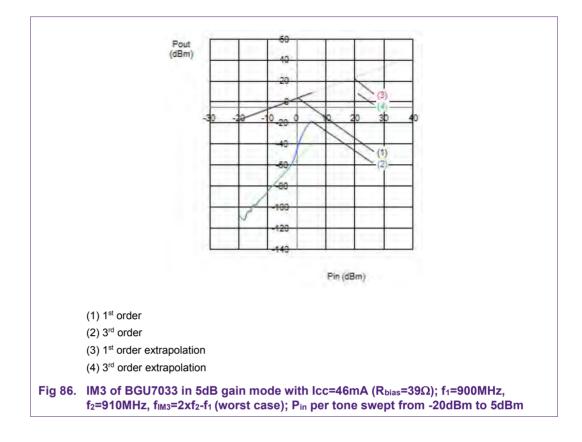


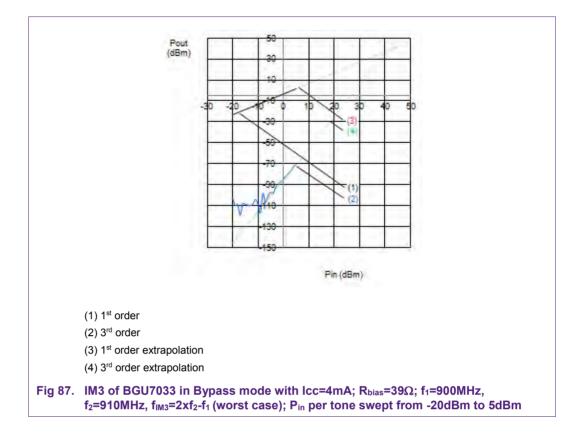
4.3.2.3 BGU7033: IM3 with f₁=900MHz, f₂=910MHz, f_{1M3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 85 to Fig 90 show 1st and 3rd order response of BGU7033 in 10dB gain, 5dB gain, and bypass modes with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.

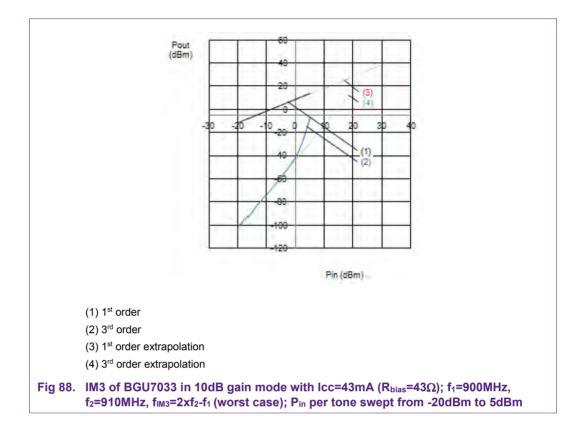


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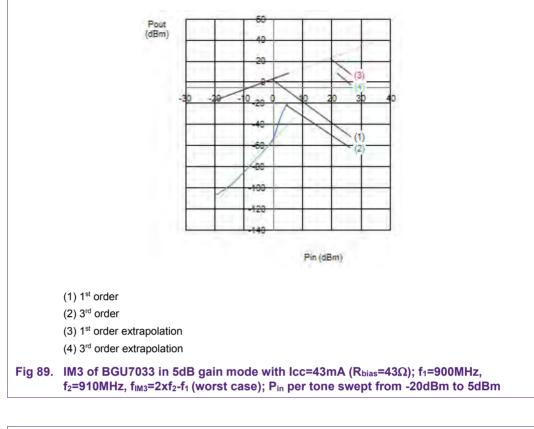


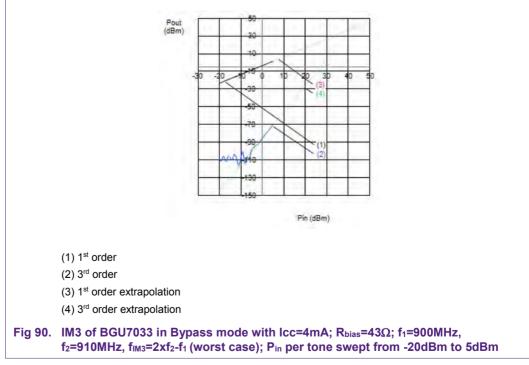


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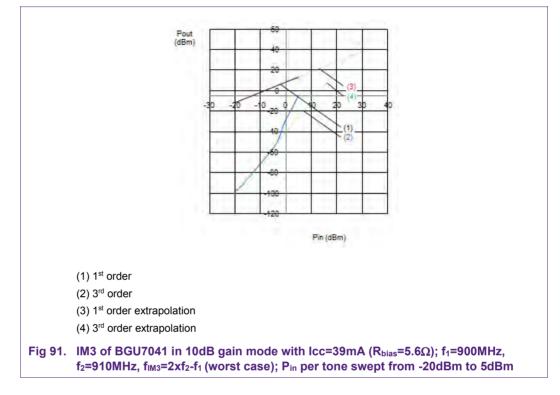
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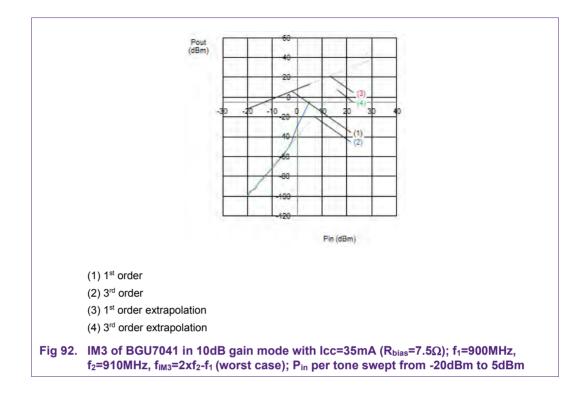


4.3.2.4 BGU7041: IM3 with f₁=900MHz, f₂=910MHz, f_{1M3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 91 to Fig 92 show 1st and 3rd order response of BGU7041 in 10dB gain mode with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.

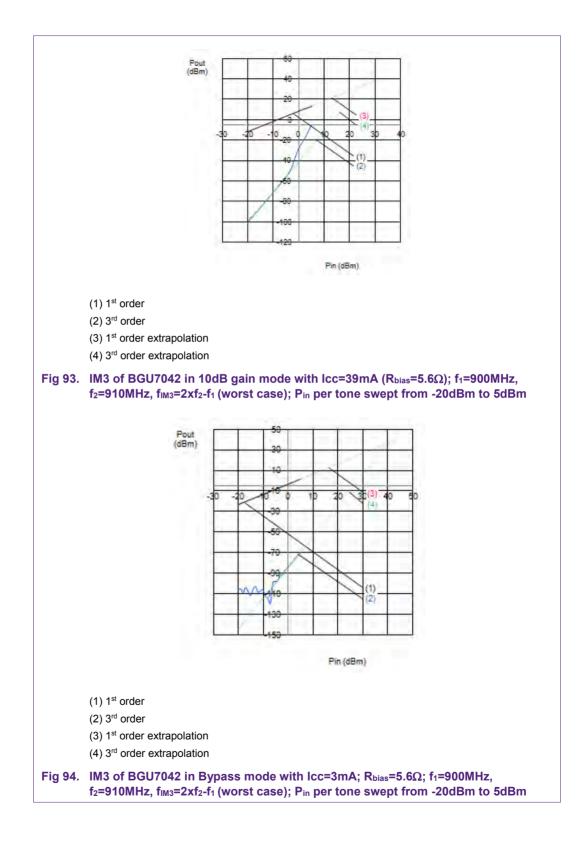


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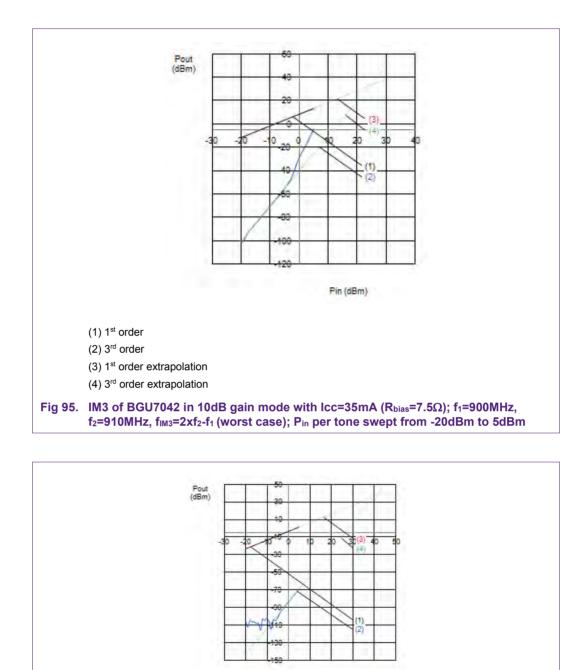


4.3.2.5 BGU7042: IM3 with f₁=900MHz, f₂=910MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 93 to Fig 96 show 1st and 3rd order response of BGU7042 in 10dB gain and bypass modes with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from - 20dBm to 5dBm.



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Pin (dBm)

Fig 96. IM3 of BGU7042 in Bypass mode with Icc=3mA; R_{bias}=7.5Ω; f₁=900MHz,

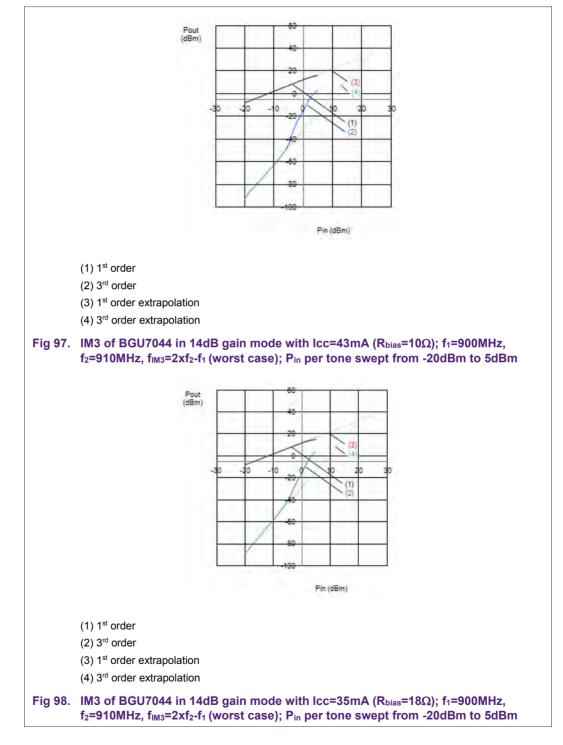
f₂=910MHz, f_{IM3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

(1) 1st order(2) 3rd order

(3) 1st order extrapolation
(4) 3rd order extrapolation

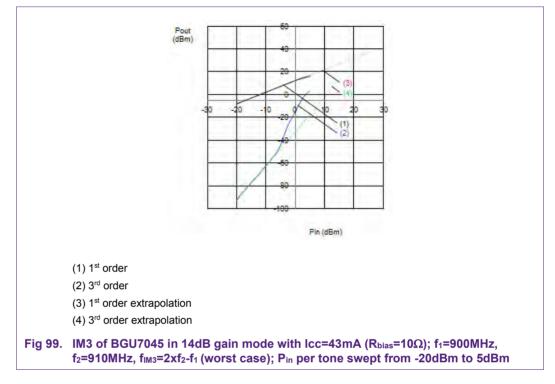
4.3.2.6 BGU7044: IM3 with f₁=900MHz, f₂=910MHz, f_{1M3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 97 to Fig 98 show 1st and 3rd order response of BGU7044 in 14dB gain mode with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from -20dBm to 5dBm.

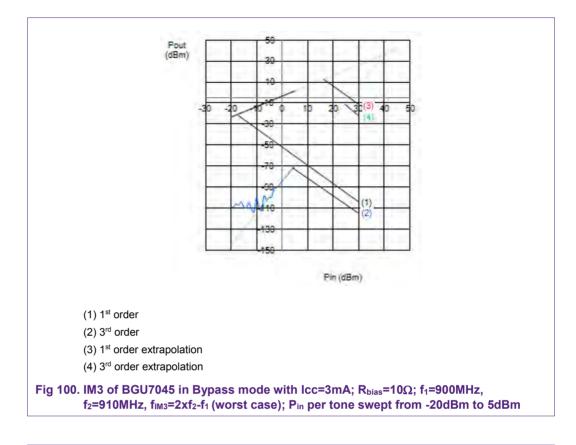


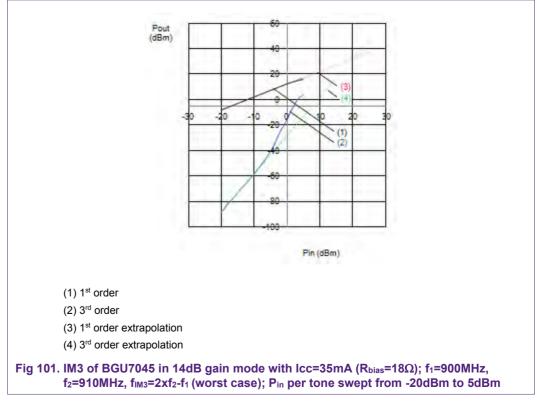
4.3.2.7 BGU7045: IM3 with f₁=900MHz, f₂=910MHz, f_{1M3}=2xf₂-f₁ (worst case); P_{in} per tone swept from -20dBm to 5dBm

Fig 99 to Fig 102 show 1st and 3rd order response of BGU7045 in 14dB gain and bypass modes with f_1 =900MHz, f_2 =910MHz, f_{IM3} =2x f_2 - f_1 (worst case); P_{in} per tone swept from - 20dBm to 5dBm.



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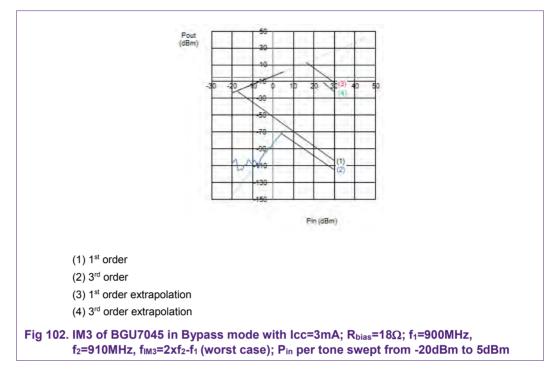




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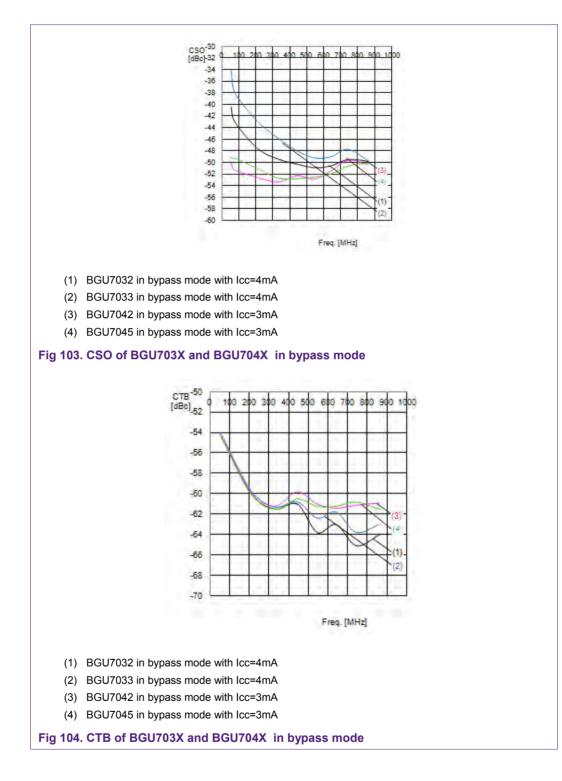
4.4 CSO and CTB

Composite Second Order beat (CSO) and Composite Triple Beat (CTB) have been measured with 131 NTSC channels, and Vout=25dBmV for bypass mode and Vin=15dBmV for gain modes.

4.4.1 CSO and CTB in Bypass Mode of BGU703X and BGU704X

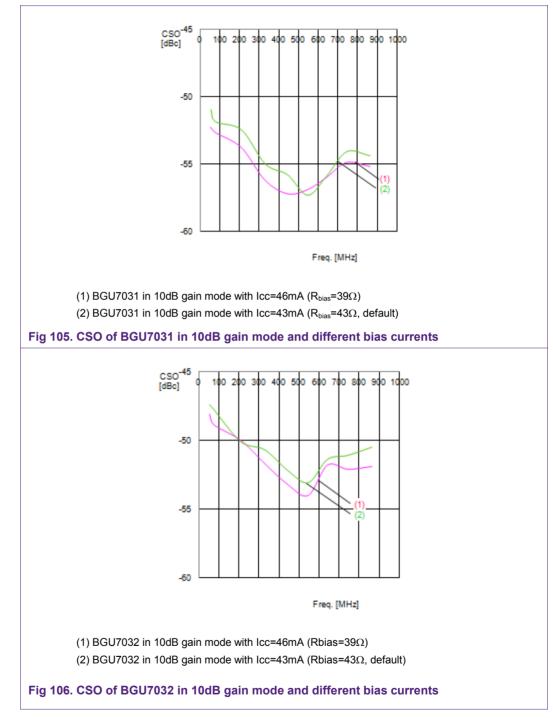
Fig 103 and Fig 104 show the CSO and CTB respectively of BGU7032, BGU7033, BGU7042 and BGU7045 in bypass mode.

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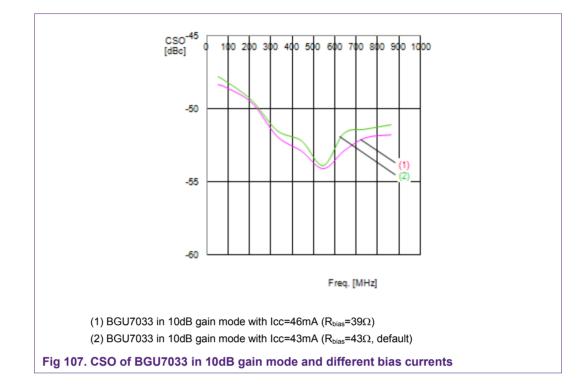
4.4.2 CSO in Gain Modes of BGU703X and BGU704X

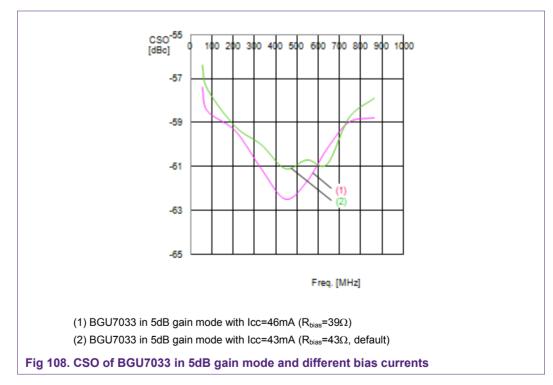
Fig 105 to Fig 108 show the CSO of BGU7031, BGU7032, and BGU7033 in different gain modes and with different bias currents. Fig 109 to Fig 112 show the CSO of



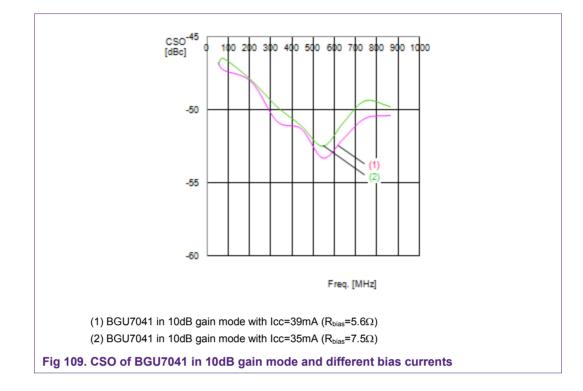
BGU7041, BGU7042, BGU7044, and BGU7045 in different gain modes and with different bias currents.

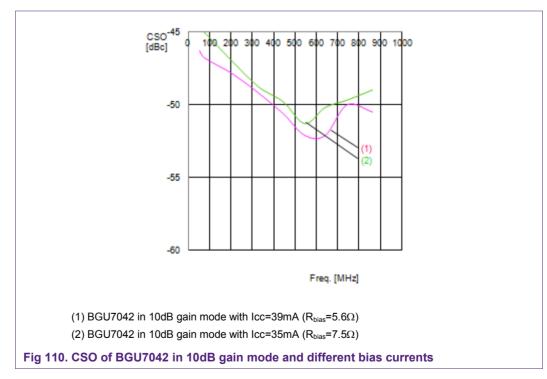
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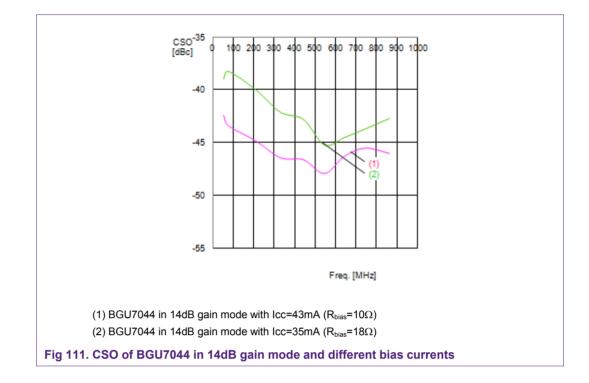


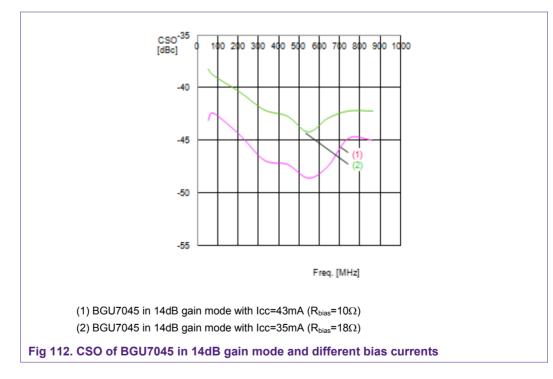
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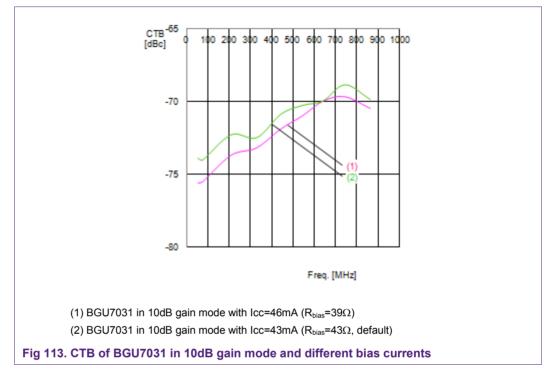


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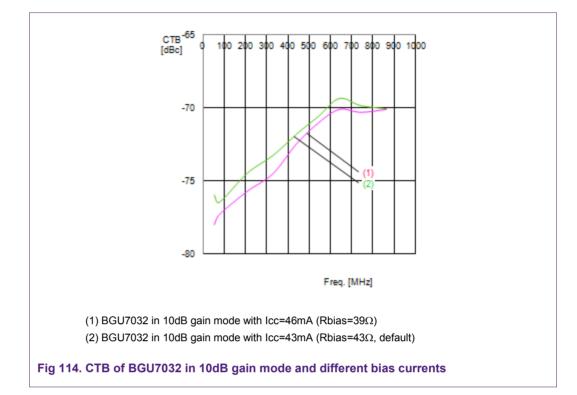
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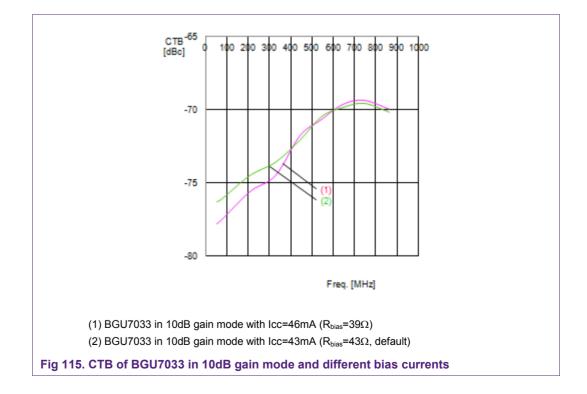
4.4.3 CTB in Gain Modes of BGU703X and BGU704X

Fig 113 to Fig 116 show the CTB of BGU7031, BGU7032, and BGU7033 in different gain modes and with different bias currents. Fig 117 to Fig 120 show the CTB of BGU7041, BGU7042, BGU7044, and BGU7045 in different gain modes and with different bias currents.

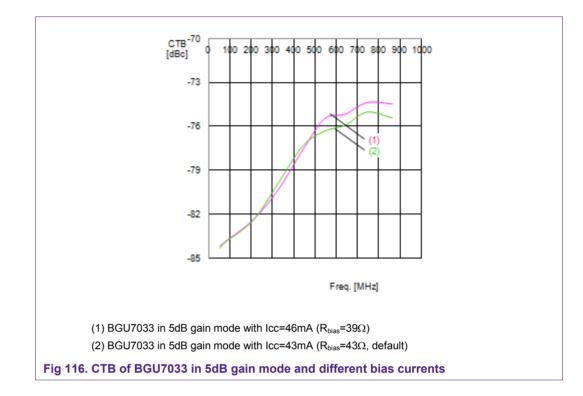


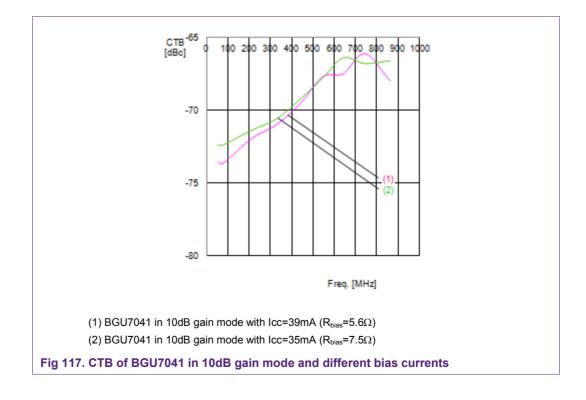
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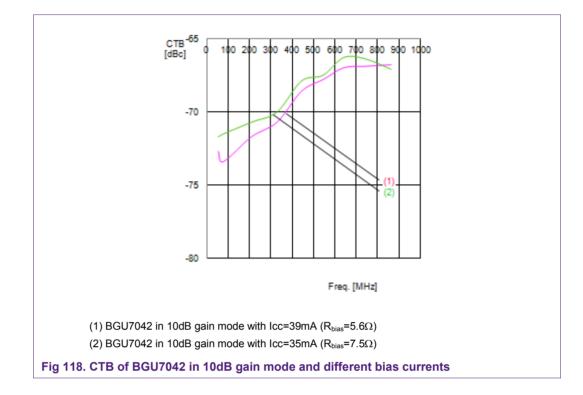
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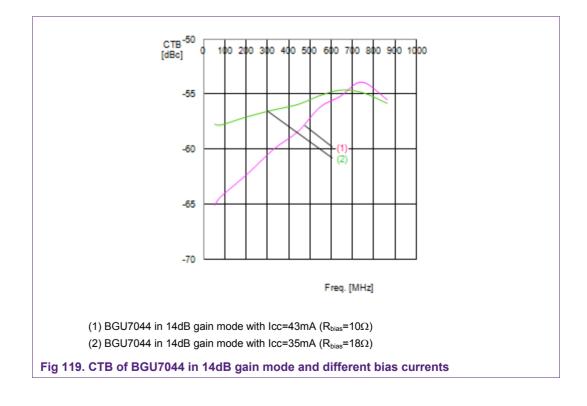


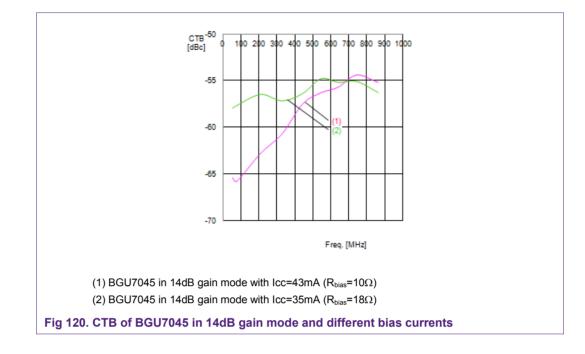


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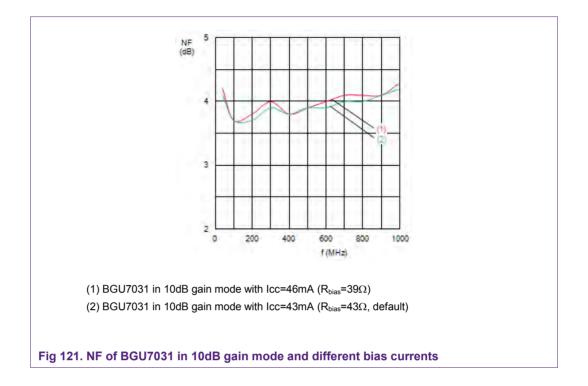
4.5 NF

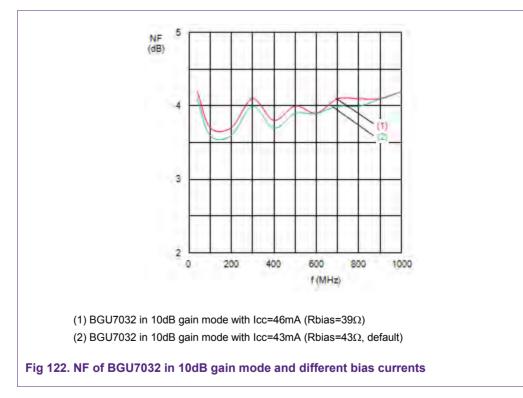
The NF measurement results for different bias currents of BGU703X and BGU704X are given in chapter 4.5.1 and chapter 4.5.2 respectively.

4.5.1 NF of BGU703X

Fig 121, Fig 122, and Fig 123 show the NF of BGU7031, BGU7032, and BGU7033 respectively in different modes and with different bias currents.

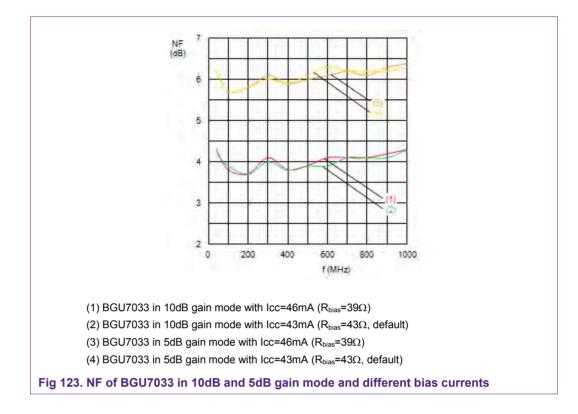
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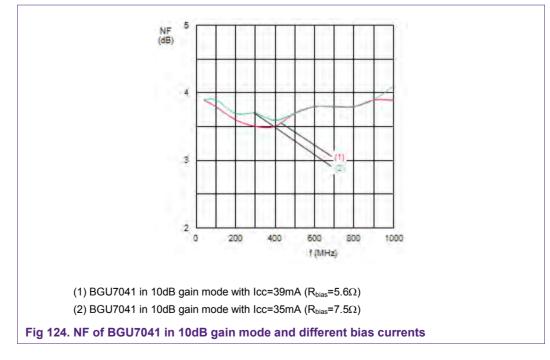
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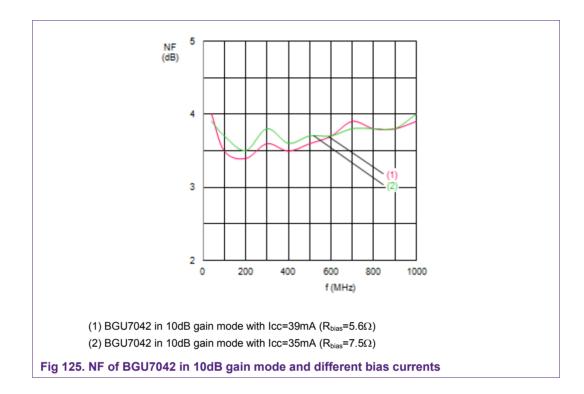
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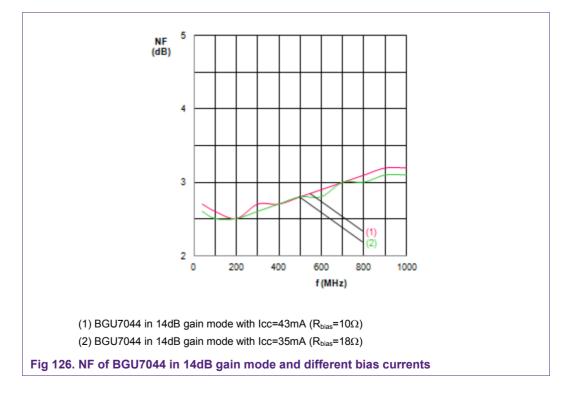


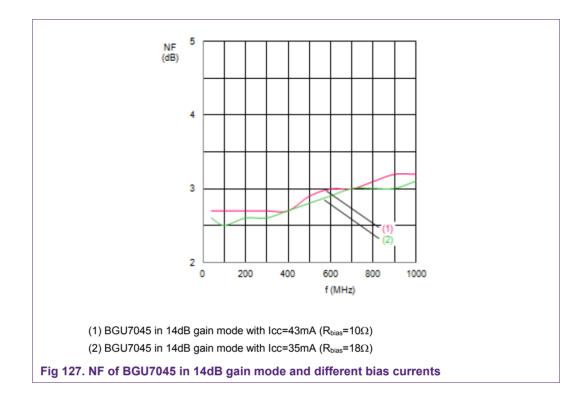
4.5.2 NF of BGU704X

Fig 124 to Fig 127 show the NF of BGU7041, BGU7042, BGU7044, and BGU7045 respectively in different modes and with different bias currents.









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	20dBm to 5dBm
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