AN11562
PN7120 Low Power Mode Configuration
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Document information

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<tr>
<td>Abstract</td>
<td>This application notes provides guidance on how PN7120 can be configured in order to reduce current consumption by using low power discovery mode</td>
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Revision history

<table>
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<tr>
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</table>

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1. Introduction

PN7120 implements an extreme low power discovery mode allowing decreasing up to 100 times the current consumption of the NFC Controller. This consumption reduction does not impact the user experience.

This application note depicts how to use and tune this feature.

2. Low Power Discovery Mode concept

PN7120 supports **RF DISCOVERY** defined within ACTIVITY specification from NFC FORUM (see [1]). PN7120 can be configured by following guidance depicted with NFC specification (see NCI specification [2]).

The discovery loop consist of 2 phases:
- POLL phase where NFCC emits RF field and sense for remote tag or peer NFC device
- LISTEN phase where NFCC hears for remote reader of peer NFC device

Average NFCC power consumption then depends on:
- Technologies enabled in the POLL phase (lead to about 5ms to 80ms duration)
- LISTEN phase duration
- Antenna system used by the application (impedance of the RF system)

![Regular Discovery loop](image-url)
Current consumption is one of the main criteria during a NFC design-in within an embedded equipment. PN7120 NFC controller implements two modes of Low Power discovery additionally to the regular discovery mode:

- Low Power Tag Detector mode
- Hybrid mode

2.1 Low Power Tag Detector mode

It consists on replacing each POLL phase of the regular discovery loop by a short LPCD pulse (few us of RF emission), allowing the PN7120 to check any change in the antenna proximity area. Whenever a change is detected, a regular POLL phase is triggered to verify the presence of tag or peer NFC device.

The obtained new duty cycle allows achieving an extremely low current consumption. NXP provides a proprietary extension to the NCI protocol to enable and configure this mode (refer to PN7120 User Manual [4] for more details).
2.2 Hybrid mode

The aim of Hybrid discovery mode is to replace some regular POLL phases by LPCD pulses.

This mode allows reducing significantly the average current consumption of the NFC Controller in comparison to the regular discovery loop, if the LPCD could not be used (infrequent cases where Low Power Tag Detector mode provide reduced user experience).

NXP provides several proprietary parameters which can be configured through CORE_SET_CONFIG_CMD from the Device Host in order to enable this mode and define the amount of LPCD pulse (refer to PN7120 User Manual [4] for more details).
Fig 5. Discovery loop in Hybrid mode

Fig 6. Oscilloscope screenshot of Hybrid mode with POLL phase every 3 LPCD pulses @ 2Hz
3. Power consumption overview

Current consumption of PN7120 depends on hardware integration within platform. Below figure depict the current consumption according the PN7120 IC state:

The wake-up time is about 2.5ms and the time to switch back in standby is a few hundreds of μs.

The current consumption is standby state is about 20μA and about 6mA in wake-up state, while in RF emission state it highly depends on the antenna impedance and matching circuitry (refer to PN7120 Product Datasheet [3] for more details about power consumption figures).

Following parameters impact the overall current consumption of the system:

- Duration of RF emission
  - \( T_{\text{POLL}} \), in Regular and Hybrid modes, relates to the Technologies enabled in the discovery loop
  - RF pulse, in Low Power Tag Detector mode, is about 150μs
- Duration of clock establishment
  - Using XTAL \( T_{\text{clock}} \) is only few us
  - Using an external system clock \( T_{\text{clock}} \) is from 1ms to 10ms (depending on platform capability)
Reference configuration used for the overview:
- Clock: XTAL
- Antenna matching impedance: 28Ohms
- \( T_{\text{POLL}} = 60\text{ms} \)
- Hybrid mode: 1 regular RF polling for 3 RF pulses are considered

Table 1. Current consumption of discovery modes

<table>
<thead>
<tr>
<th>Discovery loop mode</th>
<th>Discovery loop frequency</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1Hz</td>
</tr>
<tr>
<td>Regular</td>
<td>6,037mA</td>
</tr>
<tr>
<td>Hybrid</td>
<td>1,623mA</td>
</tr>
<tr>
<td>Low Power Tag Detector</td>
<td>75(\mu)A</td>
</tr>
</tbody>
</table>

Here is a view of current consumption in Low Power Tag Detector mode with different configurations (antenna impedance and clock):

Table 2. Current consumption depending on configuration

| Discovery loop mode          | 
|-----------------------------|-----------------
|                             | \( Z = 80\text{Ohms} \) | \( Z = 28\text{Ohms} \) |
|                             | XTAL (YS)       | XTAL (YS)       |
| Low Power Tag Detector @2Hz | 113\(\mu\)A     | 130\(\mu\)A     |

<table>
<thead>
<tr>
<th></th>
<th>Z = 28\text{Ohms}</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>SysClock 1ms</td>
</tr>
<tr>
<td>Low Power Tag Detector @2Hz</td>
<td>139(\mu)A</td>
</tr>
</tbody>
</table>
4. Configure Low Power mode

4.1 Host interface parameter description

You can find below parameters needed to configure the PN7120 in Regular, Hybrid or Low Power Tag Detector modes.

All those parameters can be modified by using the **CORE_SET_CONFIG_CMD** from NCI standard (see *NCI specification [2]*). Proprietary part is described in *PN7120 User Manual [4]*.

### Table 3. Discovery loop NCI Host Interface definition

<table>
<thead>
<tr>
<th>Name</th>
<th>NCI Tag</th>
<th>Length</th>
<th>Default value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Total_Duration</td>
<td>0x00</td>
<td>2</td>
<td>0xE803</td>
<td>Total duration of the single discovery period in [ms-Little endian coded]</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>TLISTEN = Total_Duration - TPOLL</td>
</tr>
</tbody>
</table>

### Table 4. Discovery loop proprietary Host Interface definition

<table>
<thead>
<tr>
<th>Name</th>
<th>Prop. Tag</th>
<th>Length</th>
<th>Default value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tag_Detector_CFG</td>
<td>0xA040</td>
<td>1</td>
<td>0x00</td>
<td>Tag detector setting as follows:</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>- 0x00 Tag Detector disabled</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>- 0x01 Tag Detector enabled</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>- 0x81 Tag Detector with trace enabled</td>
</tr>
<tr>
<td>Tag_Detector_Threshold_CFG</td>
<td>0xA041</td>
<td>1</td>
<td>0x04</td>
<td>Sets the detection level</td>
</tr>
<tr>
<td>Tag_Detector_Period_CFG</td>
<td>0xA042</td>
<td>1</td>
<td>0x19</td>
<td>Time in steps of 8us to wait the measurement</td>
</tr>
<tr>
<td>Tag_Detector_Fallback_Cnt_CFG</td>
<td>0xA043</td>
<td>1</td>
<td>0x50</td>
<td>Hybrid mode setting as follows:</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>- 0x00 hybrid disabled</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>- 0XX Regular RF polling triggered after XX LPCD pulse</td>
</tr>
</tbody>
</table>

PN7120 proposed a trace mechanism allowing to tune the sensitivity of the LPCD feature, later described in chapter 4.3. The format of the notification message is the following:

### Table 5. Format definition of notification message in trace mode

<table>
<thead>
<tr>
<th>Offset</th>
<th>Length</th>
<th>value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>0x6F</td>
<td>NXP proprietary NTF</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0x13</td>
<td>TAG DETECTOR message</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
<td>0x04</td>
<td>Length of the message</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
<td>0xXXXX</td>
<td>Current reference value [Little endian coded]</td>
</tr>
<tr>
<td>5</td>
<td>2</td>
<td>0xXXXX</td>
<td>Last measurement value [Little endian coded]</td>
</tr>
</tbody>
</table>
4.2 Description of main configuration

Whatever the mode used, NCI Tag “Total_Duration” has to be set if you want a specific duty cycle.

4.2.1 Enable Regular Mode

- Proprietary Tag “Tag_Detector_CFG”:
  - Set to 0x00
- Other proprietary tags are disregarded.

4.2.2 Enable Hybrid Mode

- Proprietary Tag “Tag_Detector_CFG”:
  - Set to 0x01 in order to enable the Tag Detector
- Proprietary Tag “Tag_Detector_Threshold_CFG”:
  - Tune according to the system (see 4.3)
- Proprietary Tag “Tag_Detector_Period_CFG”:
  - Keep to default value
- Extension Tag “Tag_Detector_Fallback_Cnt_CFG”:
  - This value has to be specified: If you want 1 regular POLL phase every N LPCD pulses, this tag has to be set to N+1 (e.g. setting this parameter to 0x04 lead to 1 regular POLL phase every 3 LPCD pulses)

4.2.3 Enable Low Power Card Detection Mode

- Proprietary Tag “Tag_Detector_CFG”:
  - Set to 0x01 in order to enable Tag Detector
- Proprietary Tag “Tag_Detector_Threshold_CFG”:
  - Tune according to the system (see 4.3)
- Proprietary Tag “Tag_Detector_Period_CFG”:
  - Keep to default value
- Extension Tag “Tag_Detector_Fallback_Cnt_CFG”:
  - Set to 0x00
4.3 Determining LPCD sensitivity

In order to define easily the threshold for the system, PN7120 provide trace functionality sharing measurement values from internal HW modules for each LPCD pulse. Thanks to the information raised in these messages the adequate threshold level can be determined for the current system.

4.3.1 Pre-requisite

- DUT: system based on PN7120 IC
- Non electromagnetic spacers of few centimeters
- Oscilloscope with NFC coil

4.3.2 Procedure

Step 1: Preparation

Place the DUT on top of a spacer in order to have some distance from the desk and turn the DUT antenna upward.

Then ensure that no external interference could impact the measurement. For instance, avoid having other electronic devices around.

Step 2: Set the DUT in Low Power Tag Detector with trace mode

Enable the TRACE mode by setting the proprietary Tag “Tag_Detector_CFG” to 0x81.

Then set the Tag “Tag_Detector_Threshold_CFG” to 0x10 defining the first threshold to evaluate. Verify if this configuration is well applied by reading back the parameter value with CORE_GET_CONFIG_CMD.

Check that TRACE messages are broadcasted by the PN7120.

For instance, if the system run android environment (refer to Android SDK for more information), use the following command from a computer connected through ADB (use ‘find’ instead of ‘grep’ from windows):

```plaintext
adb logcat -vtime | grep "6f 13"
```
Step 3: Evaluate the sensitivity for the default threshold
For instance, if the system run android environment, use the following command (use ‘find’ instead of ‘grep’ from windows):

```
adb logcat –vtime | grep “6f 13” > DUT_threshold_10.txt
```

Wait some minutes in order to obtain around 2500 messages; then abort the process.

Step 4: analyze logs
Extract from the logs the list of values measured by the LPCD module.
PN7120 in this mode will always set its reference value around the middle of measurements dispersion.

To define the threshold maximum and minimum measurements have to be

\[
\frac{(\text{Max} - \text{Min})}{2} = \text{Threshold}
\]

considered:Update the threshold to its new value by modifying Tag “Tag_Detector_Threshold_CFG”, then evaluate the new sensitivity set.

For instance, if the system run android environment, use the following command (use ‘find’ instead of ‘grep’ from windows):

```
adb logcat –vtime | grep “6f 13” > DUT_threshold_XX.txt
```

Wait in order to have a large amount of data (more than 5000). Extract from the logs the number of times the LPCD was triggered due to wrong detection.

Step 5: Optional – Fine tune this threshold
Depending on the final application, it could be interesting to either maximize power saving or maximize RF performance.

You can find below a table which summarizes the impact of wrong detections on the overall current consumption:

<table>
<thead>
<tr>
<th>Wrong detection rate</th>
<th>0,00%</th>
<th>0,01%</th>
<th>0,10%</th>
<th>1,00%</th>
<th>2,00%</th>
<th>5,00%</th>
<th>10,00%</th>
<th>20,00%</th>
<th>50,00%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Current consumption (mA)</td>
<td>0,139</td>
<td>0,140</td>
<td>0,151</td>
<td>0,256</td>
<td>0,370</td>
<td>0,700</td>
<td>1,209</td>
<td>2,101</td>
<td>4,063</td>
</tr>
</tbody>
</table>

This table shows that a threshold with a wrong detection rate below 1 per 1000 has a limited impact (12uA).

If you want to increase or decrease the threshold, perform again step 4 in order to verify the wrong detection rate.

Step 6: Verify the overall behavior
Once final value of threshold is defined, you could verify the overall RF behavior with a scope (see Fig 3).
4.4 Determining reader communication range

4.4.1 Required material
- DUT: system based on PN7120 IC
- Non electromagnetic spacers of different thickness (1 / 2 / 5 / 10 mm)
- Oscilloscope with NFC coil
- Tags to evaluate

4.4.2 Procedure

Step 1: Preparation
Prepare the setup as depicted within the figure below.

A spacer of 1cm has to be inserted between the NFC coil and the desk. Then another 1cm spacer will be positioned between the tag under and the NFC coil.

Oscilloscope will be useful in order to identify if the LPCD is triggered or not.

Step 2: Find detection limit
Enable the regular discovery mode (see 4.2.1).

For each tag:
1- Start with no Spacer between the Card under test and the DUT
2- Place the DUT on top of the tag under test;
3- Verify that tag is detected several times (stability of the measurement):
   a. If yes: remove the DUT, add more spacer and go back to 2;
   b. If no: Tag detection limit is reached;
Step 3: Verifying detection limit

Apply the Procedure of Step 2 for the Hybrid or Low Power Detector mode (see 4.2.2 or 4.2.3) instead of the regular discovery mode, in order to confirm the same Tag detection limit is reached in the targeted configuration.

5. Example with a reference device

5.1 Device description

Device is equipped with a 4 turn antenna (45mm * 25mm).

5.2 Definition of the sensitivity threshold of this reference device

On this reference device, here are results of the threshold definition study:

![Measurement dispersion for 5000 measurements](Fig 10)

For this device, here is the evolution of the wrong detection rate versus the threshold:
Here is a summary of the impact of the threshold on the reader range of the LPCD feature:

**Table 7. Detection range of the LPCD depending on the threshold**

<table>
<thead>
<tr>
<th>Card/Device</th>
<th>Regular discovery mode</th>
<th>Threshold in Low Power Detector mode</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>0x08</td>
</tr>
<tr>
<td><strong>ISO15693</strong></td>
<td>58mm</td>
<td>46mm</td>
</tr>
<tr>
<td><strong>TOPAZ</strong></td>
<td>37mm</td>
<td>35mm</td>
</tr>
<tr>
<td><strong>FELICA</strong></td>
<td>31mm</td>
<td>31mm</td>
</tr>
<tr>
<td><strong>MIFARE 1K</strong></td>
<td>41mm</td>
<td>39mm</td>
</tr>
<tr>
<td><strong>MIFARE 4K</strong></td>
<td>28mm</td>
<td>27mm</td>
</tr>
<tr>
<td><strong>MIFARE plus X</strong></td>
<td>22mm</td>
<td>21mm</td>
</tr>
<tr>
<td><strong>NTAG203</strong></td>
<td>38mm</td>
<td>34mm</td>
</tr>
<tr>
<td><strong>NTAG210</strong></td>
<td>57mm</td>
<td>38mm</td>
</tr>
<tr>
<td><strong>MIFARE UL</strong></td>
<td>40mm</td>
<td>38mm</td>
</tr>
<tr>
<td><strong>MIFARE ULC</strong></td>
<td>20mm</td>
<td>20mm</td>
</tr>
<tr>
<td><strong>DESFIRE</strong></td>
<td>19mm</td>
<td>19mm</td>
</tr>
<tr>
<td><strong>P2P against GS3</strong></td>
<td>30mm</td>
<td>29mm</td>
</tr>
<tr>
<td><strong>Wrong detection rate</strong></td>
<td>N/A</td>
<td>0,22%</td>
</tr>
<tr>
<td><strong>Expected current consumption</strong></td>
<td>12mA</td>
<td>232uA</td>
</tr>
</tbody>
</table>
There are 3 cases to consider in order defining the threshold value:

**Case 1**: Detection range just meet acceptance criteria in regular discovery mode

- A Low threshold (i.e. 0x08 and below) has to be set. User experience will be favored compared current consumption (no margin at RF side).

**Case 2**: Detection range is greater than acceptance criteria in regular discovery mode

- A medium threshold (i.e. 0x09 or 0x0A) has to be set. Giving an excellent trade-off between current consumption and user experience.

**Case 3**: Detection range is far greater than acceptance criteria in regular discovery mode

- A high threshold (i.e. 0x0B or more) has to be set. Current consumption will be favored compared to user experience (because we have margins at RF side).
6. How to spy RF activity by using a scope

6.1 Pre-requisite
- NFC device (the DUT)
- Oscilloscope with NFC coil or oscilloscope probe with alligator clip ground lead

6.2 Procedure

Step 1 – Set the oscilloscope
Set the X scale to a large value (i.e. 1s per division).
Set the Y scale to 200mV per division.
Set the mode to AUTO and place the trigger on the left part of the screen.

Step 2 – Observe RF activity
Bring the NFC coil or the oscilloscope probe on top of the device antenna:

Fig 12. Spying RF activity of a NFC device using a coil or a probe

The best in class approach is to put some distance between the device and the oscilloscope probe in order to not bring noise:
7. Abbreviations

Table 8. Abbreviations

<table>
<thead>
<tr>
<th>Acronym</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADB</td>
<td>Android Debug Bridge</td>
</tr>
<tr>
<td>DUT</td>
<td>Device Under Test</td>
</tr>
<tr>
<td>HW</td>
<td>Hardware</td>
</tr>
<tr>
<td>IC</td>
<td>Integrated Circuit</td>
</tr>
<tr>
<td>mm</td>
<td>Millimeter</td>
</tr>
<tr>
<td>NFC</td>
<td>Near Field Communication</td>
</tr>
<tr>
<td>NFCC</td>
<td>NFC Controller</td>
</tr>
<tr>
<td>RF</td>
<td>Radio Frequency</td>
</tr>
<tr>
<td>LPCD</td>
<td>Low Power Card Detection</td>
</tr>
<tr>
<td>Z</td>
<td>Impedance</td>
</tr>
</tbody>
</table>
8. References

[1] NFC FORUM Activity Specification 1.0
[2] NFC FORUM NFC Controller Interface, version 1.0
[3] PN7120 Datasheet
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