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Low-Cost External Remote Keyless Entry (RKE) Low Noise Amplifier for 315 / 433 / 868 MHz Automotive Applications using the BFU520W RF Transistor
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Abstract
This document provides circuit schematic, layout, BOM and typical evaluation board performance for a low-cost, wideband LNA covering 315, 433 & 868 MHz bands. Two LNA variants are presented; “Type 1” is set up to run from a fixed 5 volt supply (in-vehicle application) whereas the “Type 2” is modified so as to run from a single-cell battery as in a Keyfob.

Document information

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<tr>
<th>Info</th>
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<tbody>
<tr>
<td>Keywords</td>
<td>BFU520W, Remote Keyless Entry, RKE, Automotive, LNA</td>
</tr>
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## Revision history

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1. Introduction

The BFU520W is an Automotive-Qualified (AEC-Q101) silicon RF Transistor for high-frequency, low-noise applications. BFU520W is one of 20 different types of RF transistor in NXP’s new BFU5xxx family. Fabricated in a modern 8-inch process, these devices show exceptional device-to-device consistency and very high production yields. BFU5xxx devices are suitable for small signal to medium power RF applications up to 2 GHz, have a collector-emitter breakdown voltage $BV_{CEO}$ of 12 volts, and are available in a wide variety of industry-standard surface-mount packages. All members of this transistor family are Automotive-Qualified per AEC-Q101. Figure 1 below gives an overview of this family of devices, with the “Icc” row showing typical (not maximum permitted) operating currents for each type.

<table>
<thead>
<tr>
<th>Package</th>
<th>Transistor Types</th>
</tr>
</thead>
<tbody>
<tr>
<td>Icc</td>
<td>5 mA</td>
</tr>
<tr>
<td>SOT323</td>
<td>BFU520W</td>
</tr>
<tr>
<td>SOT23</td>
<td>BFU520A</td>
</tr>
<tr>
<td>SOT143</td>
<td>BFU520</td>
</tr>
<tr>
<td>SOT143X</td>
<td>BFU520X</td>
</tr>
<tr>
<td>SOT143XR</td>
<td>BFU520XR</td>
</tr>
<tr>
<td>SOT89</td>
<td>BFU580Q</td>
</tr>
<tr>
<td>SOT223</td>
<td>BFU580G</td>
</tr>
<tr>
<td>SOT363</td>
<td>BFU520Y</td>
</tr>
</tbody>
</table>

Fig 1. Overview of BFU5xxx family of RF Transistors

Key Benefits of BFU5xxx family:

- **Automotive-Qualified per AEC-Q101**
- High Gain, Low Noise Figure; suitable for applications up to 2 GHz
- Low cost / high performance-to-price ratio
- Five (5) different RF transistor chip types – covering low current, low-noise to higher current, medium power ‘driver’ applications
- Eight (8) different industry-standard package types with externally visible connections to facilitate visual inspection of solder joints
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS) following NXP’s RHF-2006 indicator D (dark green)

Key Benefits of BFU520W External RKE LNA described in this Applications Note:

- 3 – 5 dB improvement in RKE Receiver Sensitivity
- High Gain & Low Noise Figure with only ~ 3.5 mA current consumption
- Broad Bandwidth => same design used for 315, 433 & 868 MHz with no changes
- Very Low-cost implementation; no chip inductors (only resistors & capacitors)
- Unconditional Stability
- Easy-to-use, forgiving design, using only “E12” series component values
- Flexible – Gain, Linearity, etc. can be adjusted by changing values of externals
2. Automotive RKE application constraints, the ‘range problem’ and BFU520W LNA Design

**Figure 2** shows frequency bands in use for Remote Keyless Entry applications, along with car & light truck production volumes for different regions as of 2015. RKE systems presently in use operate at 315, 433 or 868 MHz. Note, a half-wavelength at 315 MHz would be ~ 47.5 cm, & about 34.5 cm at 434 MHz. If we use a half-wave dipole as a starting point or reference for an RKE system antenna, one can imagine how large this antenna would be, especially for use in keyfobs. Bear in mind, antenna efficiency tends to degrade dramatically as we reduce antenna size below this half-wavelength value.

<table>
<thead>
<tr>
<th>Region</th>
<th>Estimated Vehicle Production Volume (Millions), CY2015</th>
<th>RKE Frequency (‘1-way’ RKE)</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>EU</td>
<td>21</td>
<td>433 MHz</td>
<td>Limited 2-way RKE applications @ 868 MHz *</td>
</tr>
<tr>
<td>China</td>
<td>24</td>
<td>315 MHz</td>
<td>Limited 2-way RKE applications @ 868 MHz *</td>
</tr>
<tr>
<td>Japan</td>
<td>13</td>
<td>315 MHz</td>
<td>Very low permitted keyfob Tx output power</td>
</tr>
<tr>
<td>NAFTA</td>
<td>19</td>
<td>315 MHz</td>
<td>Limited 2-way RKE applications @ 868 MHz *</td>
</tr>
<tr>
<td>South America</td>
<td>3</td>
<td>315 MHz</td>
<td>Limited 2-way RKE applications @ 868 MHz *</td>
</tr>
</tbody>
</table>

**Fig 2. World-wide frequency bands in use for RKE applications.**

In automotive systems, the RKE receiver antenna is often placed in a non-ideal location for radio signal propagation - in the vehicle’s dashboard or other position dictated by cost and practical considerations. Such non-ideal locations increase path loss & reduce RKE system range. Furthermore, the antennas used are typically printed on a PC board, or constructed with wire or stamped metal, with lowest possible cost and poor efficiency. Lastly, these antennas are often ‘electrically short’ or smaller than a half-wavelength, in addition to being bent and folded into strange shapes, due to size constraints. All of these compromises further degrade antenna efficiency. See **Figure 3** for an example.

**Fig 3. Example automotive RKE receiver block which would be placed in a vehicle.** Note low-cost wire antenna, which is bent & folded to fit the available space. This implementation is far less efficient than our classic λ/2 dipole used as a reference. Size and cost limitations adversely affect antenna efficiency, reducing RKE system range.
It must be emphasized, that the above described compromises made to RKE antenna placement and implementation all tend to reduce the radio link margin or range of the RKE system. In newer, emerging 2-Way RKE systems, the small ‘keyfob’ held by the vehicle operator also must have an RKE receiver / transceiver installed, and keyfob antennas are even smaller and less efficient, being only a small fraction of a wavelength in size. Meanwhile, automotive manufacturers & their customers demand ever increasing range (and thus better radio link margin), with the RKE system being expected to work at ranges of 30 to 100 meters or more.

Despite improvements in Noise Figure (and receiver sensitivity) of CMOS RKE integrated circuits themselves, the range of the RKE system is often less than desired, given all the aforementioned compromises made for the sake of cost-reduction & practicality. The Automotive RKE system designer is faced with a challenge, of how to increase RKE system range in a cost-effective way. Figure 4 gives a list of the most common options available to increase RKE radio link margin, along with limitations of each. The last option – addition of an external Low Noise Amplifier (LNA) stage – is usually the best option, and this application note will describe such an external LNA made with NXP’s Automotive-Qualified BFU520W RF Transistor.

<table>
<thead>
<tr>
<th>Option</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Improve antenna efficiency</td>
<td>Limited by space &amp; cost</td>
</tr>
<tr>
<td>Increase transmitter output power</td>
<td>Limited or constrained by battery life &amp; government regulations</td>
</tr>
<tr>
<td>Improve RKE receiver IC noise figure / sensitivity</td>
<td>Limited by CMOS process</td>
</tr>
<tr>
<td>Add an external Low Noise Amplifier (LNA) between antenna and RKE IC</td>
<td>Most cost-effective option with minimal increase in current consumption</td>
</tr>
</tbody>
</table>

Fig 4. Options and constraints for increasing RKE system range

Friis’ formula in Figure 5, describes how overall receiver noise figure of a multi-stage system is calculated. Reducing overall or ‘cascaded receiver noise figure’ will increase the range of the radio link. Per Friis’ formula, placing a low noise gain stage with Noise Factor $F_1$ and Gain $G_1$ in front of the RKE receiver block will reduce the noise figure contribution of subsequent stages by the reciprocal of the first stage’s gain value. If the Noise Figure of the external LNA in front of the receiver chain is sufficiently low, and LNA gain sufficiently high, a significant improvement in overall receiver sensitivity and range can be achieved. The trade-off, or price paid for using this option, is some reduction in the ability of the receiver chain to handle large signals or ‘jammers’ due to the additional gain added, as well as some increase in power consumption due to the addition of the external LNA stage. The general rule-of-thumb for receiver design is, for the first LNA stage, use as low of a noise figure LNA as reasonably possible, with just enough gain such that this first stage ‘dominates’ or ‘sets’ the overall receiver noise figure. Avoiding use of excessive gain will minimize the adverse impact on large signal handling ability for.
the receiver. There are various software tools available for making these calculations and trade-offs, ranging from simple spreadsheets to more sophisticated system simulation software tools.

\[
F_{\text{total}} = \frac{F_1}{G_1} + \frac{F_2}{G_1G_2} + \frac{F_3 - 1}{G_1G_2G_3} + \ldots + \frac{F_n - 1}{G_1G_2\ldots G_{n-1}}
\]

**Fig 5.** Friis’ formula for calculation of overall or ‘cascaded’ noise figure of a receiver.

In testing with Automotive RKE Receiver and Transceiver IC’s, it has been shown that overall receiver sensitivity can be improved by 3 to 5 dB by using the BFU520W external LNA described in this Applications Note.

### 2.1 BFU520W External Automotive RKE LNA Design Targets

The ‘ideal’ LNA should have some or all of the following characteristics:

1. The active device should be AEC-Q101 qualified
2. Gain 13 - 15 dB with Noise Figure < 2 dB
3. Ability to adjust / increase or decrease Gain
4. Low-cost
5. Operate at 5 volts (in-vehicle) or over 1.9 – 3.6 volts (from keyfob battery)
6. Low current consumption
7. Adjustable operating current to optimize linearity, Gain, etc.
8. Unconditional Stability ("K" > 1 and B_1 > 0; or μ_1 > 1.0)
9. Wide Bandwidth to cover 315, 434 & 868 MHz bands with same design; broadband matching will also make LNA serve as a good termination for adjacent circuit blocks (filters, switches, antennas, etc.)
10. Simple, flexible forgiving design insensitive to PC board layout variation

Using the BFU520W RF Transistor in a resistive-feedback configuration achieves the above listed goals. The LNA Gain may be increased or decreased somewhat by varying the amount of negative feedback used – simply by changing the value of the RF feedback resistor. Gain and Linearity may also be increased by increasing LNA operating current via changing the value of the bias resistor(s). This process is described in a later section of this document. Note, no chip inductors are used to further reduce cost – only resistors and capacitors are required.

Two variants of this basic LNA design are described in subsequent sections. Please refer to **Figure 6.** The “Type 1” refers to the in-vehicle external LNA, which runs from a 5 volt regulated power supply; the “Type 2” has its bias resistor values changed to be able to operate off of a keyfob battery – i.e. 3.6 V when the keyfob’s battery is fully charged, down to 1.9 V when the battery is near the end of its life.
The schematic diagram and Bill Of Material (BOM) for each of the two LNA variants are shown in later sections.

2.2 PC Board Details, both LNA Types

The same printed circuit board is used for both Type 1 and Type 2 LNA’s. Figure 7 gives a cross-sectional diagram of the circuit board, and Figures 8 and 9 provide photos of the assembled board. Standard, low-cost FR4 PC board material is used along with ‘0402’ case size passives. Note, for the circuits shown in this Applications Note, only ‘E12’ series component values are used. 50 ohm microstrip traces are used for the input and output transmission lines. Via holes are 0.2 mm / 0.008 inch diameter drill size, before metal plating of through-holes. The total board thickness of ~ 0.060 inch / 1.65 mm makes the circuit board stiff enough to reduce flexing or bending, helping to prevent damage or cracking to components mounted to the PCB.
Fig 8. Photo of assembled BFU520W LNA PC board. Measurement reference planes for data shown in this document are as shown at RF connectors unless otherwise indicated.

Fig 9. Close-in photo of PC board with ‘0402’ case-size resistors and capacitors used.
3. Typical Application Board Test Results, “Type 1” LNA (5 Volts, 3.5mA)

This section presents results of a typical “Type 1” BFU520W LNA, designed for placement in a vehicle and running from a fixed 5 volt power supply. Measurement results are summarized in Figure 10. The Schematic Diagram and Bill of Material (BOM) are given in Figures 11 and 12, respectively. Parts placement is shown in Figure 13.

\[(T=25 \, ^{\circ}C, \text{network analyzer source power} \approx -30 \, \text{dBm}, V_{CC} = 5.0 \, \text{V}, V_{CE} = 3.2 \, \text{V}, I_c=3.5 \, \text{mA}, Z_i=Z_o=50 \, \Omega)\]

<table>
<thead>
<tr>
<th>Frequency (MHz)</th>
<th>dB(s11)</th>
<th>dB(s21)</th>
<th>dB(s12)</th>
<th>dB(s22)</th>
<th>* NF</th>
<th>IIP3</th>
<th>OIP3</th>
<th>IP1dB</th>
<th>OP1dB</th>
</tr>
</thead>
<tbody>
<tr>
<td>315</td>
<td>-10.2</td>
<td>15.2</td>
<td>-21.5</td>
<td>-11.5</td>
<td>1.5</td>
<td>-13.2</td>
<td>+2.0</td>
<td>-21.9</td>
<td>-6.7</td>
</tr>
<tr>
<td>434</td>
<td>-10.5</td>
<td>14.6</td>
<td>-21.3</td>
<td>-11.4</td>
<td>1.5</td>
<td>-13.4</td>
<td>+1.2</td>
<td>-21.7</td>
<td>-7.1</td>
</tr>
<tr>
<td>868</td>
<td>-11.7</td>
<td>11.9</td>
<td>-20.1</td>
<td>-10.3</td>
<td>1.5</td>
<td>---</td>
<td>---</td>
<td>---</td>
<td>---</td>
</tr>
<tr>
<td>915</td>
<td>-11.8</td>
<td>11.6</td>
<td>-19.9</td>
<td>-10.2</td>
<td>1.4</td>
<td>---</td>
<td>---</td>
<td>---</td>
<td>---</td>
</tr>
</tbody>
</table>

* does not extract PCB loss
If PCB and RF connector loss (at input) were extracted, noise figure would be \(\sim 0.1 \, \text{dB lower}\)

Fig 10. Summary of measurement data for “Type 1” BFU520W LNA.

**BFU520W Wideband RKE LNA**

- 5 volt “Type 1” implementation
- Using only “E12” Series Values for Passive Components
- 9 ‘external’ components
- 5 x ‘C’; 4 x ‘R’; no inductors used

\[V_{cc} = 5.0V\]

\[I = 3.5 \, mA\]

\[R3 = 100K\Omega\]

\[R4 = 680 \, \text{ohms}\]

\[R2 = 470 \, \text{ohms}\]

\[C5 = 0.1uF\]

\[C4 = 390pF\]

\[C3 = 390pF\]

\[C2 = 390pF\]

\[C1 = 390pF\]

\[Q1 (SOT323)\]

\[J3 DC Connector\]

\[J2 RF OUTPUT\]

\[J1 RF INPUT\]

\[PCB = 520W-150701 \, \text{Rev A}\]

\[PC Board Material = \text{Standard FR4}\]

\[50 \, \text{ohm trace}\]

\[50 \, \text{ohm trace}\]

\[50 \, \text{ohm trace}\]

\[50 \, \text{ohm trace}\]

Value of R3 is main control on LNA current; if value of R3 ↑, LNA current ↓

R4 sets amount of RF feedback in amplifier. If value of R4 ↑, amount of feedback ↓ and vice-versa

Fig 11. Schematic Diagram, “Type 1” BFU520W LNA.
<table>
<thead>
<tr>
<th>Reference</th>
<th>Value</th>
<th>Function / Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Q1</td>
<td>-</td>
<td>Active Device, NXP BFUS520W RF wideband Transistor, AEC-Q101 Qualified, SOT323 package</td>
</tr>
<tr>
<td>C1, C2</td>
<td>390 pF</td>
<td>Input &amp; Output DC blocks</td>
</tr>
<tr>
<td>C3</td>
<td>390 pF</td>
<td>DC block for RF feedback path (collector-to-base)</td>
</tr>
<tr>
<td>C4</td>
<td>390 pF</td>
<td>Power supply decoupling, collector</td>
</tr>
<tr>
<td>C5</td>
<td>0.1 μF</td>
<td>Low-frequency decoupling</td>
</tr>
<tr>
<td>R1</td>
<td>68 Ω</td>
<td>DC bias resistor; provides some DC negative feedback to DC operating point to compensate for changes in Q1’s hFE over temperature, etc.</td>
</tr>
<tr>
<td>R2</td>
<td>470 Ω</td>
<td>&quot;RF Choke&quot; for collector (RF output) of Q1; lower cost than an inductor Trade-off: &quot;softer&quot; Gain Compression curve, some loss of linearity</td>
</tr>
<tr>
<td>R3</td>
<td>100 kΩ</td>
<td>DC bias feed to base of Q1; primary resistor for setting Q1 collector current; adjust Ic by varying this value</td>
</tr>
<tr>
<td>R4</td>
<td>680 Ω</td>
<td>RF negative feedback to amp; when value of R4 ↓, amount of feedback is increased &amp; vice-versa. Can decrease LNA gain by making value of R4↓. But, more feedback makes Noise Figure ↑</td>
</tr>
</tbody>
</table>

**Fig 12. Bill Of Material (BOM, for Type 1 BFU520W RKE LNA (5 volt, 3.2mA variant).**

**Fig 13. Parts placement on PC Board for both “Type 1” and “Type 2” BFU520W LNAs.**
3.1 S-Parameters, Stability Factor and Noise Figure, “Type 1” LNA

Figures 14 – 17 show broadband (25 MHz – 6 GHz) S-parameters for “Type 1” (5 volt, 3.5mA) BFU520W RKE LNA, taken at T=25ºC. Figure 18 shows Stability Factor µ1, which verifies Unconditional Stability over 25 MHz – 6 GHz. Lastly, LNA Noise Figure is given in Figure 19.

Fig. 14 BFU520W “Type 1” RKE LNA, Input Return Loss. Note broadband / wideband matching.

Fig. 15 BFU520W “Type 1” RKE LNA, Forward Gain
Fig 16. BFU520W “Type 1” RKE LNA, Reverse Isolation

Fig 17. BFU520W “Type 1” RKE LNA, Output Return Loss. Note broadband / wideband matching.
Fig 18. BFU520W “Type 1” RKE LNA, Stability Factor “$\mu_1$”. $\mu_1 > 1.0$ is a necessary and sufficient condition for Unconditional Stability. LNA is Unconditionally Stable over 25 MHz – 6 GHz.

Fig 19. BFU520W “Type 1” RKE LNA, Noise Figure for $T = 25^\circ$C.
4. Typical Application Board Results, “Type 2” LNA for Keyfob Applications (1.9 – 3.6 Volts, 3.2 mA @ 2.5 V)

This section shows results of a typical “Type 2” BFU520W RKE LNA, used for placement in a Keyfob, running from a coin cell battery. Intended voltage range is ~ 3.6 volts when battery is fully charged, down to ~ 1.9 volts at end of battery life. Data for T=25ºC, for both 315 & 433 MHz is summarized at 1.9, 2.5, 3.0 and 3.6 volts in Figure 20. Schematic Diagram & BOM are given in Figures 21 & 22. Note, only resistor value changes are needed moving from Type 1 => Type 2 configuration.

<table>
<thead>
<tr>
<th>Frequency (MHz)</th>
<th>Supply Voltage ( V_{CC} ) (Volts)</th>
<th>LNA Current (mA)</th>
<th>LNA ( V_{cc} ) (Volts)</th>
<th>( \text{dB}[\text{s}11] )</th>
<th>( \text{dB}[\text{s}21] )</th>
<th>( \text{dB}[\text{s}22] )</th>
<th>Noise Figure (dB)</th>
<th>Input ( P_{\text{IN}} ) (dBm)</th>
<th>Output ( P_{\text{OUT}} ) (dBm)</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>315</td>
<td>1.9</td>
<td>2.09</td>
<td>1.24</td>
<td>-6.5</td>
<td>11.7</td>
<td>-9.6</td>
<td>1.8</td>
<td>--</td>
<td>--</td>
<td>Gain, NF significantly degraded @ 1.9V / 2.1mA</td>
</tr>
<tr>
<td></td>
<td>2.5</td>
<td>3.21</td>
<td>1.49</td>
<td>-9.4</td>
<td>14.2</td>
<td>-12.7</td>
<td>1.6</td>
<td>--</td>
<td>--</td>
<td>“Nominal” operation point</td>
</tr>
<tr>
<td></td>
<td>3.0</td>
<td>4.14</td>
<td>1.70</td>
<td>-11.8</td>
<td>16.4</td>
<td>-16.0</td>
<td>1.5</td>
<td>--</td>
<td>--</td>
<td>Good Gain if battery charged</td>
</tr>
<tr>
<td></td>
<td>3.6</td>
<td>5.25</td>
<td>1.94</td>
<td>-14.8</td>
<td>16.5</td>
<td>-18.4</td>
<td>1.5</td>
<td>--</td>
<td>--</td>
<td>Good Gain if battery charged</td>
</tr>
<tr>
<td>433</td>
<td>1.9</td>
<td>2.09</td>
<td>1.24</td>
<td>-6.7</td>
<td>11.2</td>
<td>-9.8</td>
<td>1.8</td>
<td>-22.8</td>
<td>-12.6</td>
<td>Gain, NF significantly degraded @ 1.9V / 2.1mA</td>
</tr>
<tr>
<td></td>
<td>2.5</td>
<td>3.21</td>
<td>1.49</td>
<td>-9.7</td>
<td>13.5</td>
<td>-12.6</td>
<td>1.6</td>
<td>-21.5</td>
<td>-9.0</td>
<td>“Nominal” operation point</td>
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<tr>
<td></td>
<td>3.0</td>
<td>4.14</td>
<td>1.70</td>
<td>-11.9</td>
<td>14.7</td>
<td>-16.1</td>
<td>1.5</td>
<td>-20.4</td>
<td>-6.7</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3.6</td>
<td>5.25</td>
<td>1.94</td>
<td>-14.5</td>
<td>15.6</td>
<td>-17.9</td>
<td>1.5</td>
<td>-19.2</td>
<td>-4.6</td>
<td></td>
</tr>
</tbody>
</table>

Fig 20. Summary of data for “Type 2” BFU520W RKE LNA, for T=25ºC, at 1.9, 2.5, 3.0 & 3.6 Volts.

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BFU520W Wideband RKE LNA
"Type 2" Implementation
1.9 – 3.6 Volts (Keyfob) 3.2mA @ 2.5 V

Using only "E12" Series Values for Passive Components

9 'external' components
6 x 'C', 4 x 'R'; no inductors used

V_{cc} = 1.9 to 3.6 V (Keyfob battery)

J1 DC Connector

R1 22 ohms (0402)
C4 330pF (0402)

J2 RF OUTPUT

C2 390pF (0402)
R2 330 ohms (0402)
Q1 (50T323)
BFU520W AEQ-Q101
RF Wideband Transistor

PCB = 520W-150701 Rev A
PC Board Material = Standard FR4

Value of R3 is main control on LNA current; if value of R3 ↓, LNA current ↑
R4 sets amount of RF feedback in amplifier. If value of R4 ↓, amount of feedback ↓, and vice versa

Fig 21. Schematic Diagram, “Type 2” BFU520W RKE LNA, intended for keyfob.
### 4.1 Supply current, S-Parameters, Stability Factor and Noise Figure over battery voltage (~ 3.6 volts down to ~ 1.9 volts) “Type 2” LNA

Data for the “Type 2” Keyfob-type BFU520W RKE LNA is presented in this section, showing how the LNA operating current and RF parameters vary over battery voltage at T=25°C. The main conclusions are as follows:

1) **LNA current** varies from ~ 2.1 – 5.3mA over 1.9 – 3.6 volt battery voltage range
2) **Gain** stays between 14 – 17 dB at 315 / 433 MHz for 2.5 – 3.6 volts; Gain drops to 11 – 12 dB at 1.9 volts
3) **Noise Figure** remains fairly constant between 2.5 – 3.6 volts, with ~ 0.3 dB degradation at 1.9 volts
4) **Unconditional Stability** is maintained across entire 1.9 – 3.6 volt range
5) **Impedance Match** degrades as supply voltage (and thus LNA current) decreases, with the most severe change occurring from 2.5 => 1.9 volts

**Figure 23** shows LNA current variation over battery voltage range. **Figure 24** shows LNA Input Return Loss vs voltage, and **Figure 25, 26 & 27** show Gain, Reverse Isolation, and Output Return Loss vs. voltage, respectively. The stability factor $\mu_1$ of the amplifier at each of the four voltages tested is shown in **Figures 28 – 31**. Noise Figure at each test voltage is shown in **Figures 32 – 35**.
Fig 23. Variation in LNA current & collector-emitter voltage vs. battery voltage for a “Type 2” BFU520W RKE Keyfob LNA.

<table>
<thead>
<tr>
<th>Supply Voltage $V_{CC}$ (Volts)</th>
<th>LNA Current (mA)</th>
<th>BFU520W $V_{CE}$ (Volts)</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.9</td>
<td>2.09</td>
<td>1.24</td>
<td></td>
</tr>
<tr>
<td>2.5</td>
<td>3.21</td>
<td>1.49</td>
<td>Nominal, ‘set point’</td>
</tr>
<tr>
<td>3.0</td>
<td>4.14</td>
<td>1.70</td>
<td></td>
</tr>
<tr>
<td>3.6</td>
<td>5.25</td>
<td>1.94</td>
<td></td>
</tr>
</tbody>
</table>

Fig 24. Input Return Loss vs Battery Voltage for a “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz. Note, input match degrades as voltage decreases.
Fig 25. Gain vs Battery Voltage for a “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz. We see a total gain variation of ~4.5 dB at 315 or 433 MHz as voltage decreases from 3.6 V down to 1.9V.

Fig 26. Reverse Isolation vs Battery Voltage for a “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.
Fig 27. Output Return Loss vs Battery Voltage for a “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.

Fig 28. Stability factor $\mu_1$ at 1.9 volts, “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.
Fig 29. Stability factor $\mu_1$ at 2.5 volts, “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.

Fig 30. Stability factor $\mu_1$ at 3.0 volts, “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.
Fig 31. Stability factor $\mu_1$ at 3.6 volts, “Type 2” BFU520W RKE LNA, for 25 MHz – 6 GHz.

Fig 32. Noise Figure at 1.9 volts, “Type 2” BFU520W RKE LNA
Fig 33. Noise Figure at 2.5 volts, “Type 2” BFU520W RKE LNA

Fig 34. Noise Figure at 3.0 volts, “Type 2” BFU520W RKE LNA
5. Information on BFU520W RKE LNA Customer Evaluation Kits

There are two (2) types of fully populated and tested BFU520W RKE LNA Customer Evaluation Kits available, one for each of the “Type 1” and “Type 2” demos described in this applications note. Each kit has a PC board and some extra BFU520W device samples in it. Please reference the information below when placing an order for a Customer Evaluation Kit through your NXP Sales Representative.

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
<th>Artf</th>
<th>OM-</th>
<th>12NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>BFU520W-RKE1</td>
<td>Type 1 In-vehicle (5 volt, 3.5mA)</td>
<td>157556</td>
<td>OM17022</td>
<td>12NC 934070272598</td>
</tr>
<tr>
<td>BFU520W-RKE2</td>
<td>Type 2 For Keyfob (1.9 – 3.6 volts, 3.2mA @ 2.5 Volts)</td>
<td>162023</td>
<td>OM17023</td>
<td>12NC 934070273598</td>
</tr>
</tbody>
</table>
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