

AN11852

BGU8062 bypass LNA delta gain

Rev. 1 — 14 June 2016

Application note

Document information

Info	Content
Keywords	BGU8062, Delta Gain,
Abstract	This application note describes the calculation and limits of the delta gain between LNA and By-pass mode. That can be used to set the AGC algorithms of a receiver system.
Contact information	For more information, please visit: http://www.nxp.com



Revision history

Rev	Date	Description
1	20160614	First publication

Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

1. Introduction

NXP semiconductors BGU8062 is a high performance integrated low noise amplifier with bypass function. The BGU8062 operates from 1500 MHz to 2700 MHz .The BGU8062 is ideal as 3rd stage amplifier in the Rx chain for wireless infrastructure application. Its bypass function enables higher dynamic range.

Full description of the device and its evaluation board is given in [AN11688](#).

This application note gives the calculation and limits of the delta gain between the LNA- and by-pass mode of the BGU8062.

This delta gain can be used to set the systems AGC algorithm.

The BGU8062 performance information is available in the BGU8062 [datasheet](#).

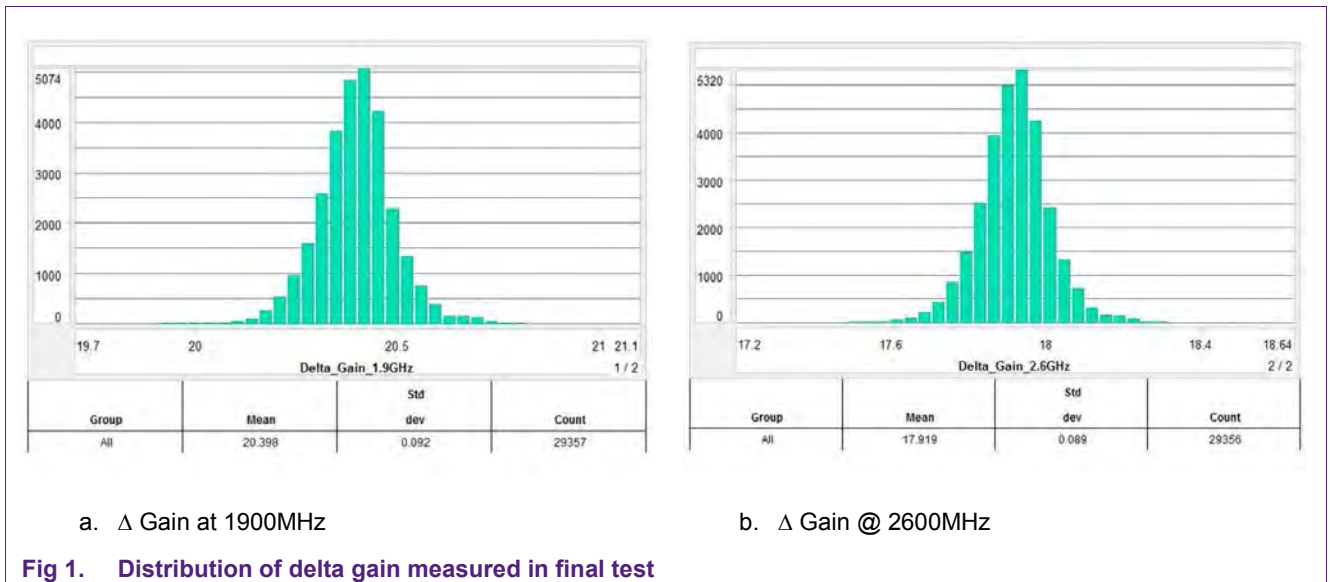
1.1 Calculation of the delta gain limits.

In order to come to the delta gain limits of the BGU8062 we did the following.

In final test LNA gain as well as Bypass loss is measured at 2 frequencies. 1.9GHz and 2.6 GHz. The distribution of the calculated delta gain at these 2 frequencies is taken as the process spread for delta gain. With $\pm 6\sigma$. See chapter 2. In addition to this we take the Gain variation over temperature for LNA and Bypass mode see chapter 3 to get to the limits for delta gain, see chapter 4

2. Distribution of the Delta Gain in final test.

The BGU8062 is final tested at the most important RF parameters at the primary frequencies of the RF band that are in the working frequency range of the BGU8062. For the delta gain the Gain data in LNA mode and the Gain data of the by-pass mode are used. These parameters are being measured at 1900MHz and 2600MHz. In the graphs below you can find the distribution of the calculated Delta gain.



For the Final test limits we use $\pm 6\sigma$ to the mean value.

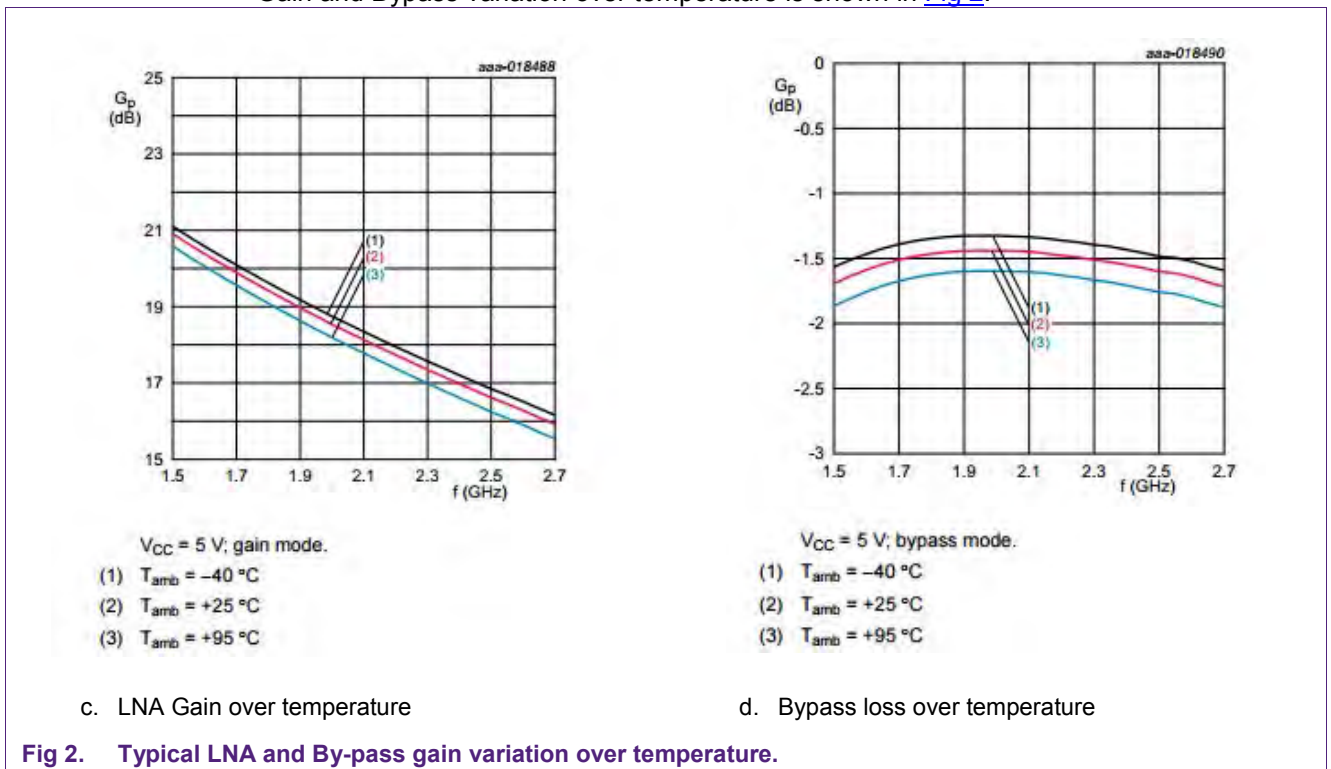
Table 1. Final test limits for Δ Gain with 6 sigma
 Measured at $V_{CC}=5V$, $T=25^{\circ}C$

Frequency	Mean	LSL	USL	STDEV
1900MHz	20.4	19.8	21.0	0.1
2600MHz	17.9	17.3	18.5	0.1

3. Gain and Bypass Loss variation over temperature.

During the release of the BGU8062 the device has been characterized over temperature. At $-40^{\circ}C$, 25° and $95^{\circ}C$. The typical variation over temperature can be found in the product datasheet.

Gain and Bypass variation over temperature is shown in [Fig 2](#).



Subtracting the By-pass gain (loss) from the LNA gain results in the Δ Gain variation over temperature. See [Fig 3](#)

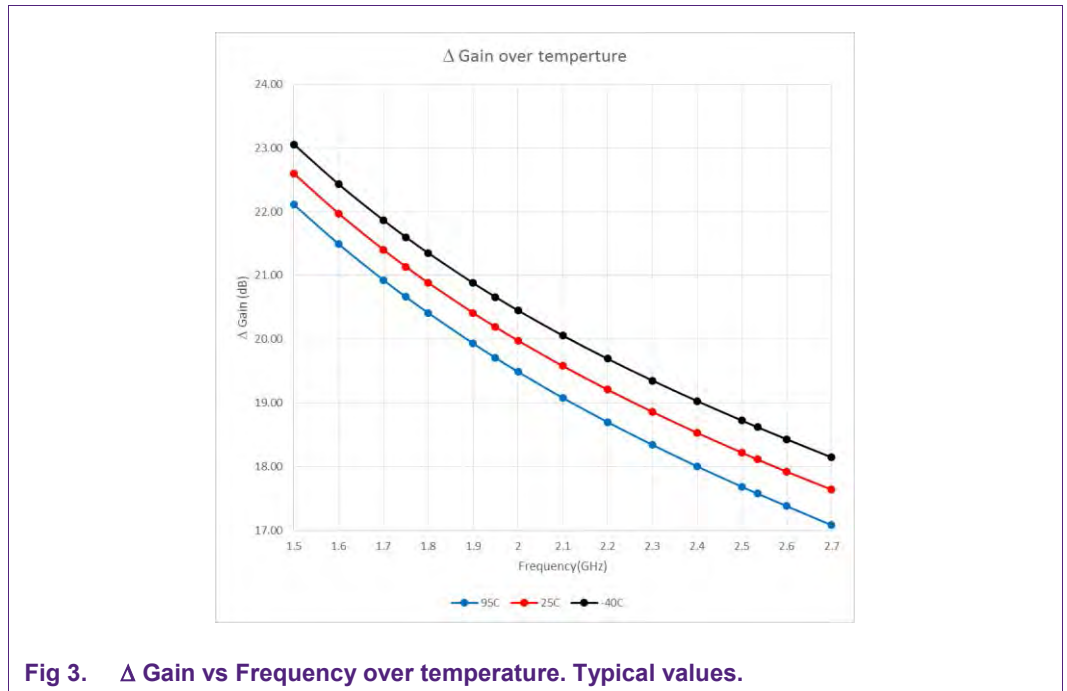


Fig 3. Δ Gain vs Frequency over temperature. Typical values.

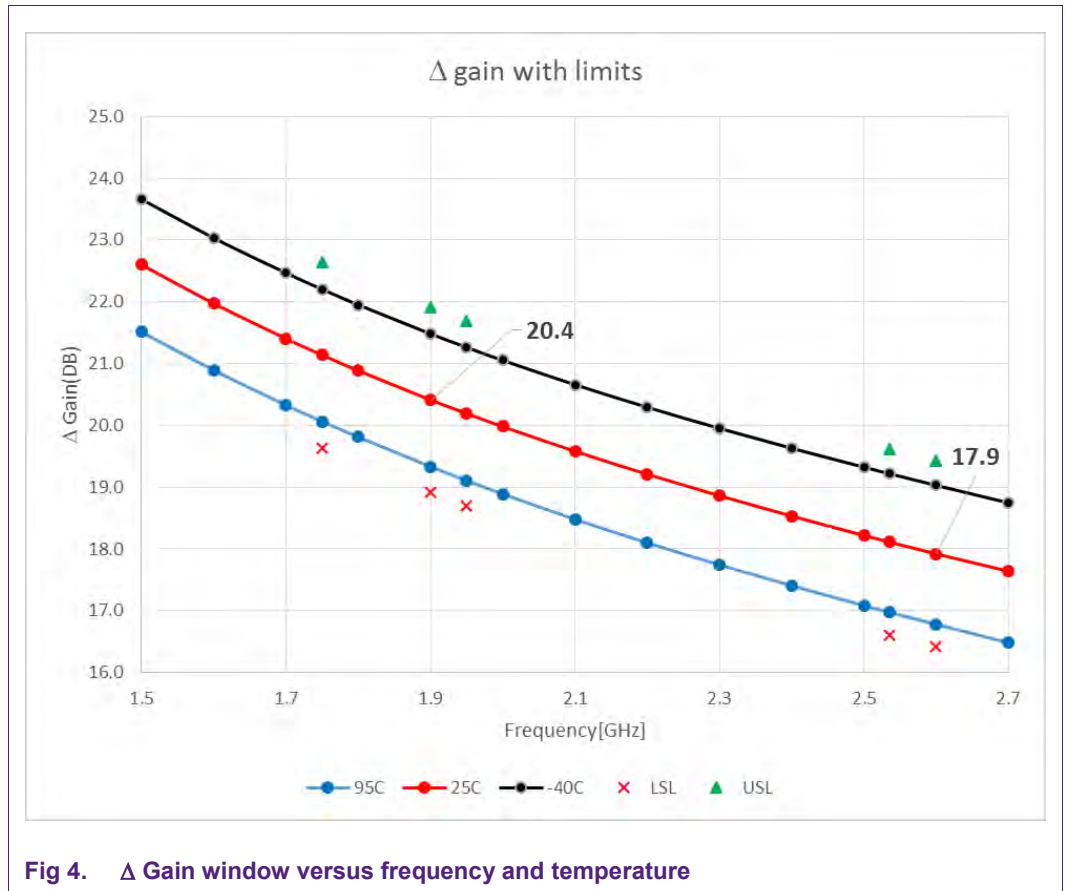
For the primary frequencies this results in the values in [Table 2](#)

Table 2. Delta gain variation over temperature for the primary frequencies
Measured at Vcc=5V from -40°C to 95°C

Frequency	-40°C	25°C	95°C	Unit
1750MHz	20.7	21.1	21.6	dB
1950MHz	19.7	20.2	20.7	dB
2535MHz	17.6	18.1	18.6	dB

4. Guaranteed Delta gain window.

Combining the temperature behavior of the Delta gain with the distribution of the delta gain measured at final test at 1900MHz and 2600MHz and the $\pm 6\sigma$ we come to the following gain window over frequency and temperature.



When combining the Distribution in final test [Table 1](#) with the temperature behavior [Table 2](#), we can agree the following delta gain behavior over process and temperature. See [Table 3](#) Here an additional 0.4 dB guard band has been implemented.

Table 3. Δ gain limits
Valid at Vcc=5V from -40°C to 95°C.

Frequency	Min	Typ	max	Unit	Comment
1750MHz	19.6	21.1	22.6	dB	
1900MHz	18.9	20.4	21.9	dB	Measured in FT
1950MHz	18.7	20.2	21.7	dB	
2535MHz	16.6	18.1	19.6	dB	
2600MHz	16.4	17.9	19.4	dB	Measured in FT

5. Abbreviations

Table 4. Abbreviations

Acronym	Description
AC	Alternating Current
DC	Direct Current
ESD	Electro Static Discharge
MMIC	Monolithic Microwave Integrated Circuit
PCB	Printed Circuit Board
RF	Radio Frequency
SMD	Surface Mounted Device

6. Legal information

6.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

6.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP

Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

6.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

7. List of figures

- Fig 1. Distribution of delta gain measured in final test. 3
- Fig 2. Typical LNA and By-pass gain over temperature.....4
- Fig 3. Δ Gain vs Frequency over temperature. Typical values.5
- Fig 4. Δ Gain window versus frequency6

8. List of tables

Table 1. Final test limits for Δ Gain with 6 sigma4
Table 2. Delta gain variation over temperature for the
primary frequencies5
Table 3. Δ gain limits.....6
Table 4. Abbreviations7

9. Contents

1.	Introduction	3
1.1	Calculation of the delta gain limits.....	3
2.	Distribution of the Delta Gain in final test.....	3
3.	Gain and Bypass Loss variation over temperature.....	4
4.	Guaranteed Delta gain window.....	5
5.	Abbreviations	7
6.	Legal information	8
6.1	Definitions	8
6.2	Disclaimers.....	8
6.3	Trademarks.....	8
7.	List of figures.....	9
8.	List of tables	10
9.	Contents.....	11

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.

© NXP B.V. 2016.

All rights reserved.

For more information, visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 14 June 2016

Document identifier: AN11852