<table>
<thead>
<tr>
<th>Document information</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Info</strong></td>
<td><strong>Content</strong></td>
</tr>
<tr>
<td><strong>Keywords</strong></td>
<td>BGU8103, GPS1301M, GNSS L2-band, LNA</td>
</tr>
<tr>
<td><strong>Abstract</strong></td>
<td>This document explains the BGU8103 [GPS1301M] GNSS L2-band LNA evaluation board</td>
</tr>
<tr>
<td><strong>Ordering info</strong></td>
<td>EVB’s available on request</td>
</tr>
<tr>
<td><strong>Contact information</strong></td>
<td>For more information, please visit: <a href="http://www.nxp.com">http://www.nxp.com</a></td>
</tr>
</tbody>
</table>
NXP Semiconductors

AN11912

BGU8103 [GPS1301M] GNSS L2-band LNA evaluation board

Revision history

<table>
<thead>
<tr>
<th>Rev</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>20170213</td>
<td>First publication</td>
</tr>
</tbody>
</table>

Contact information

For more information, please visit: [http://www.nxp.com](http://www.nxp.com)

For sales office addresses, please send an email to: salesaddresses@nxp.com
1. Introduction

NXP Semiconductors BGU8103, called GPS1301M according new naming convention, Global Navigation Satellite System (GNSS) L2-band Evaluation Board is designed to evaluate the performance of the GNSS LNA using:

- NXP Semiconductors GPS1301M GNSS Low Noise Amplifier
- A matching inductor
- A decoupling capacitor

NXP Semiconductors GPS1301M is a low-noise amplifier for wearable GNSS receiver applications in a plastic, leadless 6 pin, extremely thin small outline SOT1232 at 1.1 x 0.7 x 0.37mm, 0.4mm pitch. The GPS1301M features gain of 17.5 dB and a noise figure of 0.80 dB at the L1-band (~1575MHz) with an extremely low current consumption of 1.2 mA. Its sufficient linearity performance removes interference and noise from co-habitation cellular transmitters, while retaining sensitivity. The LNA components occupy a total area of approximately 2.5 mm².

In this document, the application diagram, board layout, bill of materials, and typical results are given for the GPS1301M EVB tuned for the GNSS L2-band, as well as some explanations on GNSS related performance parameters like out-of-band input third-order intercept point O_IIP3, gain compression under jamming and noise under jamming.

Fig 1. GPS1301M GNSS LNA evaluation board tuned for the GNSS L2-band
2. General description

Modern wearable applications like smartwatches suffer less from strong radio signals in the direct neighborhood, so problems like co-habitation are not likely to occur and linearity performance of a GNSS receiver may be reduced. To maximize battery life, the power consumption of a GNSS receiver implemented in a wearable application should be as low as possible.

Since the GNSS receiver needs to receive signals with an average power level of -130 dBm, sensitivity is very important. Currently there are several GNSS chipsets on the market that can be implemented in wearable applications. Although many of these GNSS ICs do have integrated LNA front ends, the noise performance, and as a result the system sensitivity, is not always adequate. The GNSS receiver sensitivity is a measure of how accurate the coordinates are calculated. The GNSS signal reception can be improved by a GNSS LNA, which improves the sensitivity by amplifying the wanted GNSS signal with a low-noise amplifier.
3. GPS1301M GNSS LNA L2-band evaluation board

The GPS1301M L2-band LNA evaluation board simplifies the RF evaluation of the GPS1301M GNSS LNA applied in a GNSS front-end, often used in mobile cell phones. The evaluation board enables testing of the device RF performance and requires no additional support circuitry. The board is fully assembled with the GPS1301M including the matching components required to tune the GPS1301M for the L2-band (~1230MHz) and decoupling capacitor.

The matching of this board is optimized for the L2-band (~1230MHz). This board can also be used to check the performance at the L5-band (~1180MHz).

The board is supplied with two SMA connectors for input and output connection to RF test equipment. The GPS1301M can operate from a 1.5 V to 3.1 V single supply and consumes typical 1.2 mA.

3.1 Application Circuit

The circuit diagram of the evaluation board is shown in Fig 2. With jumper JU1 the enable input can be connected either to Vcc or GND.

![Circuit diagram of the GPS1301M LNA evaluation board tuned for the GNSS L2-band](image-url)
3.2 PCB Layout

![Printed-Circuit Board layout of the GPS1301M LNA evaluation board](image)

A good PCB layout is an essential part of an RF circuit design. The LNA evaluation board of the GPS1301M can serve as a guideline for laying out a board using the GPS1301M. Use controlled impedance lines for all high frequency inputs and outputs. Bypass Vcc with decoupling capacitors, preferably located as close as possible to the device. For long bias lines it may be necessary to add decoupling capacitors along the line further away from the device. Proper grounding of the GND pins is also essential for good RF performance. Either connect the GND pins directly to the ground plane or through vias, or do both, which is recommended. The material that has been used for the evaluation board is FR4 using the stack shown in Fig 4.

![Stack of the PCB material](image)

(1) Material supplier is ISOLA DURAVER; \(\varepsilon_r = 4.6-4.9; T\delta = 0.02\)
### 3.3 Bill of materials

<table>
<thead>
<tr>
<th>Designator</th>
<th>Description</th>
<th>Footprint</th>
<th>Value</th>
<th>Supplier Name/type</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>GPS1301M</td>
<td>1.1 x 0.7 x 0.37mm³, 0.4mm pitch</td>
<td>NXP</td>
<td>SOT1232</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PCB</td>
<td>20 x 35mm</td>
<td>GPS1301M GNSS LNA EV Kit</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>C1</td>
<td>Capacitor</td>
<td>0402</td>
<td>1 nF</td>
<td>Murata GRM1555</td>
<td>Decoupling</td>
</tr>
<tr>
<td>C2</td>
<td>Capacitor</td>
<td>0402</td>
<td>1.5 pF</td>
<td>Murata GRM1555</td>
<td>Input matching</td>
</tr>
<tr>
<td>L1</td>
<td>Inductor</td>
<td>0402</td>
<td>22 nH</td>
<td>Murata LQW15</td>
<td>Input matching</td>
</tr>
<tr>
<td>L2</td>
<td>Inductor</td>
<td>0402</td>
<td>12 nH</td>
<td>Murata LQP15</td>
<td>Output matching</td>
</tr>
<tr>
<td>X1, X2</td>
<td>SMA RD connector</td>
<td>-</td>
<td>-</td>
<td>Johnson, End launch SMA 142-0701-841</td>
<td>RF input/ RF output</td>
</tr>
<tr>
<td>X3</td>
<td>DC header</td>
<td>-</td>
<td>-</td>
<td>Molex, PCB header, Right Angle, 1 row, 3 way 90121-0763</td>
<td>Bias connector</td>
</tr>
<tr>
<td>X4</td>
<td>JUMPER Stage</td>
<td>-</td>
<td>-</td>
<td>Molex, PCB header, Vertical, 1 row, 3 way 90120-0763</td>
<td>Connect Ven to Vcc or separate Ven voltage</td>
</tr>
<tr>
<td>JU1</td>
<td>JUMPER</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### 3.4 GPS1301M product description

NXP Semiconductors’ GPS1301M GNSS low noise amplifier is designed for the GNSS frequency band. The integrated biasing circuit is temperature stabilized, which keeps the current constant over temperature. It also enables the superior linearity performance of the GPS1301M. The GPS1301M is also equipped with an enable function that allows it to be controlled via a logic signal. In disabled mode it consumes less than 1 μA.

The output of the GPS1301M is internally matched for 1575.42 MHz whereas only one series inductor at the input is needed to achieve the best RF performance. Both the input and output are AC coupled via an integrated capacitor.

It requires only two external components to build a GNSS LNA having the following advantages:

- Low noise
- System optimized gain
- High linearity under jamming
- 1.1 x 0.7 x 0.37, 0.4mm pitch: SOT1232
- Low current consumption
- Short power settling time
3.5 Series inductor

The evaluation board is supplied with Murata LQW15 series inductor of 22 nH at the input. This is a wire wound type of inductor with high quality factor (Q) and low series resistance (Rs) (see Table 2). This type of inductor is recommended in order to achieve the best noise performance. High Q inductors from other suppliers can be used. If it is decided to use other low cost inductors with lower Q and higher Rs the noise performance will degrade.

Table 2. Series Inductor options

<table>
<thead>
<tr>
<th>Type</th>
<th>Murata</th>
<th>Size 0201</th>
<th>Size 0402</th>
<th>Size 0603</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Multilayer</td>
<td>LQG</td>
<td>15H</td>
<td></td>
<td>18H</td>
<td></td>
</tr>
<tr>
<td>Non-Magnetic Core</td>
<td></td>
<td>NF↑↑</td>
<td></td>
<td>NF↑</td>
<td></td>
</tr>
<tr>
<td>Film</td>
<td>LQP</td>
<td>03T</td>
<td>15M</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>NF↑↑</td>
<td></td>
<td>NF↑</td>
<td></td>
</tr>
<tr>
<td>Wirewound</td>
<td>LQW</td>
<td>15A</td>
<td></td>
<td>18A</td>
<td>Lowest NF</td>
</tr>
<tr>
<td>Non-Magnetic Core</td>
<td>Default</td>
<td></td>
<td></td>
<td>NF↓</td>
<td></td>
</tr>
</tbody>
</table>
4. Typical LNA evaluation board results

Table 3 gives the measured performance of the GPS1301M LNA EVB.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>LNA EVB</th>
<th>LNA EVB</th>
<th>LNA EVB</th>
<th>LNA EVB</th>
<th>Unit</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>V\text{CC}</td>
<td>1.5</td>
<td>1.8</td>
<td>2.8</td>
<td>3.1</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Supply Current</td>
<td>I\text{CC}</td>
<td>1.1</td>
<td>1.2</td>
<td>1.2</td>
<td>1.2</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Noise Figure</td>
<td>NF @ 1180 MHz</td>
<td>1.2</td>
<td>1.2</td>
<td>1.2</td>
<td>1.2</td>
<td>dB</td>
<td>[1]</td>
</tr>
<tr>
<td></td>
<td>NF @ 1230 MHz</td>
<td>1.3</td>
<td>1.3</td>
<td>1.3</td>
<td>1.3</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Power Gain</td>
<td>G\text{p} @ 1180 MHz</td>
<td>15.5</td>
<td>15.7</td>
<td>16.0</td>
<td>16.0</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td>G\text{p} @ 1230 MHz</td>
<td>15.2</td>
<td>15.4</td>
<td>15.7</td>
<td>15.8</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>RL_{in} @ 1180 MHz</td>
<td>13.0</td>
<td>13.6</td>
<td>13.9</td>
<td>14.0</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td>RL_{in} @ 1230 MHz</td>
<td>10.2</td>
<td>11.2</td>
<td>12.7</td>
<td>12.9</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>RL_{out} @ 1180 MHz</td>
<td>9.2</td>
<td>9.2</td>
<td>9.4</td>
<td>9.3</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td>RL_{out} @ 1230 MHz</td>
<td>7.7</td>
<td>7.8</td>
<td>7.8</td>
<td>7.8</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Reverse Isolation</td>
<td>ISO_{rev} @ 1180 MHz</td>
<td>35.6</td>
<td>35.7</td>
<td>35.5</td>
<td>35.5</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td>ISO_{rev} @ 1230 MHz</td>
<td>36.4</td>
<td>36.4</td>
<td>36.1</td>
<td>36.1</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Input 1dB Gain Compression</td>
<td>P_{1\text{dB}} @ 1175 MHz</td>
<td>-18.9</td>
<td>-17.3</td>
<td>-14.2</td>
<td>-13.5</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>P_{1\text{dB}} @ 1230 MHz</td>
<td>-18.3</td>
<td>-16.6</td>
<td>-13.6</td>
<td>-13.0</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Output 1dB Gain Compression</td>
<td>P_{0\text{dB}} @ 1175 MHz</td>
<td>-4.4</td>
<td>-2.6</td>
<td>0.8</td>
<td>1.5</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>P_{0\text{dB}} @ 1230 MHz</td>
<td>-3.8</td>
<td>-2.2</td>
<td>1.1</td>
<td>1.8</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Input third order intercept point</td>
<td>IIP3 @ 1170 MHz</td>
<td>-16.2</td>
<td>-16.3</td>
<td>-16.2</td>
<td>-16.2</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>IIP3 @ 1225 MHz</td>
<td>-15.9</td>
<td>-16.1</td>
<td>-15.9</td>
<td>-15.9</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Output third order intercept point</td>
<td>OIP3 @ 1170 MHz</td>
<td>-0.7</td>
<td>-0.6</td>
<td>-0.2</td>
<td>-0.2</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>OIP3 @ 1225 MHz</td>
<td>-0.7</td>
<td>-0.7</td>
<td>-0.2</td>
<td>-0.1</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Power settling time</td>
<td>T_{on}</td>
<td>&lt;2</td>
<td>&lt;2</td>
<td>&lt;2</td>
<td>&lt;2</td>
<td>µs</td>
<td></td>
</tr>
<tr>
<td></td>
<td>T_{off}</td>
<td>&lt;1</td>
<td>&lt;1</td>
<td>&lt;1</td>
<td>&lt;1</td>
<td>µs</td>
<td></td>
</tr>
</tbody>
</table>

[1] The noise figure and gain figures are measured at the SMA connectors of the evaluation board. The losses of the connectors and the PCB of approximately 0.05 dB are not subtracted. Measured at T_{amb} = 25 °C.

At the average power levels of –130 dBm that have to be received by a GNSS receiver, the system will not have in-band intermodulation problems caused by the GNSS-signal itself. Strong out-of-band cell phone TX jammers however can cause linearity problems, and result in third-order intermodulation products in the GNSS frequency band. In the next paragraphs the effects of these Jammer-signals on the Noise and Gain performance of the GPS1301M are described. The effect of these Jammers on the In-band and Out-of-Band Third-Order Intercept points are described in more detail in a separate User Manual: UM10453: 2-Tone Test BGU7005 and BGU7007 GNSS LNA.
4.1 In-band 1dB gain compression due to jammers

As stated before, signal levels in the GNSS frequency band of -130dBm average will not cause linearity problems in the GNSS band itself. This of course is also valid for the 1dB gain compression in-band. The 1dB compression point at 1225 MHz caused by cell phone TX jammers however is important.

Measurements have been carried out using the setup shown in Fig 5.

![Fig 5. 1dB Gain compression under jamming measurement setup (LNA evaluation board)](image)

The gain of the DUT was measured between port RFin and RFout of the EVB at the GNSS frequency 1225 MHz, while simultaneously a jammer power signal was swept at the 20dB attenuated input port of the Directional Coupler. Please note that the drive power of the jammer is 20 dB lower at the input of the DUT caused by the directional coupler.
The figures below show the supply-current (Icc) and gain compression curves with 915, 1463, 1695, 1850 and 2350 MHz jammers (input jammer power at LNA-board, taking into account the approx. 20 dB attenuation of the directional coupler and RF-cable from Jammer-Generator to the directional coupler).

The gain drops 1dB with approximately -18 dBm input jamming power at 915 MHz and at -15 dBm @1463 MHz (Vcc=1.8V) (Fig 7 and Fig 9). With an 1850MHz jamming signal, the 1dB gain compression occurs around -11 dBm input power level (Fig 11).
Fig 10. Icc versus jammer power at 1695 MHz

Fig 11. Gain versus jammer power at 1695 MHz

Fig 12. Icc versus jammer power at 1850 MHz

Fig 13. Gain versus jammer power at 1850 MHz
Pin 1225 MHz = -45 dBm

Fig 14. Icc versus jammer power at 2350 MHz

Pin 1225 MHz = -45 dBm

Fig 15. Gain versus jammer power at 2350 MHz
4.2 Noise figure as function of jammer power

Noise figure under jamming conditions is a measure of how the LNA behaves when e.g. a GSM TX interfering signal is at the input of the GNSS antenna. To measure this behavior the setup shown in Fig 16 is used.

The jammer signal is coupled via a directional coupler to the DUT: this is to avoid the jammer signal damaging the noise source. The GNSS BPF is needed to avoid driving the second-stage LNA in saturation.

![Fig 16. Noise under jamming measurement setup (LNA evaluation board)](image)

With the results of these measurements and the specification of the SAW filter, the jammer power levels that cause noise increase can be calculated.

As can be seen in Fig 17, with a 915 MHz jammer the NF of the LNA starts to increase at $P_{\text{jam}} = -25$ dBm (input jammer power at LNA-board, taking into account the approx. 20 dB attenuation of the directional coupler and RF-cable from Jammer-Generator to the directional coupler). For the 3400 MHz jammer the NF of the LNA starts to increase at $P_{\text{jam}} = -12$ dBm (see Fig 23).
Fig 17. NF versus jammer power at 915 MHz

Fig 18. NF versus jammer power at 1463 MHz

Fig 19. NF versus jammer power at 1695 MHz

Fig 20. NF versus jammer power at 1850 MHz
Fig 21. NF versus jammer power at 2350 MHz

Fig 22. NF versus jammer power at 2600 MHz

Fig 23. NF versus jammer power at 3400 MHz

Fig 24. NF versus jammer power at 5150 MHz
The figures below show the noise figure (NF) related to the jamming frequency at different voltage supplies. The influence of the jammer decreases as the frequency separation between the L2-band and the jammer increases.

**Pin 1225 MHz = -30 dBm**

**Fig 25.** Gain versus jammer frequency at Vcc = 1.8 V

**Fig 26.** Gain versus jammer frequency at Vcc = 2.8 V
5. Required Equipment

In order to measure the evaluation board the following is necessary:

- DC Power Supply up to 30 mA at 1.5 V to 3.1 V
- Two RF signal generators capable of generating RF signals at the operating frequency of 1225 MHz, as well as the jammer frequencies 915 MHz up to 5150 MHz
- An RF spectrum analyzer that covers at least the operating frequency of 1225 MHz as well as a few of the harmonics. Up to 6 GHz should be sufficient.
  "Optional" a version with the capability of measuring noise figure is convenient
- Amp meter to measure the supply current (optional)
- A network analyzer for measuring gain, return loss and reverse isolation
- Noise figure analyzer and noise source
- Directional coupler
- Proper RF cables

6. Connections and setup

The GPS1301M GNSS LNA evaluation board is fully assembled and tested. Please follow the steps below for a step-by-step guide to operate the LNA evaluation board and testing the device functions.

1. Connect the DC power supply to the Vcc and GND terminals. Set the power supply to the desired supply voltage, between 1.5 V and 3.1 V, but never exceed 3.1 V as it might damage the GPS1301M.
2. Jumper JU1 is connected between the Vcc terminal of the evaluation board and the V_en pin of the GPS1301M.
3. Connect the RF signal generator and the spectrum analyzer to the RF input and the RF output of the evaluation board, respectively. Do not turn on the RF output of the signal generator yet, set it to -45 dBm output power at 1225 MHz, set the spectrum analyzer at 1225 MHz center frequency and a reference level of 0 dBm.
4. Turn on the DC power supply and it should read approximately 1.2 mA.
5. Enable the RF output of the generator: The spectrum analyzer displays a tone around –29.3 dBm at 1225 MHz.
6. Instead of using a signal generator and spectrum analyzer one can also use a network analyzer in order to measure gain as well as in- and output return loss.
7. For noise figure evaluation, either a noise figure analyzer or a spectrum analyzer with noise option can be used. The use of a 5 dB noise source, like the Agilent 364B is recommended. When measuring the noise figure of the evaluation board, any kind of adaptors, cables etc. between the noise source and the evaluation board should be minimized, since this affects the noise figure.
Fig 27. Evaluation board including its connections
7. Legal information

7.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

7.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer’s own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer’s sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer’s applications and products planned, as well as for the planned application and use of customer’s third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer’s applications or products, or the application or use by customer’s third party customer(s). Customer is responsible for doing all necessary testing for the customer’s applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer’s third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Evaluation products — This product is provided on an “as is” and “with all faults” basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out of the use or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer’s exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

7.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.
8. List of figures

Fig 1. GPS1301M GNSS LNA evaluation board tuned for the GNSS L2-band ........................................3
Fig 2. Circuit diagram of the GPS1301M LNA evaluation board tuned for the GNSS L2-band .5
Fig 3. Printed-Circuit Board layout of the GPS1301M LNA evaluation board .................................6
Fig 4. Stack of the PCB material........................................6
Fig 5. 1dB Gain compression under jamming measurement setup (LNA evaluation board)...10
Fig 6. Icc versus jammer power at 915 MHz............11
Fig 7. Gain versus jammer power at 915 MHz........11
Fig 8. Icc versus jammer power at 1463 MHz........11
Fig 9. Gain versus jammer power at 1463 MHz........11
Fig 10. Icc versus jammer power at 1695 MHz........12
Fig 11. Gain versus jammer power at 1695 MHz........12
Fig 12. Icc versus jammer power at 1850 MHz........12
Fig 13. Gain versus jammer power at 1850 MHz........12
Fig 14. Icc versus jammer power at 2350 MHz........13
Fig 15. Gain versus jammer power at 2350 MHz........13
Fig 16. Noise under jamming measurement setup (LNA evaluation board).................................14
Fig 17. NF versus jammer power at 915 MHz........15
Fig 18. NF versus jammer power at 1463 MHz........15
Fig 19. NF versus jammer power at 1695 MHz........15
Fig 20. NF versus jammer power at 1850 MHz........15
Fig 21. NF versus jammer power at 2350 MHz........16
Fig 22. NF versus jammer power at 2600 MHz........16
Fig 23. NF versus jammer power at 3400 MHz........16
Fig 24. NF versus jammer power at 5150 MHz........16
Fig 25. Gain versus jammer frequency at Vcc = 1.8 V 17
Fig 26. Gain versus jammer frequency at Vcc = 2.8 V 17
Fig 27. Evaluation board including its connections .....19
9. List of tables

Table 1. BOM of the GPS1301M GNSS LNA evaluation board tuned for the GNSS L2-band ...............7
Table 2. Series Inductor options .................................................8
Table 3. Typical results measured on the evaluation Board .................................................................9
10. Contents

1. Introduction .................................................................3
2. General description.....................................................4
3. GPS1301M GNSS LNA L2-band evaluation board ...........................................5
   3.1 Application Circuit .................................................5
   3.2 PCB Layout ..........................................................6
   3.3 Bill of materials ....................................................7
   3.4 GPS1301M product description ..................................7
   3.5 Series inductor .....................................................8
4. Typical LNA evaluation board results .................9
   4.1 In-band 1dB gain compression due to jammers ........................................10
   4.2 Noise figure as function of jammer power ......14
5. Required Equipment .....................................................18
6. Connections and setup ..................................................18
7. Legal information .........................................................20
   7.1 Definitions ..........................................................20
   7.2 Disclaimers ..........................................................20
   7.3 Trademarks ..........................................................20
8. List of figures .............................................................21
9. List of tables .............................................................22
10. Contents .................................................................23

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.

© NXP B.V. 2017. All rights reserved.
For more information, visit: http://www.nxp.com
For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 13 February 2017
Document identifier: AN11912