AN13815 Programming LPC800 Using USART ISP Rev. 0 — 5 May 2023

Application note

Document Information

| Information | Content |
|-------------|--|
| Keywords | AN13815, ISP, LPC553x, LPC800 |
| Abstract | This document describes how to upgrade or program the LPC800 using USART ISP |

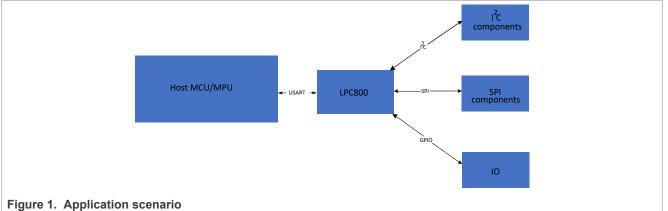


Programming LPC800 Using USART ISP

1 Introduction

The LPC800 series offers a range of low-power, space-efficient, and low-pin-count options for basic microcontroller applications.

In some application scenarios, when the pins of the main processor are insufficient or other functions must be expanded, an LPC800 is an excellent solution. As shown in <u>Figure 1</u>, the host MCU/MPU connects with LPC800 via USART, and LPC800 connects to and controls other I²C, SPI, and other components.



In addition to other functions, all LPC800 devices include ROM-based services for programming and reading flash memory. In-System Programming (ISP) works on an unprogrammed or previously programmed device using one from a selection of hardware interfaces.

This document introduces a way to program or upgrade LPC800 using USART ISP in the field.

2 LPC800 USART ISP

The ISP command handler accepts the commands listed in <u>Table 1</u>. For further information, see the corresponding *LPC8xx User Manual*.

| ISP Command | Usage |
|-------------------------------------|--|
| Unlock | U <unlock code=""></unlock> |
| Set baud rate | B <baud rate=""> <stop bit=""></stop></baud> |
| Echo | A <setting></setting> |
| Write to RAM | W <start address=""> <number bytes="" of=""></number></start> |
| Read memory | R <address> <number bytes="" of=""></number></address> |
| Prepare sectors for write operation | P <start number="" sector=""> <end number="" sector=""></end></start> |
| Copy RAM to flash | C <flash address=""> <ram address=""> <number bytes="" of=""></number></ram></flash> |
| Go | G <address> <mode></mode></address> |
| Erase sector | E <start number="" sector=""> <end number="" sector=""></end></start> |
| Erase page | X <start number="" page=""> <end number="" page=""></end></start> |
| Blank check sector | I <start number="" sector=""> <end number="" sector=""></end></start> |
| Read Part ID | J |
| Read boot code version | К |

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Table 1. ISP command...continued

| ISP Command | Usage |
|----------------------|--|
| Compare | M <address1> <address2> <number bytes="" of=""></number></address2></address1> |
| Read UID | Ν |
| Read CRC checksum | S <address> <number bytes="" of=""></number></address> |
| Read flash signature | Z |

All USART ISP commands must be sent as single ASCII strings. Strings must be terminated with Carriage Return (CR) and/or Line Feed (LF) control characters. Extra <CR> and <LF> characters are ignored. All ISP responses are sent as <CR><LF> terminated ASCII strings. Data is sent and received in plain binary format.

The host sends a '?' (0x3F) as a synchronization character and waits for a response. The host side serial port settings are 8 data bits, one stop bit, and no parity. The auto-baud routine programs the baud rate of the serial port generator by measuring the bit time of the received synchronization character in terms of its frequency. It also sends an ASCII string (Synchronized<CR><LF>) to the host. In response to this action, the host sends back the same string (Synchronized<CR><LF>).

The auto-baud routine looks at the received characters to verify synchronization. If synchronization is verified, the Synchronized<CR><LF>OK<CR><LF> string is sent to the host ("echo" is enabled by default). The host responds by sending the crystal frequency (in kHz) at which the part is running. The response is required for backward compatibility of the bootloader code and is ignored. The OK<CR><LF> string is sent to the host after receiving the crystal frequency. If synchronization is not verified, the auto-baud routine waits again for a synchronization character.

Once the crystal frequency is received, the part initializes, and the ISP command handler invokes. For safety reasons, an "Unlock" command is required before executing the commands resulting in flash erase/write operations and the "Go" command. The rest of the commands are executed without the unlock command. Execute the "Unlock" command once per ISP session.

3 ISP programming flow

The host device requires several steps to communicate and program LPC800. In short, the host device requires synchronization with LPC800, then unlock the erase/program operation, erase the flash, and finally program the flash. The following sections use flowcharts to describe this process in further detail.

The flow shown in Figure 2 provides only an example and should not be precisely adhered to. It can be adjusted suitably in practice to meet the requirements.

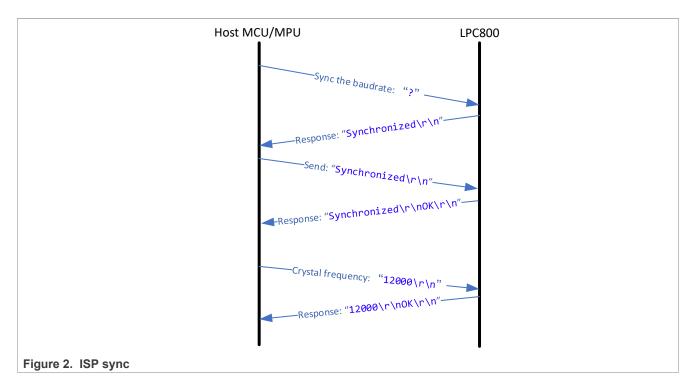
Note: The demo shown in <u>Figure 2</u> considers 64 kB flash LPC800 as an example. For different flash sizes of LPC800, it is necessary to modify the corresponding sector parameters.

3.1 Synchronize

This command is used to synchronize the baud rate between host and LPC800 via USART.

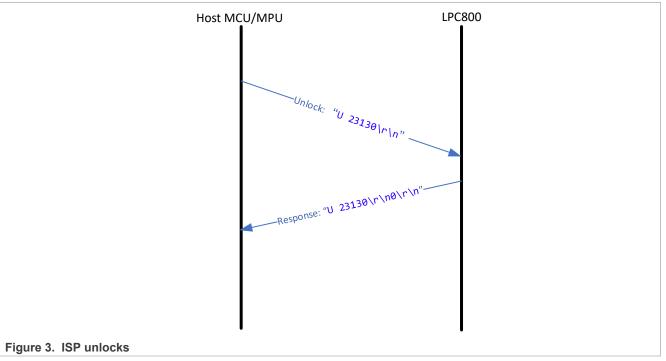
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3.2 Unlock

This command is used to unlock flash "Write", "Erase", and "Go" commands.



3.3 Erase and prepare the flash for programming

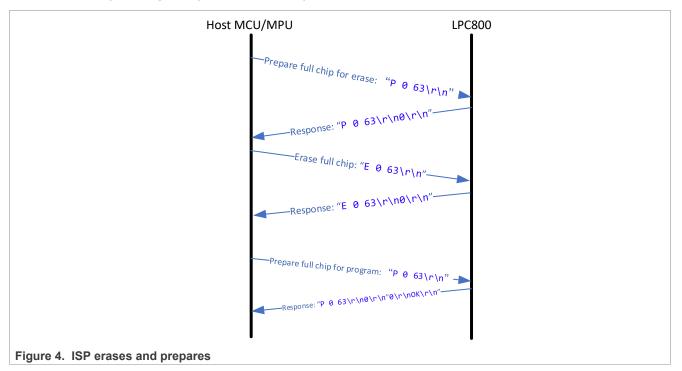
The P <Start Sector Number> <End Sector Number>\r\n is used to prepare sectors before erase and program.

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The E <Start Sector Number> <End Sector Number>\rn is used to erase one or more sectors of on-chip flash memory.

For subsequent flash programming operations, use the command P <Start Sector Number> <End Sector Number>\r\n to prepare the flash sectors again.

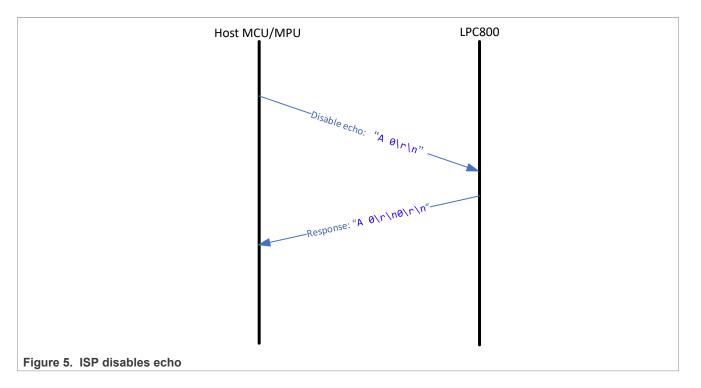




3.4 Disable the echo

The A $0 \quad r \in O$, the ISP command is ON. When ON, the ISP command handler sends the received serial data back to the host.

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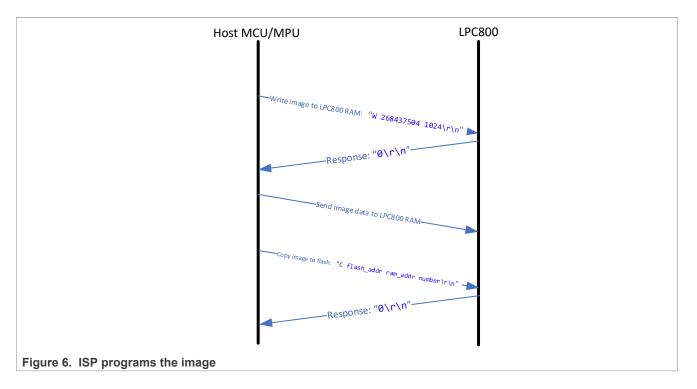
3.5 Program the image into flash

The W < Start Address < Number of Bytes r n is used to download data to RAM. After that, the host sends the plain binary code.

The C <Flash address> <RAM address> <number of bytes>\r\n is used to copy the content in RAM to the flash memory. Number of bytes to be written should be $64 \mid 128 \mid 256 \mid 512 \mid 1024$. The destination address should be a 64 byte boundary.

This process must be repeated until the entire target image is downloaded.

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4 Demonstration and example

This application note attaches a demo code that uses LPC55S36 EVK as the host device and LPCXpresso860-MAX board as the target MCU.

- 1. Import the demo example project to MCUXpresso IDE, compile, and then download into LPC55S36 EVK.
- 2. Open a serial terminal software. The default setting is 115200/8/1/N.
- 3. Connect LPC55S36 EVK and LPCXpresso860-MAX board as shown in Table 2.

 Table 2. Board-to-board connection

| LPC55S36 EVK | LPCXpresso860-MAX board | |
|--------------------|-------------------------|--|
| J7-3(P1_24 FC2 RX) | J2-4(P0_25 ISP TX) | |
| J7-4(P1_25 FC2 TX) | J2-2(P0_24 ISP RX) | |
| J7-8(GND) | J1-14(GND) | |

Note: The power supply voltage of LPC55S36 and LPC86x must be the same, that is, 3.3 V. Use the buttons on the board to force the chip into ISP mode because the LPCXpresso860-MAX board does not lead out ISP pins.

To force LPC86x into ISP mode, follow the steps below:

- a. Press $\ensuremath{\text{ISP}}$ and $\ensuremath{\text{RESET}}$ buttons at the same time.
- b. Release the **RESET** button.
- c. Release the **ISP** button.
- 4. To erase or program the device, follow the log in the terminal as shown in Figure 7.

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```
Please make sure LPC800 has entered ISP mode!!
Please input a character to erase or program LPC800, 'E' means erase
only, other character means erase and program:
1
image size is 4072, aligned size is 4096
Programming is complete, please re-enter ISP mode for further operation!
```

Figure 7. Terminal

5 Revision history

The <u>Table 3</u> lists the substantive changes done to this document since the initial release.

Table 3. Revision history

| Revision number | Date | Substantive changes | |
|-----------------|-------------|---------------------|--|
| 0 | 05 May 2023 | Initial release | |

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Legal information 6

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Date of release: 5 May 2023 Document identifier: AN13815