Abstract
This document will show how to dump, load and handle EEPROM settings of the PN76 family. By using the NXP NFC Cockpit and PN76 SDK examples.
Revision history

<table>
<thead>
<tr>
<th>Rev</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>v.1.0</td>
<td>20230818</td>
<td>Initial version</td>
</tr>
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</table>
1 Introduction

This document explains various ways how the EEPROM values can be modified, stored, and copied from and to the PN7642. What settings are correct, how to trim and correctly configure the PN76 is not part of this document. There are various dedicated application notes available how to find the best settings for your individual setup.

Even though it is referred to as EEPROM, it is located in the flash memory of the PN7642 controller. Only accessible via the EEPROM HAL (Hardware Abstraction Layer) APIs from the user.

The settings stored in this configuration EEPROM are used for a basic configuration of the controller, which does not change frequently. Typically it is performed once during the configuration of a product. For frequently changing configurations, the registers have to be used, which do not keep their value during power off or reset.

Note: Writing to the EEPROM has to be performed with Read-Modify-Write for all memory addresses that contain RFU bits. While RFU bits are not to be modified!


1.1 Environment

The environment used and referred to within this application note:

- MCUXpresso v11.7.1 (or later)
- MCUXpresso PN7642 SDK 2.12.3
- PNEV7642 Rev.B FW v1.0
- NFC Cockpit v7.2.0
- SEGGER J-Link
2 Use cases

At one point in developing a product the PN76 configuration must be adapted to its environment (PCB, antenna, power supply, enclosure, …). How to handle the EEPROM configuration the easiest way depends on the users system architecture, environment and goal.

This document explains the most common use-cases and approaches.

2.1 Developing phase

In the developing phase of a product, the configuration of the PN76 must be adapted to its unique environment. Such as housing, PCB, surrounding components, antenna, power budget and so on. This adaptation of settings is crucial for a good performance and operating within the PN76 limits.

This document does not describe the process of finding the correct configuration of the PN76. There are dedicated Application Notes, User Manuals, and Guides for different functions (DPC, ULPCD, Antenna setup, …) existing.

The goal in this phase is to find the best EEPROM configurations for your unique environment. In this process, the EEPROM is read/written multiple times and also shared with colleagues and other parties.

The easiest and fastest way is using the NNC (NXP NFC Cockpit). With the NNC, writing and reading single values and loading or dumping the whole EEPROM is easily done.

See Section 4 "NXP NFC Cockpit" for further information on NNC.

2.2 Production phase

A EEPROM configuration for the PN76 has been found and this configuration must be replicated on every PN76 chip.

The EEPROM is not accessible from the outside via a debugger or programming interface. Only via the system service APIs, called from the user application space. The EEPROM must be updated from an application.

Two options to do so are:

Table 1. EEPROM update options

<table>
<thead>
<tr>
<th>Option</th>
<th>Pro</th>
<th>Con</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Integration of the EEPROM update into the user application.</td>
<td>• Only a single application to be developed.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Only one programming cycle in production.</td>
</tr>
<tr>
<td>2</td>
<td>Dedicated application for configuration</td>
<td>• The user application can be developed independent.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• No additional flash space occupied with EEPROM configuration.</td>
</tr>
</tbody>
</table>

(1): NXP provides an example how to update the EEPROM from an application as part of its PN76 SDK. The example is described in more detail in Section 5 "MCUXpresso Example".

Those two options are described in more detail in the next chapters. How the final implementation can look like is user individual and depends on each unique system design and software architecture. Following options are only two of many and can be used as inspiration.
2.2.1 Integration in user application

The biggest benefit of this approach would be that save programming cycles in production. As a dedicated update application (Section 2.2.2 "Dedicated application") has to be programmed first, executed and after the successful execution overwritten (reprogrammed) with the user application.

On the other hand is the integration of the EEPROM update into the users application making the app itself more complex. At one point, there must be a check if the chip has been already updated or not. This logic has to be solid, as a EEPROM update (write) at every boot-up means more write-cycles and a premature flash wear-out.

![Integration in user application flowchart](image)

**Figure 1. Integration in user application flowchart**

**[1] Boot-Up**
The controller boots up and basic initializations are done.

**[2] Update EEPROM?**
Before starting the actual business logic of the user application, there must be a check if the EEPROM configuration has been updated already. This can be done, for example, by checking a value stored in the EEPROM_USER_AREA or at an empty flash page.

If an EEPROM update is necessary progress to step [3].

The EEPROM update only must be done once, as the EEPROM is persistent memory and will not be overwritten by the firmware.

**[3] Update EEPROM**
The EEPROM is updated with the provided values. This can be the whole area or only a small portion. NXP provides an example how it is done. The final integration of this code is up to the user.

See Section 5 "MCUXpresso Example" for more details about the example.

**[4] App Execution**
The users application is executed. At this point, the EEPROM must have its final content.
2.2.2 Dedicated application

Building a dedicated application for the EEPROM update, and potentially firmware update as well, has the benefit that the user application is independent. In the user application, there is also no check update necessity needed.

The drawback is that one extra programming cycle is necessary. To flash first the dedicated update application, execute it, and after successful execution, flash the chip again with the final user application.

![Dedicated application flow](image)

**Figure 2. Dedicated application flow**

1. **Flash APP-UPT**
   - Program the PN76 with the application dedicated to configure and update the PN76.

2. **Update firmware**
   - If the firmware must be updated, it updates the PN76s firmware.

3. **Update EEPROM**
   - Updates the EEPROM of the PN76 with the provided values.

4. **Finish update**
   - As the firmware and/or EEPROM update is done in application, an indicator to signal that the chip is finished with the update process should be considered. To avoid flashing the user application before the update is finished.
   
   This can be a signal on a GPIO, an output on the HIF (Host Interface), or any other signal. This depends on the customers programming environment. If nothing is feasible a timeout might be used. But there is no indication of success or fail.

5. **Flash APP-USR**
   - The second programming cycle. Here the users final application is programmed.

6. **App Execution**
   - A sanity check if the user application is running properly, is always recommended.
3 EEPROM

The PN76 has three EEPROM locations accessible by the user. The EEPROM is only accessible via the dedicated Eeprom APIs. Offset based addressing is used instead of the real physical address. For every area, the offset starts at 0.

For a detailed description of the EEPROM values, see the PN76 product data sheet.

Table 2. EEPROM areas

<table>
<thead>
<tr>
<th>Area</th>
<th>Symbol</th>
<th>Size</th>
<th>Start - End</th>
<th>Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>User Area</td>
<td>EEPROM_USER_AREA</td>
<td>128</td>
<td>0x000 - 0x080</td>
<td>The first 128 bytes are reserved for NXP use and its content is described in the data sheet.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>894</td>
<td>0x081 - 0x3FF</td>
<td>The remaining 894 bytes are open for the customer to use for data storage.</td>
</tr>
<tr>
<td>Secure Lib</td>
<td>EEPROM_SECURE_LIB</td>
<td>2048</td>
<td>0x000 - 0x7FF</td>
<td>Chip settings accessible and configurable by the user. Described in data sheet.</td>
</tr>
<tr>
<td>IC Config</td>
<td>EEPROM_IC_CONFIG</td>
<td>1024</td>
<td>0x000 - 0x3FF</td>
<td>IC configurations, not accessible for the user.</td>
</tr>
</tbody>
</table>

3.1 EEPROM APIs

The EEPROM of the PN76 can be accessed via the system services layer of the PN76.

Figure 3. EEPROM functions

For a detailed description of the functions, consolidate the PN7642 NFC Controller User API Documentation.
4 NXP NFC Cockpit

The NXP NFC Cockpit (NNC) is a powerful GUI tool to configure and adapt IC settings. It is recommended to use the NNC to adjust your antenna settings or other EEPROM values.

This chapter explains what EEPROM operations you can do with NNC. It is assumed the reader of this document is familiar with the NNC and how to start, and connect, it to the PN76.

4.1 Read/write EEPROM

The NFC Cockpit allows direct access to the EEPROM. When a reader is connected the area "EEPROM Single-Byte Access" comes available. As mentioned in Table 2 "EEPROM areas" the user can access the 'SECURE_LIB_CONFIG' or 'USER_AREA'.

![Figure 4. NNC EEPROM Single-Byte Access](image)

**Explanation:**

- **Address**: The address (offset) where do read/write.
- **Data**: Value that is either read or value to be written.
- **Config**: The EEPROM map from, or where to, read and write.
- **'Read EEPROM' button**: Reads from the address in the Address field and EEPROM map chose in Config. The read value is shown in the Data field.
- **'Write EEPROM' button**: Writes the value from the Data field to the address in the Address field.

**Read EEPROM Example:**

Reading from address '0x01' of config 'SECURE_LIB_CONFIG', this represents the 'DCDC_CONFIG' value. The read value is 0x31, as shown in the Data field and also in the Log Monitor.
4.2 Load/Dump EEPROM

With the NNC, it is easy to load EEPROM files or Dump them.

**Load EEPROM**

By clicking Load EEPROM, a navigation window opens and a EEPROM file (*.xml or *.c) can be chosen. This EEPROM file and all its value are written from NFC Cockpit to the PN76 EEPROM regions.

This is helpful to replicate one configuration to another chip. You can share the EEPROM file and easily load the same file on multiple chips.

Use-cases:

- Replicate the same configuration on multiple chips while developing to have the same base configuration.
- Share EEPROM files with other users (for example, NXP for debugging).
- Switch between different EEPROM configurations to evaluate changes of behavior.

A default EEPROM file can be found in the NFC Cockpit installation folder → cfg → PN7642 → Default.
Dump EEPROM

The Dump EEPROM button dumps the whole EEPROM configuration of the chip to a file (*.xml, *.h or *.c). This dump can further be used in the Load EEPROM of the NFC Cockpit or to prepare your Chip Configuration Application.

4.3 EEPROM files

The NNC can load EEPROM files with the file ending *.xml and *.c. While it can dump the EEPROM configuration to files with the ending *.xml, *.h and *.c.

While the *.xml file is for humans the easiest to read and manipulate, for the PN76 use case the *.h or *.c is more suitable.

4.3.1 EEPROM file *.xml

The *.xml file and style are used for other NXP NFC chips as well. It is the easiest human readable file. But cannot only be used with the NFC Cockpit and not integrated into a user application.

Each parameter is easy to be found with its Name and Offset. And the present Value of this EEPROM register.

Figure 7. NNC EEPROM dump as *.xml
4.3.2 EEPROM file *.h

For the PN76, the NFC Cockpit can dump the EEPROM also in an C/C++ compliant header file. Each EEPROM value is represented as a macro with its Name, offset, length, and value.

![Figure 8. NNC EEPROM dump as *.h](image_url)
4.3.3 EEPROM file *.c

For the PN76, the NNC is able to dump the whole EEPROM configuration into a C/C++ compliant array within a *.c file. The PN76s MCUXpresso SDKs example "pnev7642fama_userdata_update" shows, how such a file can be incorporated in an application. To update the EEPROM from user application space.

```
#include "PN76_Eeprom.h"

/* Array having the General EEPROM information.
   * 4 Bytes of Length: Complete EEPROM Buffer size (LSB First)
   * 4 Bytes EEPROM Info Count (LSB First): will be only once.
   * Will be in form of sets based on the EEPROM parameters
   * 1 Byte EEPROM_Config
   * 2 Bytes Offset (LSB First)
   * 2 Bytes Length (LSB First)
   * N Bytes of Data */

const uint8_t EEPROM_Info[ ] =
{
/* Size of EEPROM information buffer. Is a Sum of
   = EEPROM Information count
   = EEPROM Config
   = EEPROM Offset
   = EEPROM Length
   = EEPROM Data */
0xAE, 0x05, 0x00, 0x00,
/* Number of EEPROM sets */
0x01, 0x00, 0x00, 0x00,
/* USER_PMU */
E_PN76_EEPROM_SECURE_LIB_CONFIG,
0x00, 0x00,
0x0A, 0x00,
0xE4, 0x31, 0x07, 0xAE, 0xFF, 0xFF, 0x00, 0x2A, 0x2A, 0x1D,
/* CLKGEN */
E_PN76_EEPROM_SECURE_LIB_CONFIG,
0x0F, 0x00,
0x02, 0x00,
0x00, 0xFF,
/* RF_CLOCK_CFG */
E_PN76_EEPROM_SECURE_LIB_CONFIG,
0x11, 0x00
```

Figure 9. NNC EEPROM dump as *.c

In the array aEEPROM_Info[ ] all dumped EEPROM settings can be found. How to read them is explained below.
Figure 10. EEPROM array assembly explained

The setting in Figure 10 is to be interpreted as follows:

Table 3. EEPROM array assembly explained

<table>
<thead>
<tr>
<th>Nr.</th>
<th>Length</th>
<th>Description</th>
</tr>
</thead>
</table>
| 1   | 1 byte | Specifies in which EEPROM area this configuration is written.  
• EEPROMSECURELIB  
• EEPROM_USER_AREA  
• EEPROM_IC_CONFIG |
| 2   | 2 bytes | Offset (address) of the configuration parameter. LSB first. See data sheet for all EEPROM configuration addresses.  
In this example, the address is set as '0x3A, 0x06' with LSB first → 0x063A offset |
| 3   | 2 bytes | Length of the data field. LSB first.  
'0x06, 0x00' → 0x0006 → 6 bytes of data length |
| 4   | n byte | The first byte is written to the initial offset. Counting upward.  
• 0x97 → Offset + 0 → 0x063A  
• 0x3D → Offset + 1 → 0x063B  
• 0x3F → Offset + 2 → 0x063C  
• ... |
5 MCUXpresso Example

The PN76 MCUXpresso SDK ([PN76 SDK](#)) example "userdata_update" shows, besides other functions, how a EEPROM configuration file (*.c) can be included in a user application.

![Figure 11. PN76 SDK example userdata_update](image)

5.1 Example flow

The high-level example flow is as shown in the below flowchart.
Figure 12. Example ‘userdata_update’ flowchart

[1] Read Magic Number

The magic number determines which mode is executed. The magic number is stored at a flash address, specified at the beginning of the file userdata_update.c with the macro ADDR_NSFLASH_APP_MODE.

```c
/* check which mode the application has to go into
* 1. APP_CONFIG_MODE --> Magic Number: 0x11335577 Application is in configuration mode, where the configuration
*     settings are applied.
* 2. APP_DWL_MODE --> Magic Number: 0x22446688 Application download mode.
* 3. APP_EXEC_MODE --> Magic Number: 0x33557799 Application is in execution mode.
* For the same, a fixed location in Non-secure flash will be assigned to indicate, which mode of application to
* boot-in. Fixed location in Non-Secure flash would be say: 0x00025478
*/

/* Read the application magic number...*/
GET_NSFLASH_APP_MODE(&dwValue);
dwValue = MAGIC_NUM_APP_CONFIG_MODE;
switch (dwValue)
```
In Figure 13 at line 458, the magic number is read from the flash space. As the example is only a showcase how it could be done, the next line - 459, is making the value "dwValue" fixed. Per default, there is no valid magic number written at the specific flash address.

Note: To execute the wanted mode, modify either the value at the flash address or the variable dwValue directly in the source code.

[2] Config Mode

If the 'dwValue' is equal to MAGIC_NUM_APP_CONFIG_MODE the yellow LED is turned on, and the method [3] 'App_Mode_SetConfig(...)' is called. This method is responsible to read, interpret, and write the EEPROM values represented in the dumped *.c array.

In the example 'userdata_update', the file 'DumpEEPROM.c' is holding the EEPROM dump from a default PN7642 FW v1.0 chip.

Following while loop is iterating through the Eeprom array and writing the values with the system service API 'PN76_WriteEeprom(...)'.

```c
    while ((dwTrSize < dwSize) && (dwCtr < dwElementsCnt)) {
        bType = *(ptr++);
        if (bType > E_PN76_EEPROM_IC_CONFIG) {
            PRINTF("\nType is not correct\n");
            break;
        }
        wOffset = *(ptr++);
        wOffset |= *(ptr++) << 8;
        blen = *(ptr++);
        blen |= *(ptr++) << 8;

        // ptr is pointing to the data now... so call the NSC APIs...
        eStatus = PN76_WriteEeprom((uint8_t *)ptr, wOffset, blen, (PN76_EEPROM_Config_t)bType);
        if (PN76_STATUS_SUCCESS != eStatus) {
            // An error is occurred. Need to exit out of this.. */
            break;
        }
        ptr += blen;
        dwCtr++;
        dwTrSize += blen + 2 + 2 + 1;
    }
```

Figure 14. Write EEPROM loop

The next part is iterating through the RF Protocol settings and writing them into the RF Configuration area. Using the system service API 'PN76_Sys_UpdateRfConfiguration'.
If both have been successfully executed, the EEPROM of the PN76 is updated with the values of the provided EEPROM array.
6 References

[1] PN7642 Product data sheet
[2] PN7642 NFC controller User API Documentation
## 7 Abbreviations

<table>
<thead>
<tr>
<th>Acronym</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NNC</td>
<td>NXP NFC Cockpit</td>
</tr>
<tr>
<td>SDK</td>
<td>Software Development Kit</td>
</tr>
<tr>
<td>NFC</td>
<td>Near Field Communication</td>
</tr>
<tr>
<td>FW</td>
<td>Firmware</td>
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</tbody>
</table>
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## Tables

<table>
<thead>
<tr>
<th>Tab.</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>EEPROM update options</td>
<td>4</td>
</tr>
<tr>
<td>2.</td>
<td>EEPROM areas</td>
<td>7</td>
</tr>
<tr>
<td>3.</td>
<td>EEPROM array assembly explained</td>
<td>13</td>
</tr>
<tr>
<td>4.</td>
<td>Abbreviations</td>
<td>19</td>
</tr>
</tbody>
</table>
### Figures

| Fig. 1. | Integration in user application flowchart | 5 |
| Fig. 2. | Dedicated application flow | 6 |
| Fig. 3. | EEPROM functions | 7 |
| Fig. 4. | NNC EEPROM Single-Byte Access | 8 |
| Fig. 5. | NNC Read EEPROM | 9 |
| Fig. 6. | NNC Load/Dump EEPROM | 9 |
| Fig. 7. | NNC EEPROM dump as *.xml | 10 |
| Fig. 8. | NNC EEPROM dump as *.h | 11 |
| Fig. 9. | NNC EEPROM dump as *.c | 12 |
| Fig. 10. | EEPROM array assembly explained | 13 |
| Fig. 11. | PN76 SDK example userdata_update | 14 |
| Fig. 12. | Example 'userdata_update' flowchart | 15 |
| Fig. 13. | Example code - Read magic number | 15 |
| Fig. 14. | Write EEPROM loop | 16 |
| Fig. 15. | Write RF Configuration loop | 17 |
## Contents

1 Introduction ......................................................... 3  
1.1 Environment .......................................................3  
2 Use cases ............................................................ 4  
2.1 Developing phase ..............................................4  
2.2 Production phase ...............................................4  
2.2.1 Integration in user application ............................5  
2.2.2 Dedicated application ......................................6  
3 EEPROM ...............................................................7  
3.1 EEPROM APIs .................................................. 7  
4 NXP NFC Cockpit ................................................8  
4.1 Read/write EEPROM .........................................8  
4.2 Load/Dump EEPROM ..........................................9  
4.3 EEPROM files ..................................................10  
4.3.1 EEPROM file *.xml ..........................................10  
4.3.2 EEPROM file *.h ............................................11  
4.3.3 EEPROM file *.c ............................................12  
5 MCUXpresso Example ...................................... 14  
5.1 Example flow ................................................... 14  
6 References .........................................................18  
7 Abbreviations .................................................... 19  
8 Legal information ..................................................20