How to Configure a MPC860 System (1 of 4)

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
	L1TCLKB TOUT4* CLK8 PA0	TIN4 BRGO4 CLK7 PA1	L1RCLKB TOUT3* CLK6 PA2	TIN3 BRGO3 CLK5 PA3	TOUT2* CLK4 PA4	TIN2 L1TCLKA BRGO2 CLK3 PA5	TOUT1* CLK2 PA6	TIN1 L1RCLKA BRGO1 CLK1 PA7	L1RXDA PA8	L1TXDA PA9	L1RXDB PA10	L1TXDB PA11	TXD2 PA12	RXD2 PA13	TXD1 PA14	RXD1 PA15
SCC1 NMSI		/		/												
SCC2 NMSI		/		/												
SCC3 NMSI						/		/								
SCC4 NMSI						/		/								
TDMa																
TDMb																
SMC1																
SMC2																
SPI																
Ethnet CAM																
Timer 1																
Timer 2																
Timer 3																
Timer 4																
IDMA 1																
IDMA 2																
I2C																
PIP																

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
PADAT																
PAODR	0	0	0	0	0	0	0	0	0		0			0		0
PADIR																
PAPAR																

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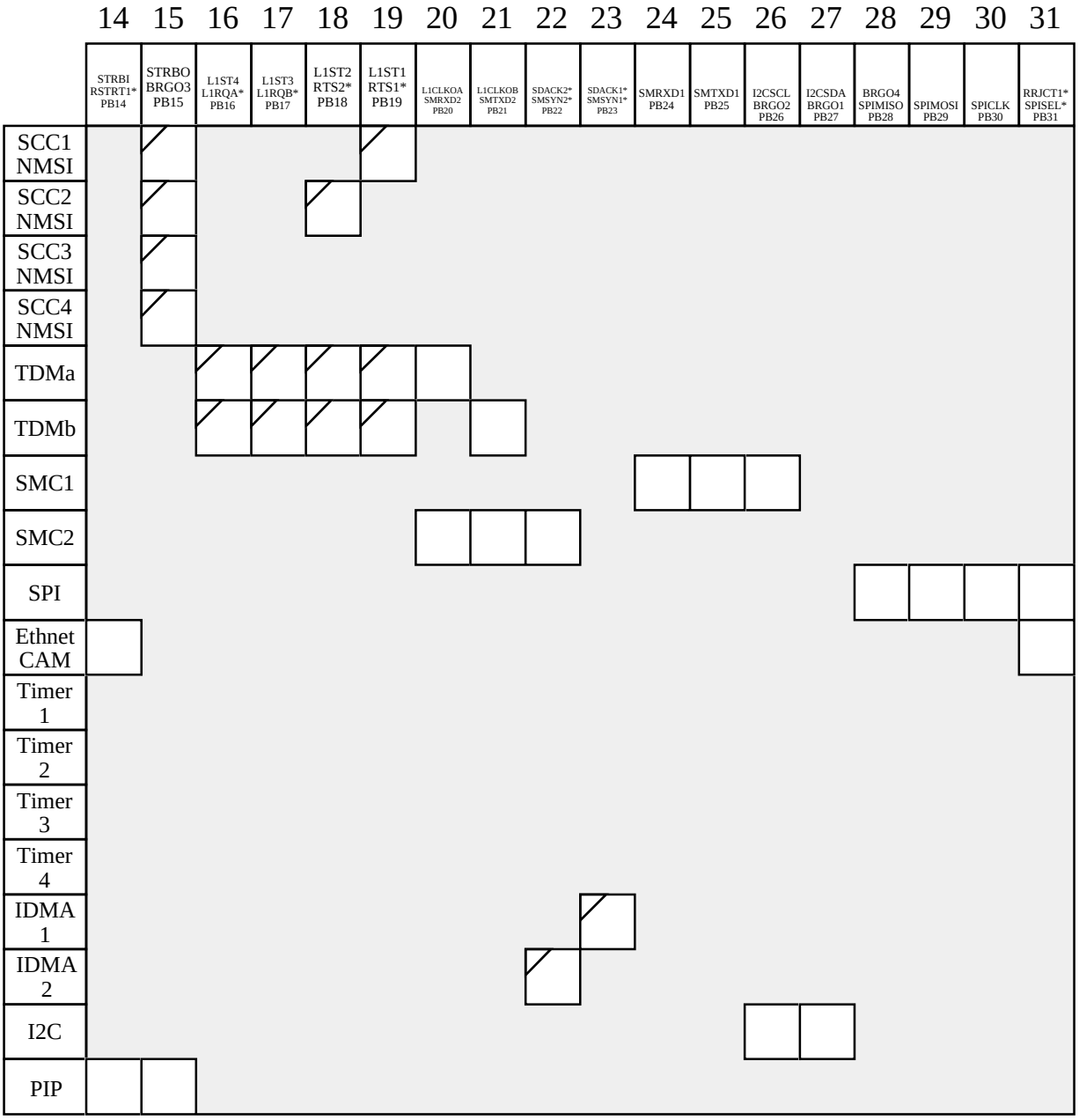
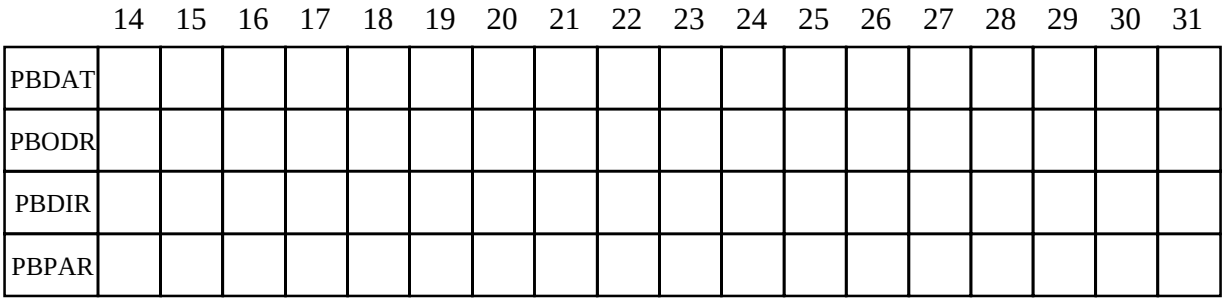
How to Configure a MPC860 System (2 of 4)





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How to Configure a MPC860 System (3 of 4)

	4	5	6	7	8	9	10	11	12	13	14	15
	L1RSYNCA* CD4* PC4	SDACK1* L1TSYNCA* CTS4* PC5	L1RSYNCB* CD3* PC6	SDACK2* L1TSYNCB* CTS3* PC7	TGATE2* CD2* PC8	CTS2* PC9	TGATE1* CD1* PC10	CTS1* PC11	L1ST4 L1RQA* PC12	L1ST3 L1RQB* PC13	L1ST2 RTS2* DREQ2 PC1	L1ST1 RTS1* DREQ1 PC0
SCC1 NMSI												
SCC2 NMSI												
SCC3 NMSI												
SCC4 NMSI												
TDMa												
TDMb												
SMC1												
SMC2												
SPI												
Ethnet CAM												
Timer 1												
Timer 2												
Timer 3												
Timer 4												
IDMA 1												
IDMA 2												
I2C												
PIP												

	4	5	6	7	8	9	10	11	12	13	14	15
PCDAT												
PCDIR												
PCPAR												
PCSO												
PCINT												

How to Configure a MPC860 System (4 of 4)

	3	4	5	6	7	8	9	10	11	12	13	14	15
	RRJCT4 SDACK2 PD3	RRJCT3 SDACK1 PD4	RRJCT2 PD5	RTS4* PD6	RTS3* PD7	TXD4 PD8	RXD4 PD9	TXD3 PD10	RXD3 PD11	L1RSYNCB PD12	L1TSYNCB PD13	L1RSYNCA PD14	L1TSYNCA PD15
SCC1 NMSI													
SCC2 NMSI													
SCC3 NMSI													
SCC4 NMSI													
TDMa													
TDMb													
SMC1													
SMC2													
SPI													
Ethnet CAM													
Timer 1													
Timer 2													
Timer 3													
Timer 4													
IDMA 1													
IDMA 2													
I2C													
PIP													

	3	4	5	6	7	8	9	10	11	12	13	14	15
PDDAT													
PDDIR*													
PDPAR													

* PDDIR, bits 0 &1, can be used to make PD8 and\or PD10 open drain.

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