Interfacing an LCD to the MC9S08LC60

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1 MC9S08LC60 Introduction

The MC9S08LC60 is an HCS08 device that combines a liquid crystal display (LCD) driver module with a rich set of peripherals. This high level of integration reduces the total system cost by providing a single chip solution for applications needing a display with up to 160 segments. LCDs are commonplace in many applications, including thermostats, calculators, digital multi-meters, medical monitoring devices, toys, and appliances.

The LCD module integration in the MC9S08LC60 provides a high-level of configurability allowing for lower power operation. In fact, the LCD driver can be configured to operate a display even when the MC9S08LC60 enters stop3 sleep mode. Figure 1 provides a block diagram of the MC9S08LC60. In addition to the built-in LCD driver, the MC9S08LC60 has notable peripherals such as a 12-bit ADC and dual Flash arrays (for EEPROM emulation), as shown in the block diagram.
This application note introduces the MC9S08LC60 and details a procedure for interfacing a LCD glass to the MC9S08LC60 from a hardware and software perspective. Using the DEMO9S08LC60 EVB hardware, the relationship between the LCD glass segments and the individual bits in the MC9S08LC60 LCDRAM registers is demonstrated. The demo validates that the LCD glass was properly interfaced to the MC9S08LC60.

Sample software to illustrate a basic application that uses a LCD is included in this document. The document contains both an overview of the demo software and a quickstart for the demo. All code for the demo is provided in the file AN3280SW1.zip, which can be found on the Freescale web page (freescale.com).

1.1 Type of LCD Glass Compatible with MC9S08LC60

Two common LCD types are TN (twisted nematics) and STN (super twisted nematics). TN-type LCD glass is very cost-effective and is compatible with multiplexing LCD drivers using fewer than 16 backplanes. The multiplexing LCD driver uses multiple backplanes to reduce the number of connections between the LCD and the LCD driver. Using multiplexing, each frontplane pin can be connected to as many segments as there are backplanes. With up to four backplanes and running in 1/3 bias mode, the MC9S08LC60 is compatible with TN-type LCD glass.
The multiplexing configuration relates to the LCD duty cycle. The duty cycle indicates the amount of time the LCD panel segment is energized during each LCD module frame cycle. The available duty cycle options for the MC9S08LC60 LCD module are 1/2, 1/3, and 1/4. The denominator of the duty cycle indicates the number of backplanes being used to drive a LCD panel.

This application note does not provide an introduction to LCD theory, types of LCD, LCD waveforms, or principles of operation. AN3219, XGATE Library: TN/STN LCD Driver, provides a detailed description of driving TN and STN LCDs using general-purpose input/output (GPIO) pins. Even though the MC9S08LC60 has a built-in LCD driver module, the discussions presented in AN3219 on multiplex waveforms and LCD theory are relevant.

1.2 Number of Segments and Using Segments in LCD

Depending on package and LCD configuration, the MC9S08LC60 can support up to 160 segments. The number of segments is equal to the number of backplanes times the number of frontplanes. With multiplex LCD drivers like the MC9S08LC60, each frontplane pin can be connected to as many segments as there are backplanes. With up to 160 segments, the MC9S08LC60 can be used in displays that are numeric, alpha-numeric, symbolic, or a combination of these. Numeric displays require seven segments for each digit; alpha-numeric displays require either 14 or 16 segments per character. Table 1 lists the available configurations and the potential number of digits when using a 7-, 14-, or 16-segment configuration.

<table>
<thead>
<tr>
<th>Package</th>
<th>Number of Backplanes</th>
<th>Number of Frontplanes</th>
<th>Total Number of Segments</th>
<th>Potential Number of Characters (Remainder)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>7 segment</td>
</tr>
<tr>
<td>64 pin</td>
<td>4</td>
<td>32</td>
<td>128</td>
<td>18 (2)</td>
</tr>
<tr>
<td></td>
<td>3</td>
<td>33</td>
<td>99</td>
<td>14 (1)</td>
</tr>
<tr>
<td>80 pin</td>
<td>4</td>
<td>40</td>
<td>160</td>
<td>22 (6)</td>
</tr>
<tr>
<td></td>
<td>3</td>
<td>41</td>
<td>123</td>
<td>17 (4)</td>
</tr>
</tbody>
</table>
1.3 LCD Glass Specification

When obtaining TN-type LCD glass for an application, several parameters should be considered. These include:

- LCD operating voltage — Typical values for LCD glass operating voltage are 3 V or 5 V. The operating voltage is the nominal voltage level threshold to power on a segment.
- Current consumption — The current consumption is related to the total segment area.
- View area — This is related to the size of the LCD glass.
- Driving mode — Duty cycle (1/2, 1/3, and 1/4) and bias (1/3)
- View angle — 3 o’clock, 6 o’clock, 9 o’clock, and 12 o’clock
- View mode — Reflective, transmissive, and transflective
- Operating temperature — Varies

2 LCD Module Overview

2.1 LCD Module Power Supply Configuration

The LCD module provides several configurations for its power supply. The LCD module can be powered via $V_{DD}$ or, alternatively, by an external LCD power supply connected to the $V_{LCD}$ pin (the $V_{LCD}$ voltage range is 1.4 to 1.8 V). The LCD module power supply configuration is determined by the VSUPPLY[1:0] bit field. A block diagram of the power supply architecture is shown in Figure 2. The designer must choose from one of the power modes shown in Figure 2. This decision will be based on application requirements.

![Figure 2. LCD Module Power Supply Configuration Options](image-url)
Another factor affecting power supply configuration has to do with the LCD glass panel. Typical driving voltages for LCD glass panels are 3 V and 5 V. The subsequent sections summarize power supply configuration options when driving both 3 V and 5 V LCD glass.

2.1.1 Configuration Options for 3 V LCD Glass

When driving 3 V LCD glass, the following configurations are available:

- Power the LCD module via $V_{LCD}$, where $V_{LCD}$ is nominally 1.5 volts, with the LCD module configured for doubler mode.
- Power the LCD module via $V_{DD}$, where $V_{DD}$ is nominally 3 V, with the LCD module voltage supply switch ($VSUPPLY[1:0]$) configured to generate $V_{LL3}$ from $V_{DD}$
- Power the LCD module via $V_{DD}$, where $V_{DD}$ is nominally 2 V, with the LCD module voltage supply switch ($VSUPPLY[1:0]$) configured to generate $V_{LL2}$ from $V_{DD}$

2.1.2 Configuration Options for 5 V LCD Glass

When driving 5 V LCD glass, the following configurations are available:

- Power LCD module via $V_{LCD}$, where $V_{LCD}$ is nominally 1.67 volts, with the LCD module configured for tripler mode.
- Power the LCD module via $V_{DD}$, where $V_{DD}$ is nominally 3.3 volts, with the LCD module voltage supply switch ($VSUPPLY[1:0]$) configured to generate $V_{LL2}$ from $V_{DD}$

2.2 LCD Module Clock

Figure 3 illustrates the MC9S08LC60 clock source, which can either be sourced from the MC9S08LC60 internal clock reference or an external clock source. The LCD module is designed to operate using a 32.768 kHz clock regardless of clock source, so clock dividers must be used to achieve the target 32.768 kHz clock target. In Figure 3, the 32.768 kHz clock target is labeled LCDCLK. LCDCLK is used to determine the following LCD operational settings:

- LCD base frequency
- LCD blink rate
- LCD charge pump rate
2.2.1 External Crystal and the LCD Frame Frequency

The lowest-power configuration for the MC9S08LC60 uses the external clock. It is also advantageous to select the slowest acceptable clock rate for the LCD base frequency, the LCD frame frequency, and the LCD charge pump rate that supports the LCD glass in the application. Reducing the clock rate reduces the current consumption of the LCD module.

2.3 LCD Blinking

One of the most outstanding features of the MC9S08LC60 is the flexible support for segment blinking. Some features of the segment blinking implementation are:

- Blink capability is programmable for the entire display or individual segments
- Multiple programmable options for the blink rate are available
- Blink functionality is available in stop3 mode

The LCDDRMS bit in the LCDCMD register is used in conjunction with the LCDRAM registers to provide blinking control for individual segments. If the LCDDRMS bit is cleared, the LCDRAM registers control the display on/off state for segments on the LCD display. If LCDDRMS bit is set, the LCDRAM registers control the blink enable on/off state for the corresponding segment. See Section 4.2.3.2.1, “Non-alphanumerics,” for example software functions that use the LCDDRMS bit to toggle the LCDRAM between its function to control the display on/off state and to control blink enable on/off state.

To configure all LCD segments to blink regardless of the contents of the LCDRAM registers while the LCDDRMS bit is set, the BLKMODE bit in the LCDBCTL control register must also be set.
3  Interfacing the LCD MCU to an LCD Glass

This application uses the DEMOLC60 EVB to detail the requirements for interfacing an LCD glass to the MC9S08LC60. Figure 4 shows the top layer silk screen of the DEMO9S08LC60 with the LCD glass panel. The LCD glass panel is shown with all LCD segments turned on. This section will review the LCD glass design in detail and discuss the interfacing of the LCD module to the LCD glass via hardware and software.
3.1 LCD Glass Detailed Description

The LCD panel used in the DEMO9S08LC60 has a viewable area of approximately 75 mm × 25 mm and is shown in Figure 5. It is a custom 160-segment LCD TN-type glass that combines nine individual alphanumeric segment groups with numerous individual segments in the form of labels, punctuation, icons, etc. The alphanumeric segment groups are labeled 1 to 9 starting from the leftmost character position. The LCD panel is manufactured by S-Tek Displays (part number GD3980P).

![Figure 5. Custom LCD from S-Tek Displays](image)

The LCD panel’s segment layout is not the only important aspect of its design. Other LCD glass specifications, such as those mentioned in Section 1.1, also dictate how the LCD glass must be driven by an LCD driver for optimal performance. Table 2 provides a summary of GD3980P parameters.

<table>
<thead>
<tr>
<th>GD3890P Parameters</th>
<th>Units</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>Driving Voltage</td>
<td>Volts</td>
<td>3</td>
</tr>
<tr>
<td>Duty</td>
<td></td>
<td>1/4</td>
</tr>
<tr>
<td>Driving Frequency</td>
<td>Hz</td>
<td>64</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>C</td>
<td>0-50</td>
</tr>
<tr>
<td>Display Mode</td>
<td></td>
<td>Positive, Reflective</td>
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<tr>
<td>Viewing Angle</td>
<td>o’clock</td>
<td>6</td>
</tr>
</tbody>
</table>
Figure 6 shows all 160 segments labeled individually. This information is provided by the LCD manufacturer. See the appendix for the full GD3980P specification for S-Tek Displays.

Figure 6. Segment Labels
Interfacing an LCD to the MC9S08LC60, Rev. 0

Table 3 lists the LCD glass pin out along with each of the 160 segment labels. The LCD glass has a total of 44 pins, including four pins for backplane connections and 40 for frontplane connections.

<table>
<thead>
<tr>
<th>PIN</th>
<th>COM1</th>
<th>COM2</th>
<th>COM3</th>
<th>COM4</th>
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<tr>
<td>1</td>
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<td>6</td>
<td>1A</td>
<td>1J</td>
<td>1G</td>
<td>1M</td>
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<td>7</td>
<td>1K</td>
<td>1L</td>
<td>1D</td>
<td>TIME</td>
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<td>8</td>
<td>1B</td>
<td>1C</td>
<td>DT1</td>
<td>DT</td>
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</tr>
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<td>2J</td>
<td>2G</td>
<td>2M</td>
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<td>2L</td>
<td>2D</td>
<td>DATE</td>
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<td>2C</td>
<td>COL1</td>
<td>DT2</td>
</tr>
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<td>7F</td>
<td>7E</td>
<td>7N</td>
</tr>
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<td>7L</td>
<td>7D</td>
<td>P</td>
</tr>
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<td>7C</td>
<td>COL3</td>
<td>DT7</td>
</tr>
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<td>DT8</td>
<td>MODE</td>
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<td>9H</td>
<td>9F</td>
<td>9E</td>
<td>9N</td>
</tr>
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<td>9J</td>
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<table>
<thead>
<tr>
<th>PIN</th>
<th>COM1</th>
<th>COM2</th>
<th>COM3</th>
<th>COM4</th>
</tr>
</thead>
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<td>23</td>
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<td>9L</td>
<td>9D</td>
<td>COL4</td>
</tr>
<tr>
<td>24</td>
<td>9B</td>
<td>9C</td>
<td>DT9</td>
<td>V1</td>
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<td>25</td>
<td>V2</td>
<td>AM</td>
<td>PM</td>
<td>OM</td>
</tr>
<tr>
<td>26</td>
<td>KWh</td>
<td>Amps</td>
<td>Volts</td>
<td>Program</td>
</tr>
<tr>
<td>27</td>
<td>VOL</td>
<td>V3</td>
<td>V4</td>
<td>V5</td>
</tr>
<tr>
<td>28</td>
<td>T4</td>
<td>T3</td>
<td>T2</td>
<td>T1</td>
</tr>
<tr>
<td>29</td>
<td>6B</td>
<td>6C</td>
<td>DT6</td>
<td>T</td>
</tr>
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<td>30</td>
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<td>6L</td>
<td>6D</td>
<td>K3</td>
</tr>
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<td>6J</td>
<td>6G</td>
<td>6M</td>
</tr>
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<td>6F</td>
<td>6E</td>
<td>6N</td>
</tr>
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<td>5B</td>
<td>5C</td>
<td>COL2</td>
<td>DT5</td>
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<td>5L</td>
<td>5D</td>
<td>VOLUME</td>
</tr>
<tr>
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<td>5L</td>
<td>5J</td>
<td>5G</td>
<td>5M</td>
</tr>
<tr>
<td>36</td>
<td>5H</td>
<td>5F</td>
<td>5E</td>
<td>5N</td>
</tr>
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<td>37</td>
<td>4B</td>
<td>4C</td>
<td>DT4</td>
<td>K2</td>
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<td>3F</td>
<td>3E</td>
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</tr>
</tbody>
</table>


3.2 Mapping LCD Glass Segments to the MCU LCD RAM Registers

After the relationship between the individual segments and the LCD pins is understood, the relationship between the LCD pins and the MCU pins and MCU LCD RAM registers needs to be established. The MCU and LCD pins are wired together, making sure that the respective backplane and frontplane pins are matched. Figure 7 shows connections between the GP3980P and the MC9S08LC60 for the DEMO9S08LC60.

![Figure 7. MCU-to-LCD pin connections](image)

With the MCU pins connected to the LCD glass pins, a simple relationship between the LCD display segments and the MCU LCD RAM registers can be established. The mapping is determined by both the MCU-to-LCD pin connections (Figure 7) and the LCD pin out specification (Table 3). Each bit in the MCU RAM registers is mapped to an individual segment on the LCD glass.

Table 4 shows the MCU LCD RAM registers, where the individual bits are labelled FPxBPy. Figure 7 shows both the “BPy” and the “FPx” relationships between the MCU pinout and LCD panel pins. For example:

- BP3 is connected to the LCD pin 4. (It is important to note this is COM4.)
- FP15 is connected to LCD pin 37.
Interfacing an LCD to the MC9S08LC60, Rev. 0

Interfacing the LCD MCU to an LCD Glass

Using this information and Table 3 (for COM4 and LCD pin 37), one finds that FP15BP3 is connected to the label “K2.” Using Figure 6, the label “K2” is associated with the snowflake icon segment. Using this approach, all the relationships between the LCD display segments and the MCU LCD RAM registers can be established. Powering individual segments on and off is a matter of toggling the correct bit associated with the segment. For example, when the FP15BP3 bit is set to 1, the corresponding segment (the snowflake icon) will become visible on the LCD glass. When the FP15BP3 bit is set to 0, the segment is not displayed.

Table 4. MCU LCD RAM

<table>
<thead>
<tr>
<th>Address</th>
<th>Segment 0</th>
<th>Segment 1</th>
<th>Segment 2</th>
<th>Segment 3</th>
<th>Segment 4</th>
<th>Segment 5</th>
<th>Segment 6</th>
<th>Segment 7</th>
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<tbody>
<tr>
<td>0x1848</td>
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<td>FP1BP2</td>
<td>FP1BP1</td>
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<td>FP27BP0</td>
<td>FP26BP3</td>
<td>FP26BP2</td>
<td>FP26BP1</td>
<td>FP26BP0</td>
</tr>
<tr>
<td>0x1856</td>
<td>FP29BP3</td>
<td>FP29BP2</td>
<td>FP29BP1</td>
<td>FP29BP0</td>
<td>FP28BP3</td>
<td>FP28BP2</td>
<td>FP28BP1</td>
<td>FP28BP0</td>
</tr>
<tr>
<td>0x1857</td>
<td>FP31BP3</td>
<td>FP31BP2</td>
<td>FP31BP1</td>
<td>FP31BP0</td>
<td>FP30BP3</td>
<td>FP30BP2</td>
<td>FP30BP1</td>
<td>FP30BP0</td>
</tr>
<tr>
<td>0x1858</td>
<td>FP33BP3</td>
<td>FP33BP2</td>
<td>FP33BP1</td>
<td>FP33BP0</td>
<td>FP32BP3</td>
<td>FP32BP2</td>
<td>FP32BP1</td>
<td>FP32BP0</td>
</tr>
<tr>
<td>0x1859</td>
<td>FP35BP3</td>
<td>FP35BP2</td>
<td>FP35BP1</td>
<td>FP35BP0</td>
<td>FP34BP3</td>
<td>FP34BP2</td>
<td>FP34BP1</td>
<td>FP34BP0</td>
</tr>
<tr>
<td>0x185A</td>
<td>FP37BP3</td>
<td>FP37BP2</td>
<td>FP37BP1</td>
<td>FP37BP0</td>
<td>FP36BP3</td>
<td>FP36BP2</td>
<td>FP36BP1</td>
<td>FP36BP0</td>
</tr>
<tr>
<td>0x185B</td>
<td>FP39BP3</td>
<td>FP39BP2</td>
<td>FP39BP1</td>
<td>FP39BP0</td>
<td>FP38BP3</td>
<td>FP38BP2</td>
<td>FP38BP1</td>
<td>FP38BP0</td>
</tr>
<tr>
<td>0x185C</td>
<td>FP40BP3</td>
<td>FP40BP2</td>
<td>FP40BP1</td>
<td>FP40BP0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

3.3 Mapping the MCU LCD RAM to Alphanumeric Segment Groups

Using the procedure above, turning individual segments on and off is very straightforward. For the alphanumeric displays (the DEMO9S08LC60 contains 13-segment displays), the procedure is basically the same except that individual segments are grouped and modified together depending on the alphanumeric character that is specified to be activated in the display.
3.3.1 GP3890P Alphanumeric Segment Groups

The alphanumeric segment group for the DEMO9S08LC60 is a modified version of a standard 14-segment display group. This custom segment group for alphanumeric displays in the GP3890P LCD glass, shown in Figure 8, uses only 13 segments. The difference between this group and a standard 14-segment group is the single segment “G”, which normally consists of two segments of equal size. The GP3890P has a total of nine 13-segment displays. Referencing Figure 6, the leftmost 13-segment display group is the first position. With a 13-segment display layout, each alphanumeric character will require 13 bits of MCU LCD RAM.

Figure 8. GP3980P Custom 13-Segment Display Layout Pattern

3.3.2 Alphanumeric Segment Group MCU LCD RAM Mapping

This section gives an example of the mapping of an alphanumeric segment group for the leftmost 13-segment display (position 1) on the GP3890P to display the letter “M”. Figure 9 shows the layout of the 13-segment display, an excerpt from the LCD pinout specification table, and the pinout connections between the MCU and the LCD glass. Table 5 shows the corresponding MCU LCD RAM registers. Note that two adjacent MCD LCD RAM registers, LCDRAM1 and LCDRAM0, are used for the 13-segment alphanumeric segment group.
In Table 5, the MCU LCD RAM registers are shown with both the bit names and the corresponding labels from Figure 6. The register bits values in Table 5 are provided for the display of the alphanumeric character “M” in the LCD display. A value of “X” in the table is provided for the bit locations where the value is not associated with the 13-segment display. For these locations, the bit value is irrelevant. In this case (for alphanumeric characters in character position 1), these are the bits for the labels “TIME”, “DT”, and “DT1”. Table 6 provides a more detailed tabular form of the decoding from MCU pins to LCD segments.
The same mapping approach can be used for the remaining alphanumeric character positions. An important observation is that the other character positions follow the MCU LCD RAM mapping organization and layout pattern (see Figure 8) using two adjacent registers. The shaded bit locations in Table 6 are used arbitrarily for all other segments not used in an alphanumeric group. Table 7 lists the MCU LCD RAM registers used for alphanumeric groups 1–9 (LCDRAM[0:17], respectively). The LCDRAM[18:19] registers are not used for alphanumeric groups. LCDRAM20 is not used at all because FP40 is not used in 1/4 duty mode. In 1/4 duty mode, the multiplexed BP3/FP40 pin is configured as BP3.

### Table 6. Detailed Alphanumeric Segment Mapping

<table>
<thead>
<tr>
<th>MCU Pin Function</th>
<th>MCU Pin # in 80 Pin Package</th>
<th>MCU Pin # in 64 Pin Package</th>
<th>LCD Pin</th>
<th>LCD Segment / MCU LCD RAM Bit</th>
</tr>
</thead>
<tbody>
<tr>
<td>FP0</td>
<td>7</td>
<td>6</td>
<td>5</td>
<td>1H/FP0BP0 1F/FP0BP1 1E/FP0BP2 1N/FP0BP3</td>
</tr>
<tr>
<td>FP1</td>
<td>6</td>
<td>5</td>
<td>6</td>
<td>1A/FP1BP0 1J/FP1BP1 1G/FP1BP2 1M/FP1BP3</td>
</tr>
<tr>
<td>FP2</td>
<td>5</td>
<td>4</td>
<td>7</td>
<td>1K/FP2BP0 1L/FP2BP1 1D/FP2BP2 TIME/FP2BP3</td>
</tr>
<tr>
<td>FP3</td>
<td>4</td>
<td>3</td>
<td>8</td>
<td>1B/FP3BP0 1C/FP3BP1 DT1/FP3BP2 DT/FP3BP3</td>
</tr>
</tbody>
</table>

### Table 7. MCU LCD RAM Registers for Alphanumeric Groups

<table>
<thead>
<tr>
<th>Alphanumeric Segment Group</th>
<th>MCU LCD RAM</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LCDRAM0 LCDRAM1</td>
</tr>
<tr>
<td>2</td>
<td>LCDRAM2 LCDRAM3</td>
</tr>
<tr>
<td>3</td>
<td>LCDRAM4 LCDRAM5</td>
</tr>
<tr>
<td>4</td>
<td>LCDRAM6 LCDRAM7</td>
</tr>
<tr>
<td>5</td>
<td>LCDRAM8 LCDRAM9</td>
</tr>
<tr>
<td>6</td>
<td>LCDRAM10 LCDRAM11</td>
</tr>
<tr>
<td>7</td>
<td>LCDRAM12 LCDRAM13</td>
</tr>
<tr>
<td>8</td>
<td>LCDRAM14 LCDRAM15</td>
</tr>
<tr>
<td>9</td>
<td>LCDRAM16 LCDRAM17</td>
</tr>
</tbody>
</table>
4 Example Application Using the DEMO9S08LC60

This section provides a brief description of a basic software demo using the DEMO9S08LC60 demonstration board which uses the MC9S08LC60. Figure 10 shows the a photo of the DEMO9S08LC60 Example Application.

![Figure 10. DEMO9S08LC60 Example Application](image)

The demo captures alphanumeric data from SCI0 and then displays the data on the LCD display. Alphanumeric data can be transmitted to MC9S08LC60’s SCI0 using a PC and a COM terminal program (such as Hyperterminal or Tera Term). Other features of the demo are:

- Demonstrates the usage of an MC9S08LC60 with a 32.768 kHz external crystal clock source
  - Demonstrates configuration of the ICG
- Demonstrates the reception of data via SCI0
  - Demonstrates configuration of the SCI0 baud rate
- Demonstrates the usage of the LCD modules
  - Demonstrates the LCD module power configuration to drive the GP3890P (3 volts LCD glass)
  - Demonstrates the LCD module clock configuration
  - Demonstrates the LCD module segment blinking
  - Demonstrates the LCD module driving of alphanumeric characters
- Demonstrates the reception of data via SCI0
- Demonstrates the use of GPIO
  - LEDs toggle when user presses buttons
All the software for the basic demo is provided as a companion to this application note and can be downloaded from the Freescale web site. The file name is AN3280SW1.zip. Use the software at your own risk. The software is provided “as-is,” with no warranties, guarantees, or support.

**NOTE**

Out of box, the DEMO9S08LC60 comes preprogrammed with an LCD demo. This preprogrammed demo application is provided by SofTech Microsystems and it is operational when the DEMO9S08LC60 is powered. The SofTech Microsystems demo and the demo described in this application note, though having similar functions, have slightly different source code implementations. The SofTech Microsystems demo source code is provided on a CD in the DEMO9S08LC60 kit.

### 4.1 DEMO9S08LCD60 Overview and Configuration

The DEMO9S08LC60 (see Figure 4) is a full-featured customer evaluation PCB with a built-in USB-to-BDM programmer. Besides providing a programming interface, the USB cable can also be used to power the device.

The DEMO9S08LC60 has the following user interface elements:

- LEDs
- Buttons
- Potentiometer
- Serial port
- Photo cell
- Temp sensor
- Speaker
- 3-Axis accelerometer

For this demo, the DEMO9S08LC60 must be configured with the jumper positions given in Table 8.

<table>
<thead>
<tr>
<th>Jumper(s)</th>
<th>Setting</th>
</tr>
</thead>
<tbody>
<tr>
<td>LED EN</td>
<td>all ON</td>
</tr>
<tr>
<td>IO1 ENA</td>
<td>all ON</td>
</tr>
<tr>
<td>IO2 ENA</td>
<td>all ON</td>
</tr>
<tr>
<td>PTA7 SEL</td>
<td>LED</td>
</tr>
<tr>
<td>CLK ENA</td>
<td>all ON</td>
</tr>
<tr>
<td>POWER SEL</td>
<td>USB</td>
</tr>
<tr>
<td>g-Sel1</td>
<td>don't care</td>
</tr>
<tr>
<td>g-Sel1</td>
<td>don't care</td>
</tr>
<tr>
<td>g-Sleep Mode</td>
<td>don't care</td>
</tr>
</tbody>
</table>
4.2 Example Application Software Overview

This section provides an overview of the configuration of the example application demo. The overview reviews settings for:

- ICG
- SCI
- LCD

These LCD drivers are a subset of the application. The application could be an energy meter or a toy.

The goal of this section is not to describe every function in detail. Instead, only select functions will be described. For example, a description will be provided for the implementation of a procedure for the mapping of the MCU LCD RAM to alphanumeric segment groups as described in the previous section.

4.2.1 ICG Configuration

The ICG has four possible configurations, including the following three modes:

- FLL bypassed, external clock (FBE) mode
- FLL engaged, internal clock (FEI) mode
- FLL engaged, external clock (FEE) mode

Both FBE and FEE use the on-board 32.768 kHz crystal on the DEMO9S08LC60. For these modes, the DEMO9S08LC60 CLK ENA jumper must be installed. For the lowest power mode, the FBE mode is recommended. If the use of the LCD in stop3 is desired, FBE mode is also recommended. If lower system/BOM (bill of materials) cost is the priority, the FEI mode should be used. Cpu.h defines variables to select the desired ICG clock mode.

While the demo software is pre-configured to use the ICG FBE mode, the demo project can be modified so that the user can also operate the demo in FEI and FEE ICG modes. When using either FEI and FEE ICG modes, the demo uses a faster SCI baud setting (9600 bps). The code excerpt below from Cpu.h shows the ICG options built-in to the project.

```c
//=======================================================
//Select a clock mode
//=======================================================
//FBE = FLL ByPassed External Clock
#define FBE_32KHZ 1 /* 32 kHz crystal and 16 kHz fbus */
//FEI = FLL Enabled using Internal Clock
#define FEI_8MBUS 0 /* fbus 17.77/2 MHz; untrimmed */
```
4.2.2 SCI Configuration

The SCI configuration requires:

- The enabling of the SCI receiver and transmitter
- The configuration of the SCI baud registers
- Configuration of other SCI parameters

The code excerpt below provides a typical scheme for SCI configuration.

```c
SCIC1 = 0;
/* SCI1C3: R8=0,T8=0,TXDIR=0,??=0,ORIE=0,NEIE=0,FEIE=0,PEIE=0 */
SCIC3 = 0;
SCIBD = SCIDIVIDER; //Set in sci.h file
SCIC2_TE = 1; /* Enable transmitter */
SCIC2_RE = 1; /* Enable receiver */
```

Depending on the ICG configuration, the SCIDIVIDER variable may need to be modified to obtain a particular baud rate. The SCI is clocked by the bus clock. With a 32.768 kHz crystal and FBE mode, a 9600 baud rate cannot be achieved. (The value of SCIDIVIDER is approximately equal to the bus clock / 16 / desired baud rate). In the demo application, SCIDIVIDER can be configured in sci.h. In the case of a 32.768 kHz crystal in FBE mode, SCIDIVIDER is selected (in sci.h) such that the baud rate is 110 bps.

4.2.3 LCD Configuration

An LCD driver can be developed using a variety of approaches. LCD drivers provide the ability to initialize the LCD module and write to the display. Because of this, LCD driver should provide the following functionality:

- LCD initialization
  - Initialization of the LCD clock
  - Initialization of the LCD frame frequency and blink rate
  - Initialization of the LCD power supply
  - Setup of the waveform type
  - Setup of the charge pump frequency
  - Setup of the LCD buffer
  - Setup of the LCD buffer driver capability
  - Enabling of the frontplanes and the LCD

- LCD methods
  - Write to the display (for alphanumerics and non-alphanumerics)
  - Clear the display (for alphanumerics and non-alphanumerics)
  - Configure, start, and end blinking

- LCD events
  - LCD interrupt enable
  - LCD interrupt handler
LCD drivers to write to the display are simple to implement after the relationship of the LCD segments to the LCD RAM is determined. For this example application, the method to determine the LCD segment to LCD RAM relationship was described in Section 3. Section 3 also discussed an approach to display alphanumeric characters. This approach will be implemented in software.

4.2.3.1 LCD Initialization Drivers LCD_init()

In the example application, the initialization is accomplished via the LCD_init() function. The source code for LCD_init() is provided below. LCD_init() calls functions that configure the power, clocks, and other parameters for the LCD. Many of these parameters are compile time decisions. Each of the functions called by LCD_init() can be found in lcddrv.c.

```c
void LCD_init() {
    //Configure clock source
    CONFIG_CLKSOURCE();

    //power supply configuration
    SET_CONFIG_VSUPPLY();

    //Configure operation in stop/wait
    SET_LCDCR1_REG();

    //Configure frame frequency
    SET_LCD_FRAME_FREQU();

    //Configure blink rate
    CONFIG_BLINKING( 2 /* Hz*/, OFF);

    //Enable Frontplanes
    ENABLE_FP();

    //Enable LCD
    ENABLE_LCD(ON);

    //Map RAM to ARRAY
    MAP_LCDDRIVER_TO_LCDGLASS();
}
```

The example application uses lcddrv.h to set up the LCD module power, clock, and other initialization options using several define directives. Some of these directive-defined variables are also tied to variables defined in cpu.h because LCD clock configuration parameters are dependent on the ICG configuration. An excerpt of variables defined in lcddrv.h is provided below, and shows the options for LCD power configuration and LCD duty cycle.

```c
//===========================
//LCD power configuration
//===========================
#define LCDPWR_VDD    1
#define LCDPWR_VLCD   0
#define LCDPWR_VLLLN  0
```

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Using the directive-defined variables in lcddrv.h, cpu.h, and sci.h, the demo and the LCD module configuration can be changed quickly.

### 4.2.3.2 LCD Methods

The functions listed in this section manage the displaying and clearing of segments in the LCD display during the execution of the program. The functions manage either non-alphanumeric (icons, labels, etc.) or alphanumeric (13-segment display group) segments.

#### 4.2.3.2.1 Non-alphanumerics

The functions available to handle non-alphanumerics are provided below. In this implementation, the non-alphanumerics were grouped into functional categories. The selection of the grouping is not important from an application standpoint.

For the non-alphanumerics, these functions also allow the setting of the blink mode for those individual segments. The lcddramm variable tells the function if the LCDRAM[20:0] bits control the display on/off function or the blink enable on/off function. When calling these functions with lcddramm set to SEGONOFF, the LCDRAM[20:0] bits control the display on/off function; when calling these functions with lcddramm set to SEGBLINKEN, the LCDRAM[20:0] bits control the blink enable on/off function.

```c
void SET_LOGO(char lcddramm, char k);
void SET_VOL(char lcddramm, char v, byte numbars);
void SET_BATT(char lcddramm, char t, byte numbars);
void SET_POWERSAVE(char lcddramm, char p);
void SET_TIMESEGS(byte lcddramm, byte am, byte pm, byte k3);
void SET_TEMPSEGS(byte lcddramm, byte k1, byte k2, char col4);
void SET_DMMLABLES(char lcddramm, char kwatt, char volt, char amp, char om);
void SET_COLONS(char lcddramm, char col1, char col2, char col3, char dt);
void SET_DECIMALS(char lcddramm, char dt1, char dt2, char dt3, char dt4, char dt5, char dt6, char dt7, char dt8, char dt9);
void SET_MENU(char lcddramm, char lab1, char lab2, char lab3, char lab4, char lab5, char lab6, char lab7);
```

The functional arguments after the lcddramm argument point to individual bits in the LCD RAM. In most cases, the variable name shown matches the LCD panel label for the segments. For example, “k” in SET_LOGO(char lcddramm, char k) points to the Freescale logo segment. To control the on/off state, an ON and an OFF variable are provided. An example of the typical usage of these functions is provided below:

```
//Configure the display of am label, pm label and clock icon
SET_TIMESEGS(SEGONOFF, OFF, ON, ON); //am label is OFF while pm label and clock icon are ON
//Configure the blink enable of am label, pm label and clock icon
SET_TIMESEGS(SEGBLINKEN, OFF, ON, ON); //pm label & clock icon are configured to blink
```
4.2.3.2.2 Alphanumerics

The example application provides the output_strg_lcd(tU08 *string, tU08 length) function to display alphanumerics to the LCD display. The complete source code for output_strg_lcd() can be found in lcddrv.c.

The output_strg_lcd() function writes the contents of the string argument to the LCD display. It starts by writing the rightmost character of the string argument to the rightmost alphanumeric position on the LCD glass (i.e., the ninth position; see Figure 6). The output_strg_lcd() function loops through the string argument variable characters and indexes to the next LCD glass character position to complete the display.

To display the alphanumerics of the string argument, the software must determine the correct segments of the 13-segment display to power on while leaving the others off. The software uses a lookup table to determine the correct segments. The excerpt of the output_strg_lcd() source code shown below illustrates the usage of the lookup table and the manipulation of the proper LCDRAM registers to display the correct alphanumeric. When a new value is to be written to the LCDRAM registers, it must be ORed with the previous value. This is necessary because there are other segments in the LCDRAM register that are not part of the 13-segment display and their values must be preserved (see Figure 9).

```c
  c = ((string[i]-0x20)*2);  // convert ASCII to table index (in bytes)
  if (c < 128) {
      *LCD_pos[LCD_position*2-2] = *LCD_pos[LCD_position*2-2] | (tU08)ascii[c];
      *LCD_pos[LCD_position*2-1] = *LCD_pos[LCD_position*2-1] |(tU08)ascii[c+1];
  }
```

The look-up table is contained in the ascii[] array, which is shown below. This array determines what values need to be written to the MCU LCD RAM registers for a given alphanumeric value. The values of the look-up table were determined using the procedure discussed in Section 3.3.2. An Excel worksheet is provided in AN3280SW1.zip that helps determine the values for the ascii[] array.

```c
const tU08 ascii[] = {
    /* */ 0x00,0x00,
    /*!*/ 0x00,0x00,
    /**/ 0x00,0x00,
    /*#*/ 0x00,0x00,
    /*$*/ 0x00,0x00,
    /*%*/ 0x00,0x00,
    /*&*/ 0x0,0x00,
    /*('*/ 0x00,0x00,
    /*)*/ 0x00,0x00,
    /**/ 0xe9,0x3,
    /**/ 0xe0,0x0,
    /**/, 0x00,0x00,
    /*-*/ 0x40,0x0,
    /*0*/ 0x16,0x34,
    /*1*/ 0xA0,0x0,
    /*2*/ 0x54,0x14,
    /*3*/ 0x50,0x34,
    /*4*/ 0x52,0x24,
    /*5*/ 0x56,0x24,
    /*6*/ 0x56,0x24,
};
```

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4.2.3.2.3 Scrolling a String Across the LCD Display — Scroll_String()

The example application provides the Scroll_String(U8 *string, byte local_length) function to scroll an alphanumeric character string across the LCD display. The complete source code for Scroll_String() can be found in lcddrv.c. The Scroll_String() function accepts a string argument of length n, manipulates it, and calls output_strg_lcd(). Table 9 shows a specific example of how the string is manipulated by the Scroll_String() function for a 5 character LCD display. In this example, the string argument provided to the Scroll_String() function is “HELLO.” A call to the CLEAR_ALL_ALPNUM() function, a delay, and a call to the output_strg_lcd() function are executed between each of the sequences.
4.2.3.3 LCD Events / Interrupts

For the demo application, the LCD interrupts are initialized in lcddrv.h and the interrupt handler is found in vectors.c. The LCD interrupt handler toggles the GPIO port (PTC2), configured as an output, which is connected to the DEMO9S08LC60 speaker.

4.2.4 main()

The main() program function for this application is very simple, providing the following functionality:

- Initialize ports, ICG, SCI, LCD
- Initialize interrupts
- Display the default message
- Provide application loop

Below is the source code for the program loop in main():

```c
//Program LOOP
while (1) {
    //Check incoming data
    result = AS1_RecvChar(&c);
    if (result == ERR_OK ) {
        sci_input[inputcounter] = c;//toupper(c);
        inputcounter = inputcounter + 1;
        if (c == '\r') {
            sci_input[inputcounter-1] = '\0';
            string = sci_input;
            Scroll_String(string, strlen(string));
            inputcounter = 0;
            cmdstring[0] = ' ';
            for (i=0; i <= 30; i++) sci_input[i] = 0;
```

---

Table 9. String Argument Manipulation by the Scroll_String Function

<table>
<thead>
<tr>
<th>String Argument Manipulation Sequence</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
</tr>
<tr>
<td>HE</td>
</tr>
<tr>
<td>HEL</td>
</tr>
<tr>
<td>HELL</td>
</tr>
<tr>
<td>HELLO</td>
</tr>
<tr>
<td>ELLO</td>
</tr>
<tr>
<td>LLO</td>
</tr>
<tr>
<td>LO</td>
</tr>
<tr>
<td>O</td>
</tr>
</tbody>
</table>

---
//Change BAT and VOL randomly
SET_VOL(SEGONOFF, ON, 5-1);
SET_BATT(SEGONOFF, ON, 1);
if (l == 4) l=0;
else l++;;

//Display default message
DefaultDisplayMessage();
}
}

The loop polls the SCI0 data register and, when validate is available, stores the SCI0 data into the array sci_input[inputcounter]. The inputcounter variable counts the total number of SCI0 characters stored in sci_input. When the SCI0 data received is equal to a carriage return, ‘\r’, the Scroll_String() function is called with sci_input as an argument. After the scrolling of the data on the LCD display, the sci_input array is cleared and a default LCD message is displayed. Afterwards, the program loop continues polling the SCI0 data register for the next user input.

4.3 Example Application Operation and Quick Start

The demo uses the FBE ICG configuration so it used the DEMO9S08LC60 32.768 kHz crystal clock source. The code in AN3280SW1.zip is pre-configured to drive the LCD panel at 3 Volts with a 1/4 duty cycle and a frame frequency of 64 Hz. The following instructions provide a walkthrough of the demo operation.

1. Unpack the DEMO9S08LC60 and place jumpers as described in Table 8.
2. Connect a serial cable from a PC to the DEMO9S08LC60.
3. Connect an USB cable from a PC to the DEMO9S08LC60.
4. Unzip the AN3280SW1.zip file
5. Start Code Warrior and open the project file for the demo found in the AN3280SW1.zip file
   — Project file is named AN3280_CWPRJ.mcp
   — Ensure the Code Warrior version supports the MC9S08LC60. For Code Warrior 5.1, an MC9S08LC60 patch is available from Freescale.com. Alternatively, Code Warrior 5.x can be used with an MC9S08LC60 driver provided by SofTech Microsystems. The SofTech Microsystems is provided on a CD in the DEMO9S08LC60 kit.
6. With the AN3280_CWPRJ.mcp open, select SofTech as the programming target and then start the program download. This will start the Code Warrior debugger, Hiwave. The Code Warrior IDE icon that executes the command to start the debugger is the following:

7. At this point, the Hiwave debugger will erase and reprogram the DEMO9S08LC60 — several dialog boxes will show the reprogramming progress.
8. After the reprogramming is complete, press Hiwave debugger the run icon to start the demo. The Hiware icon that executes the command to run the program is the following:

9. LCD will display a “HELLO WORLD” message.

10. After the “HELLO WORLD” message, the default LCD text message – “9S08LC60” – is displayed.

11. Start a terminal program on the PC and configure the baud rate to 110 bps. (Note AN3280SW1.zip includes a pre-configured HyperTerminal session file, AN3280_Term.ht, that can be used for this part of the demo)

12. Type an alphanumeric character sequence in the terminal program and then press the Enter key.
   a) The program will not change the display until the Enter key is pressed.
   b) You can press the Enter key without entering alphanumeric.
   c) Every time the Enter key is pressed, the LCD battery and volume indicators change.

13. After the Enter key is pressed, the new data will scroll across the LCD display. As the message scrolls, the program will not accept any additional input from the terminal program.

14. When the display is complete, the default LCD text message will be displayed.
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