

# Freescale Semiconductor

**Application Note** 

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# **LCD Driver Specification**

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# 1 Introduction

This document describes a driver for twisted nematic displays (TN) or super twisted nematic displays (TNS). This LCD driver allows the user to customize glass requirements with the microcontroller LCD module.

This driver was tested for the following microcontroller demo boards: the TWR-K40X256, TWR-K53N512, MCF51EM256 (DEMOEM), MC9S08LG32 (DEMO9S08LG32), MC9S08LL16 (DEMO9S09LL16), MC9RS08LA8 (DEMO09RS08LA8), and MC9RS08LE4 (DEMO09RS08LE4).

The software architecture was designed to provide seamless migration between these devices.

This document is intended to be used by all software development engineers, test engineers, and anyone else who has to use the microcontrollers with an LCD.

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#### Introduction to the Twisted Nematic (TN) LCD

Issues and suggestions about this document and driver must be provided through the support webpage at www.freescale.com/support.

# 2 Introduction to the Twisted Nematic (TN) LCD

A twisted nematic (TN) display is used in most LCD applications; this device consists of a nematic liquid crystal sandwiched between two plates of glass. The inner glass surfaces are coated with a transparent metal oxide film that acts as an electrode. The surfaces are used to apply voltages across the cells.



Figure 1. Twisted nematic LCD

### 2.1 Principle of Operation

The light entering the display from the top is polarized by the top polarizer and as it travels through the liquid crystals its polarization rotates with the molecules. When it emerges its polarization has been changed by 90 degrees and passes through the bottom polarizer.

However, when a voltage is applied to the electrodes the majority of the molecules are arranged vertically. In this case, polarization of the light passing through the liquid crystals remains unchanged and the light beam is blocked by the bottom polarizer.

### 2.2 Types of TN LCDs

There are two types of LCDs:



• Static LCDs — Have only one backplane (BP) electrode and one frontplane (FP) pin to turn on one segment. Therefore, a 7-segment display needs eight terminals to display one character. If you increase the number of segments you increase the number of terminals, the cost of the LCD, and the total cost of the application.



• Dynamic LCDs — Help optimize the required number of terminals needed to drive the LCD. Instead of having one BP and one FP, dynamic displays reduce the number of terminals by multiplexing the segments; this means that two or more segments are connected to the same terminal. The way that dynamic displays work is similar to keyboards.



Figure 3. Dynamic LCD

For further information about dynamically driven LCDs, see Freescale application note *XGATE Library: TN/STN LCD Driver* (document AN3219), available on the Freescale.com website.



LCD Software

# 3 LCD Software

### 3.1 LCD Software Architecture



Figure 4. Software architecture

Hardware abstraction layer (HAL) — The hardware abstraction layer is defined as a collection of software components that directly access hardware resources. In this layer, the LCD driver defines macros and functions that configure the custom glass requirements in the LCD module registers.

Hardware independent layer (HIL) — The hardware independent layer is defined as a collection of software components that access hardware resources through HAL. Peripheral drivers are implemented in this layer.

# 4 Hardware Abstraction Layer (HAL) Implementation

The HAL module defines the macros and functions needed to match the custom glass hardware specifications with the MCU LCD registers. This information is obtained from each custom glass specification. The changes to these macros are performed through modifications on the macro definitions located in the LCD\_HAL.h header file. The user functions are located in LCD\_HAL.c.

The user is responsible for providing correct definitions for these hardware access macros. Descriptions of the individual macros and examples of their definitions are given below.

### 4.1 \_LCDBACKPLANES

The LCD backplane macros defines the number of backplane electrodes on the custom glass. The duty ratio of the waveforms generated by the LCD module is 1/(\_LCDBACKPLANES).

This module supports values from 1–8.

Example	1	
Example		

|--|--|--|



### 4.2 \_LCDCLKPSL

This macro defines the LCD clock prescaler which in combination with the number of backplanes of the LCD, and determines the LCD frame frequency.

The LCD frame frequency is the number of times that the LCD is energized per second. The LCD module frame frequency must be selected to prevent the LCD display from flickering (LCD module frame frequency is too low) or ghosting (LCD module frame frequency is too high).

To avoid these conditions, an LCD module frame frequency in the range of 28 Hz to 58 Hz is required. Table 1 shows the LCD frame frequency calculations.

Duty Cycle	1/1	1/2	1/3	1/4	1/5	1/6	1/7	1/8
Y	16	8	5	4	3	3	2	2
LCLK[2:0]								
0	76.3	76.3	81.4	76.3	81.4	67.8	87.2	76.3
1	61	61	65.1	61	65.1	54.3	69.8	61
2	50.9	50.9	54.3	50.9	54.3	45.2	58.1	50.9
3	43.6	43.6	46.5	43.6	46.5	38.8	49.8	43.6
4	38.1	38.1	40.7	38.1	40.7	33.9	43.6	38.1
5	33.9	33.9	36.2	33.9	36.2	30.1	38.8	33.9
6	30.5	30.5	32.6	30.5	32.6	27.1	34.9	30.5
7	27.7	27.7	29.6	27.7	29.6	24.7	31.7	27.7

Table 1. LCD frame frequency calculations

### 4.3 \_LCDCLKSOURCE

The \_LCDCLKSOURCE macro defines the LCD module clock source. The available clocks are the internal (also called alternate) clock or the external clock of 32.768 kHz. The supported clock range is from 30 kHz to 39.053 kHz.

- 0 Selects external clock source
- 1 Selects alternate clock source

### 4.4 \_LCDVSUPPLY

The LCD voltage supply macro defines whether the LCD module power supply is internal or external. These device sources change in each device.

Supply sources for the MCF51EM256 are:

- 0 Drive VLL2 internally from  $V_{DD}$
- 1 Drive VLL3 internally from V<sub>DD</sub>
- 2 Drive VLL1 internally from  $V_{LCD}$
- 3 Drive VLL3 externally Or V<sub>IREG</sub>



#### Hardware Abstraction Layer (HAL) Implementation

Supply sources for the MC9S08LG32 are:

- 0 Drive VLL2 internally from V<sub>DD</sub>
- 1 Drive VLL3 internally from V<sub>DD</sub>
- 2 Reserved
- 3 Drive VLL3 externally

Supply sources for the MC9S08LL16 are:

- 0 Drive VLL2 internally from  $V_{DD}$
- 1 Drive VLL3 internally from V<sub>DD</sub>
- 2 Drive VLL1 internally from  $V_{LCD}$
- 3 Drive VLL3 externally Or V<sub>IREG</sub>

Supply fsources for the MC9RS08LA8:

- 0 Drive VLL2 internally from V<sub>DD</sub>
- 1 Drive VLL3 internally from V<sub>DD</sub>
- 2 Reserved
- 3 Drive VLL3 externally

Supply sources for the MC9RS08LE4:

- 0 Reserved
- 1 Drive VLL3 internally from V<sub>DD</sub>
- 2 Reserved
- 3 Drive VLL3 externally

### 4.5 \_LCDCPSEL

The LCD charge pump selector macro defines the type of supply for the LCD voltages VLL1, VLL2, and VLL3. The LCD module provides two options for the supply voltage: resistor network or charge pump.

This option is only available for the MCF51EM256, MC9S08LG32, MC9S08LL16, and MC9RS08LA8 families. The defined value (0 or 1) will not affect the MC9RS08LE4.

- 0 Selects the resistor network
- 1 Selects the charge pump

### 4.6 \_LCDLOADADJUST

The load adjust macro configures the LCD module to manage a different LCD glass capacitance. The capacitance of the LCD depends on the custom glass. The value written in this macro is related to the type of voltage source selected (resistor network or charge pump). The results for the different possible combinations of \_LCDLOADADJUST and \_LCDCPSEL (0 — resistor network, 1 — charge pump) for the MCF51EM256, MC9S08LG32, and MC9S08LL16 are shown in Table 2.



# Table 2. \_LCDLOADADJUST and \_LCDCPSEL Combinations for the MCF51EM256, MC9S08LG32, and MC9S08LL16

_LCDLOADADJUST	_LCDCPSEL = 1	_LCDCPSEL = 0
0	8000 pf	2000 pf
1	6000 pf	2000 pf
2	4000 pf	8000 pf
3	2000 pf	8000 pf

The configuration for the MC9RS08LA8 and MC9RS08LE4 devices are shown in Table 3. For the MC9RS08LA8 the value of \_LCDCPSEL must be 0 and for the MC9RS08LE4 the value does not matter.

Table 3. \_LCDLOADADJUST Configuration for the MC9RS08LA8 and MC9RS08LE4

_LCDLC	ADADJUST
0	2000 pf
1	2000 pf
2	8000 pf
3	8000 pf

### NOTE

For further details consult the specific device reference manual.

### 4.7 \_LCDFRAMEINTERRUPT

The LCD frame interrupt macro enables an LCD interrupt that matches the LCD module frame frequency.

- 0 Disable interrupt
- 1 Enable interrupt

### 4.8 \_LCDBLINKRATE

The LCD blink rate macro selects the frequency at which the LCD display blinks when the software blink control is enabled. This macro can be any number between 0–7. Equation 1 shows how the LCD blink rate is calculated.

LCD module blink rate = 
$$\frac{LCDCLK}{2^{(12 + LCDBLINKRATE)}}$$

Eqn. 1

Table 4 shows calculations for all values of BRATE.



#### Hardware Abstraction Layer (HAL) Implementation

BRATE[2:0]	0	1	2	3	4	5	6	7
LCD Clock				Blink Freq	uency (Hz)			
30 kHz	7.32	3.66	1.831	.916	.46	.23	.11	.06
32.768 kHz	8	4	2	1	.5	.25	.13	.06
39.063 kHz	9.54	4.77	2.38	1.19	.6	.30	.15	.075

#### Table 4. BRATE values

#### NOTE

The MCF51EM256, MC9S08LG32, MC9S08LL features software control. The MC9RS08LA and MC9RS08LE do not feature software control.

### 4.9 \_CHARNUM

This macro defines the number of alphanumerics for the custom glass.



Figure 5. 16-Segment alphanumeric

### 4.10 LCDTYPE

This macro defines the number of LCDWF needed to drive one alphanumeric of the custom glass.

### 4.11 Custom Glass Configuration

The macros explained below interface between the custom glass pinout and the MCU LCD module. The user must take into consideration that these are only definitions, therefore these macros are needed and called in the LCD\_HAL.c file.

#### NOTE

Download the AN3796SW.zip file from www.freescale.com for a spreadsheet that helps in filling-out these definitions.



#### **EnableLCDpins** 4.11.1

Description:

Enable the corresponding LCD pin for the LCD operation.

Name:

EnableLCDpins(RegNum, Mask)

Parameters:

- RegNum Number of the register to write •
- Mask Mask to habilitate LCD pins •

**Excel Location:** 

```
Sheet — Custom Glass Column— L
```

Example 2.

#define	PENO()	EnableLCDpins (0,255)	//LCD0-LCD7 are LCD pins	
1 act the		110010100prind (0,1000)	,,,,, die Find	

#### **EnableBackplanes** 4.11.2

Description:

Enable the corresponding LCD pin for the backplane operation.

Name:

```
EnableBackplanes(RegNum, Mask)
```

Parameters:

- RegNum Number of the register to write •
- Mask Mask to select backplanes ٠

**Excel Location:** 

Sheet — Custom Glass Column — M

		Example 3.	
#define	_BPEN0()	EnableBackplanes (0, 15)	// LCD0-LCD3 are backplanes

#### **SetBackplane** 4.11.3

Description:

Gives the common number (COM) to the previous enabled backplanes.

Name:

```
SetBackplane(ComNum, LCDn)
```

Parameters:

ComNum — COM number •



#### Hardware Abstraction Layer (HAL) Implementation

• LCDn — Number of the LCD pin connected to the backplane

Excel Location:

```
Sheet — Custom Glass Column — N
```

#### Example 4.

```
#define _SETCOM1() SetBackplane (1, 3) /LCD3 like COM1
```

### 4.11.4 \_CharacterPlace

Description:

This macro defines the LDCWaveforms directions needed to write the alphanumeric character.

Name:

CharacterPlace(LCDn)

Parameters:

• LCDpin — LCDn number of pin connected

Excel Location:

```
Sheet — Custom Glass Column — O
```

Example 5.

#define	CHAR1A (5)	//Alphanumeric 1 drive by LCD5 and LCD15
#define	CHAR1B (15)	

### 4.11.5 AllSegmentsON

Description:

Turns on all the segments of the custom glass.

Name:

```
AllSegmentsON(LCDn, Mask)
```

Parameters:

- LCDn LCDWF that needs to be turned on
- ComMask Bits that are turned on (example: 8 COM = 255, 2 COM = 3)

Excel Location:

Sheet — Custom Glass Column — Q

#### Example 6.

define SEGMENT10_ON AllSegments	ON (14,	255)	//Turns	on all	the bits	from the	e LCD14
---------------------------------	---------	------	---------	--------	----------	----------	---------



### 4.11.6 SymbolON

Description:

Turns on the segment dedicated to write a specific symbol.

Parameters:

BufferNumber — Number of the LCD pin connected

BitNumber — COM number that turns on that symbol

Excel Location:

Sheet — Special Symbols Column — H

Example 7.





### 4.11.7 SymbolOFF

Description:

Turns off the segment dedicated to write a specific symbol.

Name:

SymbolOFF(LCDn, BitNumber)

Parameters:

- BufferNumber Number of the LCD pin connected
- BitNumber COM number that turns on that symbol

Excel location:

Sheet — Special Symbols Column — I

#### Example 8.

|--|

### 4.12 LCD fault detection

LCD fault detection capability is included only in Kinetis family.

Most failures, where a LCD segment is either unexpectedly on or off, result in an erroneous information that can mislead a user and cause a dangerous situation. The LCD Display Fault Detect circuit's (LFD)



#### Hardware Abstraction Layer (HAL) Implementation

function find faults in the LCD display, display connector, and board connections between the MCU and the display.

The LCD panel can be regarded as a matrix of segments, as shown in the following figures. Any open/short connection changes the capacitive characteristics of the segment matrix. The pullup fault detection checks the capacitive characteristic of an LCD\_Pn pin by applying a weak pullup voltage to the display capacitor matrix. The rise time response is sampled and summed within a user defined time frame and stored for post-processing in the FDSR[FDCNT] bitfield. The summing of the digitized values uses the principal that the response of a capacitor for each charge/discharge is constant for the same loading conditions.

For more information on how LCD fault detection operates see "LCD fault detect circuit" section in the Reference Manual of K30 and K40 families available from www.freescale.com/kinetis.

### void vfnlcd\_pinmux(uint8 mux\_val)

Description:

Configure LCD pin to operate in LCD normal operation or for fault detection. To measure the capacitance of the pin all LCD pins through the port control must be configured for ALT7 function which enable the LCD fault circuitry. During LCD normal operation the port control must be configured for ALT0.

### NOTE

When fault detect measurement is performed, a glitch on the LCD is observed due to handling of pull ups used for the fault detect circuit. Once the fault detection is completed the user must ensure to return the port control to ALT0 configuration.

ANSIC prototype:

void vfnlcd\_pinmux(uint8 mux\_val)

#### Input parameters:

mux\_val = 0 —Configure LCD pins for LCD normal operation

mux\_val = 7 — Configure LCD pins for LCD fault detection. Configuring to these values allow the LCD Fault detect circuitry to control the pull-up for proper measurement.

#### NOTE

A glitch on the LCD, or darkness on the LCD occurs while Fault detection is on progress.

Return value:

None

### uint8 u8fnlcd\_measure\_pin\_cap(uint8 pinid, uint8 pin\_type, uint8 fdprs\_val, uint8 fdsww\_val)

Description: measure capacitance of the pin

ANSIC prototype:

uint8 u8fnlcd\_measure\_pin\_cap(uint8 pinid, uint8 pin\_type, uint8 fdprs\_val,uint8 fdsww\_val)

Input parameters:

#### Hardware Abstraction Layer (HAL) Implementation



(LCDWF number 0-47)	
FP_TYPE=0 or BP_TYPE =	1
Fault detect preescaler value	0-7
Fault detect windows width	0-7
	(LCDWF number 0-47) FP_TYPE=0 or BP_TYPE = Fault detect preescaler value Fault detect windows width

Return value:

capacitance value (FDSR\_FDCNT)

#### Example 9.

#### Measuring capacitance of all LCD pins enabled.

```
void ldc_fault_detection_measure(void)
  {
    uint8 i;
   uint8 lcd_cap_val;
   vfnlcd_pinmux(7);
    for(i=0;i<_LCDUSEDPINS;i++)</pre>
    {
      if (i<_LCDFRONTPLANES)
      {
      lcd_cap_val =
u8fnlcd_measure_pin_cap(WF_ORDERING_TABLE[i],FP_TYPE,lcd_fp_fdprs_val,lcd_fp_fdsww_val);
      }
      else
     {
       lcd_cap_val =
u8fnlcd_measure_pin_cap(WF_ORDERING_TABLE[i],BP_TYPE,lcd_bp_fdprs_val,lcd_bp_fdsww_val);
     ł
     lcd_pin_cap[i] = lcd_cap_val;
    }
   vfnlcd_pinmux (0);
  }
```



LCD HAL Functions

# 5 LCD HAL Functions

# 5.1 vfnLCD\_Init

Description:

Initializes the LCD with the parameters given in the LCD\_HAL.h.

ANSIC prototype:

void vfnLCD\_Init (void);

Input parameters:

None

Return value:

None

Typical usage:

```
main()
{
    vfnLCD_Init();
    for (;;)
    {    }
}
```

### CAUTION

This caution is for MCF51EM256 devices. LCD registers require several cycles between write accesses. Only byte writes should be used to write these registers. Finally, consecutive writes must be seperated by at least one non-write cycle.

# 5.2 vfnLCD\_EnablePins

Description:

Provides the LCD characteristics to the MCU pins previously selected in the LCD\_HAL.h file. This function is used on vfnLCD\_Init. The LCD characteristics help with the generation of the waveforms that drive the liquid crystal display.

ANSIC prototype:

```
void vfnLCD_EnablePins (void)
```

Input parameters:

None

Return value:

None



### 5.3 vfnLCD\_ConfigureBackplanes

Description:

Enables an LCD pin to be a backplane and gives the number of the COM corresponding to each backplane. This function is used for the LCD initialization sequence and therefore is called from vfnLCD\_Init.

ANSIC prototype:

void vfnLCD\_ConfigureBackplane (void)

Input parameters:

None

Return value:

None

### 5.4 vfnLCD \_Clear\_Display

#### Description:

Turns off all the segments of the LCD. This function uses the previous AllSegmentsON definition.

ANSIC prototype:

void vfnLCD\_Clear\_Display (void)

Input parameters:

None

Return value:

None

Typical usage:

```
#include "lcd.h"
main()
{
vfnLCD_Init();
vfnLCD_Clear_Display ();
for (;;)
{
}
```

### 5.5 vfnLCD \_Set\_Display

Description:

Turns on all the segments of the LCD. This function uses the previous AllSegmentsON definition.

ANSIC prototype:

void vfnLCD\_Set\_Display (void)



#### LCD HAL Functions

Input parameters:

None

Return value:

None

Typical usage:

```
#include "lcd.h"
main()
{
vfnLCD_Init();
vfnLCD_Set_Display ();
vfnLCD_Clear_Display ();
for (;;)
{
}
```

### 5.6 vfnLCD\_Home

Description:

Reset the pointer to the first alphanumeric position — LCD\_CharPosition = 0.

Input parameters:

none

Return value:

none

Typical usage:

```
#include "lcd.h"
main()
{
    vfnLCD_Init();
    LCD_writeMSG("")
    vfnLCD_Home();
    LCD_writeMSG("200"); "LCD result = 200 Volts"
    for (;;)
    {
    }
}
```

### 5.7 vfnLCD\_Write\_Char

Description:

Writes one ASCII character on the next alphanumeric. If the alphanumeric counter reaches the maximum value, vfnLCD\_Home is called.

ANSIC prototype:

```
void vfnLCD_Write_Char (UINT8 u8Value);
```



Input parameters:

• UINT8 — u8Value (ASCII to write on the alphanumeric)

Return value:

none

Typical usage:

```
#inlcude "lcd.h"
main()
{
vfnLCD_Init();
vfnLCD_Write_Char (`a');
for (;;)
    {
}
```

### 5.8 vfnLCD\_Write\_MsgPlace

Description:

Write a message on an LCD alphanumeric specific position. Write only the number of characters specified by the user.

ANSIC prototype:

```
vfnLCD_Write_MsgPlace(UINT8 _POINTER pu8Message, UINT8 u8NumChars, UINT8 u8Place)
```

Input parameters:

- UINT8\*pu8Message The first character on the array to write
- UINT8 u8NumChars The numbers of characters to write
- UINT8 u8Plcae— Address alphanumeric

Return value:

None

Typical usage:

```
#inlcude "lcd.h"
const byte cbaMessage1 [ ] = {"123456"};
main()
{
    vfnLCD_Init();
    vfnLCD_Write_Char ('a');
    vfnLCD_Write_MsgPlace(&cbaMessage1[0], 3); //Only write "123"
    for (;;)
        { }
}
```



LCD HAL Functions

# 5.9 vfnLCD\_Write\_Msg

Description:

Write a message to the LCD alphanumeric space. If the message is longer than the maximum number of alphanumeric characters, it will truncate the message. If the message is shorter than the maximum number of alphanumeric characters it will write the message and the remaining alphanumeric characters will be written with spaces.

ANSIC prototype:

void vfnLCD\_Write\_Msg (UINT8 \_POINTER pu8Message);

Input parameters:

• UINT8 \* pu8Message — The first character on the array to write

Return Value:

none

Typical Usage:

```
#inlcude "lcd.h"
const byte cbaMessage1 [] = {"123456"};
main()
{
vfnLCD_Init();
vfnLCD_Write_Char (`a');
vfnLCD_Write_Msg (&cbaMessage1[0];
for (;;)
    {
}
```

### 5.10 vfnLCD\_Contrast

Description:

The software contrast is updated with the value of the input parameter. The permitted values for the contrast are in the range 0x00-0x0F. By increasing the value the glass becomes more opaque, decreasing the value of the contrast makes the LCD more transparent.

Prototype:

```
vfnLCD_Contrast (UINT8 Contrast)
```

Input parameters:

byte — contrast

Return value:

None



### Typical usage:

```
#include "lcd.h"
main()
{
    vfnLCD_Contrast(15);
    for (;;)
{ }
}
```

### NOTE

The contrast function can only be used with the MCF51EM256 and MC9S08LL16 devices.

### 5.11 vfnLCD\_GoTo

Description:

Move cursor to a specific position.

Prototype:

vfnLCD\_GoTo (Place)

Input parameters:

UINT8 — Place,

Return value:

None

Typical usage:

# 6 Custom Glass Implementation

Using good practices to configure the LCD hardware pinout simplifies the software. Consider the following:

- Drive one segment with the same backplane at all alphanumerics.
- Arrange all the symbols on the same pin.
- Increasing the number of backplanes increases the number of segments that are possible to drive.

When customizing software for a specific glass, the following steps must be executed:

1. Make a new project for the MCU that you select.



#### **Custom Glass Implementation**

- 2. Add the files LCD\_HAL.h, LCD\_HAL.c, and LCD.h to the project.
- 3. Include LCD.h in main (#include "LCD.h").
- 4. Open the LCD\_HAL.h., customize the LCD configuration macros and uncomment the MCU from that application, leaving the other two MCUs (not used) commented, as shown.



- 5. Open the spreadsheet and fill the columns in the custom glass worksheet that are mentioned below.
  - Pin (Column A) Custom glass pin
  - LCDn (Column B) Number of the MCU LCD pin connected to the custom glass
  - BP(Column C) COM number connected to the pin; if that pin is not a backplane leave the cell blank
  - ChNx (Column D) Select the number of alphanumeric characters that the custom glass is driving. When alphanumerics need more than one pin, the alphabet will be used for identification.

Pin	LCDn	BP	ChNx
1	0	1	
2	1	2	
3	2	3	
4	3	4	
5	4		1a
6	5		1b
7	6		1c
8	7		1d
9	8		2a
10	9		2b
11	10		2c
12	11		2d
13	12		7a
14	13		7b
15	33		7c
16	34		7d
17	41		8a
18	42		8b
19	43		8c
20	14		8d
21	15		9a
22	16		9b
23	28		9c

#### Table 5. Spreadsheet

Pin	LCD <i>n</i>	BP	ChNx		
24	27		9d		
25	26				
26	25				
27	24				
28	23				
29	22		6d		
30	36		6c		
31	35		6b		
32	21	6a			
33	20	5d			
34	19	5c			
35	18	5b			
36	17	5a			
37	32		4d		
38	31		4c		
39	40	4b			
40	39	4a		39	
41	38		3d		
42	37		Зс		
43	30		3b		
44	29		За		

### Table 5. Spreadsheet (continued)

6. Copy the information shown in Table 6 from the custom glass worksheet into the LCD\_HAL.h.

Column	Place in LCD_HAL.h
М	EnableLCDPins
N	EnableBackplanes
0	SetBackplanes
Р	CharacterPlace
R	SegmentsON
S	SegmentsOFF

### Table 6. Custom glass worksheet to LCD\_HAL.h



#### **Custom Glass Implementation**

Copy from the worksheet:

Set Backplane Macro				
#define	SetCom1()	SetBackplane (0, 0)		
#define	SetCom2()	SetBackplane (1, 1)		
#define	SetCom3()	SetBackplane (2, 2)		
#define	SetCom4()	SetBackplane (3, 3)		

Table 7. Example for SetBackplane macros

Paste to the project:

P&E Multilink/Cyclone F Files Link Order Target	SetBackplane Give the COM number to the pr param: ComNum COM number */	evious enable Backplane LCDpin LCDn number of pin that its connected
✓     File       ✓     ⊖       Sources     ✓       ✓     □	<pre>#define _SETCOM1() #define _SETCOM2() #define _SETCOM3() #define _SETCOM4()</pre>	SetBackplane (0, 0) SetBackplane (1, 1) SetBackplane (2, 2) SetBackplane (3, 3)
CD_HAL.c     CD_HAL.h     CD_HAL.h     Co_HAL.h     Libs	/* Character place This macro defines the LDCWavef param: LCDpin LCDn number	roms directions needed for write the alphanumerics of pin that its connected */

- 7. On the worksheet named Special Symbols fill the columns: Name (Column C) — Name of the custom glass segments. Pin (Column D) — Custom glass connected to that symbol. COM (Column E) — Number of backplane or common that turns on that symbol.
- 8. Copy the columns shown in Table 8 from the special symbols worksheet and paste into the LCD\_HAL.h.

Table 8. Special symbols worksheet to LCD\_HAL.h

Column	Place in the LCD_HAL.h		
Н	SymbolON		
I	SymbolOFF		

- 9. Fill the columns explained below on the Map Segments worksheet. Copy column D and paste it in the LCD\_HAL.h at the Name Segment section.
- 10. Copy the columns shown in Table 9 from the Custom Glass worksheet and paste into the LCD\_HAL.c.

### Table 9. Custom glass worksheet to LCD\_HAL.c

Column	Place in LCD_HAL.c
Q	aPlace



### NOTE

Before pasting, column Q must be sorted in alphabetical order.

Before Sorting			After Sor
Q			CHARIA, // LCI
			CHARIB, // LCD
			CHARIC, // LCI
			CHARID, // LCI
aPlace			CHAR2A, // LCI
ĺ	l l		CHAR2B, // LCD
	î		CHAR2C, //LCD
	î		CHAR2D, // LCD
	î		CHAR3A, // LCD
CHARIA, // LCD4	Pin:S		CHAR3B, // LCD
CHAR1B, // LCD5	Pin:6		CHAR3C, // LCD
CHARIC, // LCD6	Pin:7		CHAR3D, // LCD
CHARID, // LCD7	Pin:8		CHAR4A, // LCD
CHAR2A, // LCD8	Pin:9		CHAR4B, // LCD4
CHAR2B, //LCD9-	Pin:10		CHAR4C, // LCD
CHAR2C, // LCD10	Pin:11		CHAR4D, // LCD
CHAR2D, //LCD11	Pin:12		CHARSA, // LCD
CHAR7A, // LCD12	Pin:13		CHAR5B, // LCD
CHAR7B, //LCD13	Pin:14		CHARSC, // LCD
CHAR7C, //LCD33	Pin:15		
CHAR7D, //LCD34	Pin:16		
CHAR8A, //LCD41	Pin:17		
CHAR8B, //LCD42	Pin:18		
CHAR8C, //LCD43	Pin:19		
CHAR8D, //LCD14	Pin:20		
CHAR9A, // LCD15	Pin:21		
CHAR9B, // LCD16	Pin:22		
CHAR9C, //LCD28	Pin:23		
CHAR9D, //LCD27	Pin:24		
Files Link Order Target	/** LCD w	aveform for T8 aPlace [	each alphanumeric */ 1 =
File	{		
🖂 🚍 Sources	CHAR1 CHAR1	A. // LCD4 B // LCD5	Pin:5
main.c	CHAR1	C. // ICD6	Pin:7
LCD_HAL.h	CHAR1	D. // LCD7	Pin:8
🕀 🧰 Includes	CHAR2	B, // LCD8	Pin:10
⊞r⊡ Libs ⊡ ⊖ Project Settings	CHAR2	C. // LCD1	0 Pin:11
H™ Froject Settings	CHAR2 CHAR3	A. // LCD1	1 Pin:12 9 Pin:44
	CHAR3	B, // LCD3	0 Pin:43
	CHAR3 CHAR3	C, // LCD3 D // LCD3	7 Pin:42 8 Pin:41
	CHAR4	A. // LCD3	9 Pin:40
	CHAR4	B, // LCD4	0 Pin:39
	CHAR4 CHAR4	D. // LCD3	2 Pin:37
	CHARS	A. // LCD1	7 Pin:36
	CHAR5 CHAR5	C, // LCD1	o Fin:35 9 Pin:34
	CHAR5	D. // LCD2	0 Pin:33
	CHAR6 CHAR6	A. ZZ LCD2 B. ZZ LCD3	1 Pin:32 5 Pin:31
	CHAR6	C. // ICD3	6 Pin:30
	CHAR6	D, 🛛 🖊 LCD2	2 Pin:29

#### After Sorting

CHAR1A, // LCD4 Pin:5
CHAR1B, // LCD5 Pin:6
CHARIC, // LCD6 Pin:7
CHAR1D, // LCD7 Pin:8
CHAR2A, // LCD8 Pin:9
CHAR2B, // LCD9 Pin:10
CHAR2C, // LCD10 Pin:11
CHAR2D, // LCD11 Pin:12
CHAR3A, // LCD29 Pin:44
CHAR3B, // LCD30 Pin:43
CHAR3C, // LCD37 Pin:42
CHAR3D, // LCD38 Pin:41
CHAR4A, // LCD39 Pin:40
CHAR4B, // LCD40 Pin:39
CHAR4C, // LCD31 Pin:38
CHAR4D, // LCD32 Pin:37
CHARSA, // LCD17 Pin:36
CHAR5B, // LCD18 Pin:35
CHAR5C, // LCD19 Pin:34

Figure 6.	Before	sorting	and	after	sorting
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Conclusion

11. Create your own alphanumeric definition in the ASCII sheet columns. After defining the segments, copy the AF column and paste it in the baASCII array.

If the application needs to declare text, the arrays that do this must be between the red lines in the main file after including headers and libraries. This is to save data in a special memory segment for the MC9RS08 devices. When using the LL family the memory will not be saved in a special place.

```
#pragma CONST_SEG __FAR_SEG mySEG
const byte Mytext [ ] = {"HELLO WORLD"};
#pragma CONST_SEG DEFAULT
```

- For the MCF51EM256, 13 segments will configure with four lines (chara, charb, charc, and chard). These are defined in aBackup[] array
- For the MC9S08LG32, 13 segments will configure with four lines (chara, charb, charc, and chard).
- For the MC9S08LL16, 16 segments configure the two lines (chara and charb). These are defined in baBackup[] array.
- For the MC9RS08LA8 and MC9RS08LE4, 13 segments configure the two lines (chara and charb).

You can take the information from the ASCII array.



# 7 Conclusion

This driver provides a software interface between the custom glass pinout and the LCD module in low-end Freescale microcontrollers. Minimal changes are required to customize an LCD application and fit specified hardware requirements. These routines are a reliable platform to migrate between the MC9RS08LE, MC9RS08LA, MC9S08LL, MC9S08LG32, MCF51EM256, K40 and K50 families and reduces development time when using Freescale products.



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