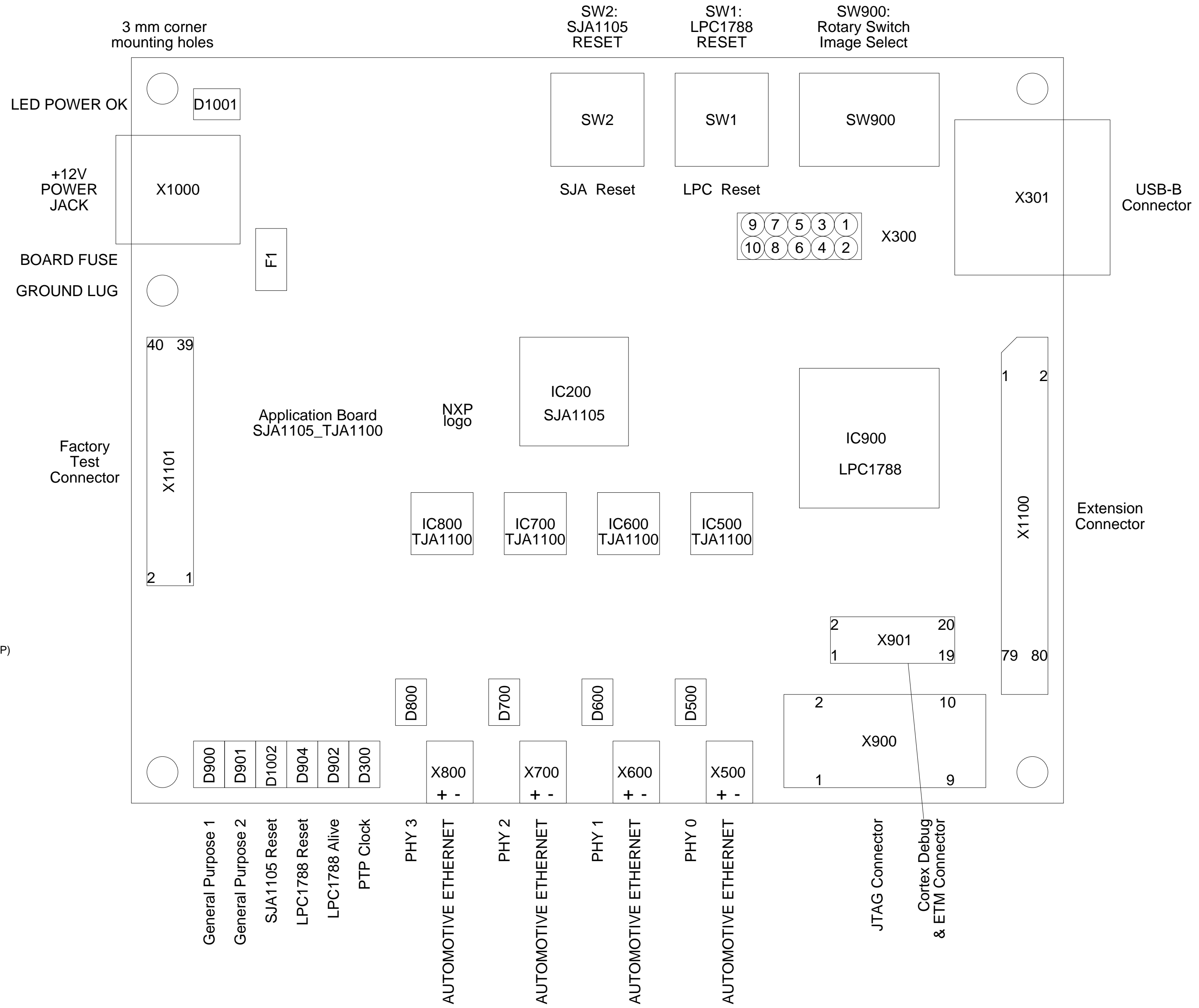
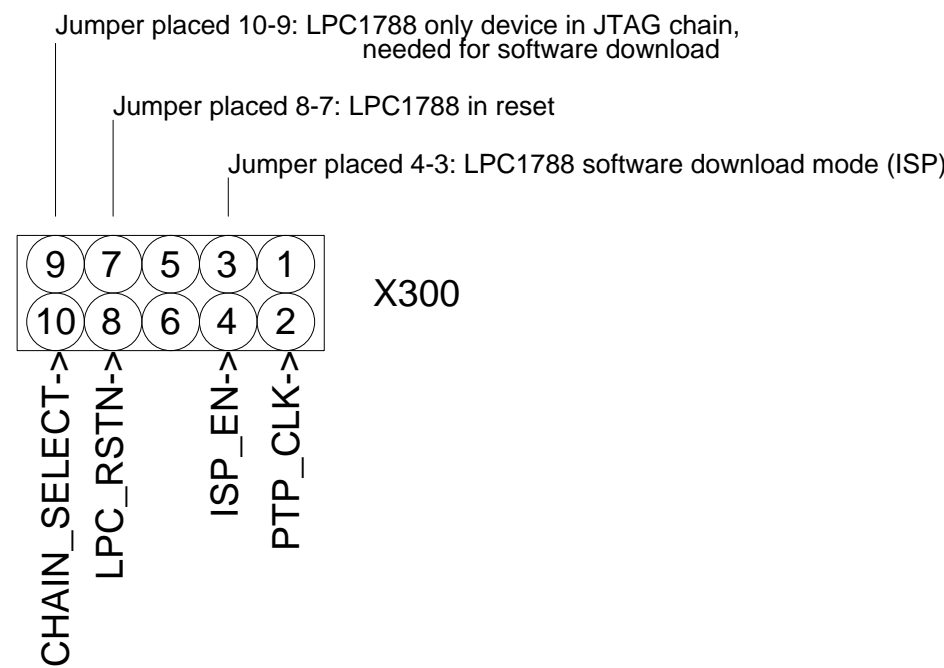
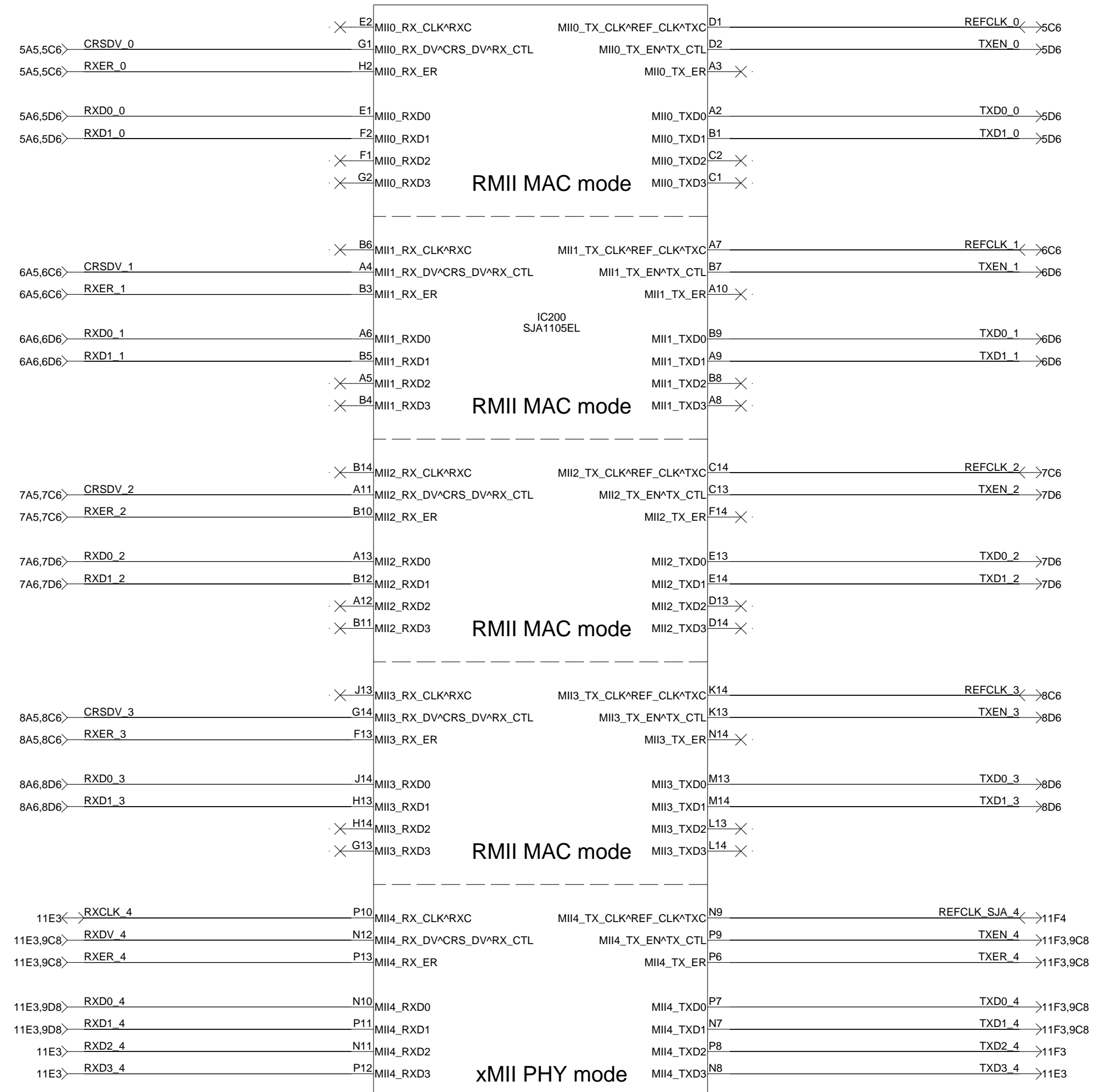


OVERVIEW

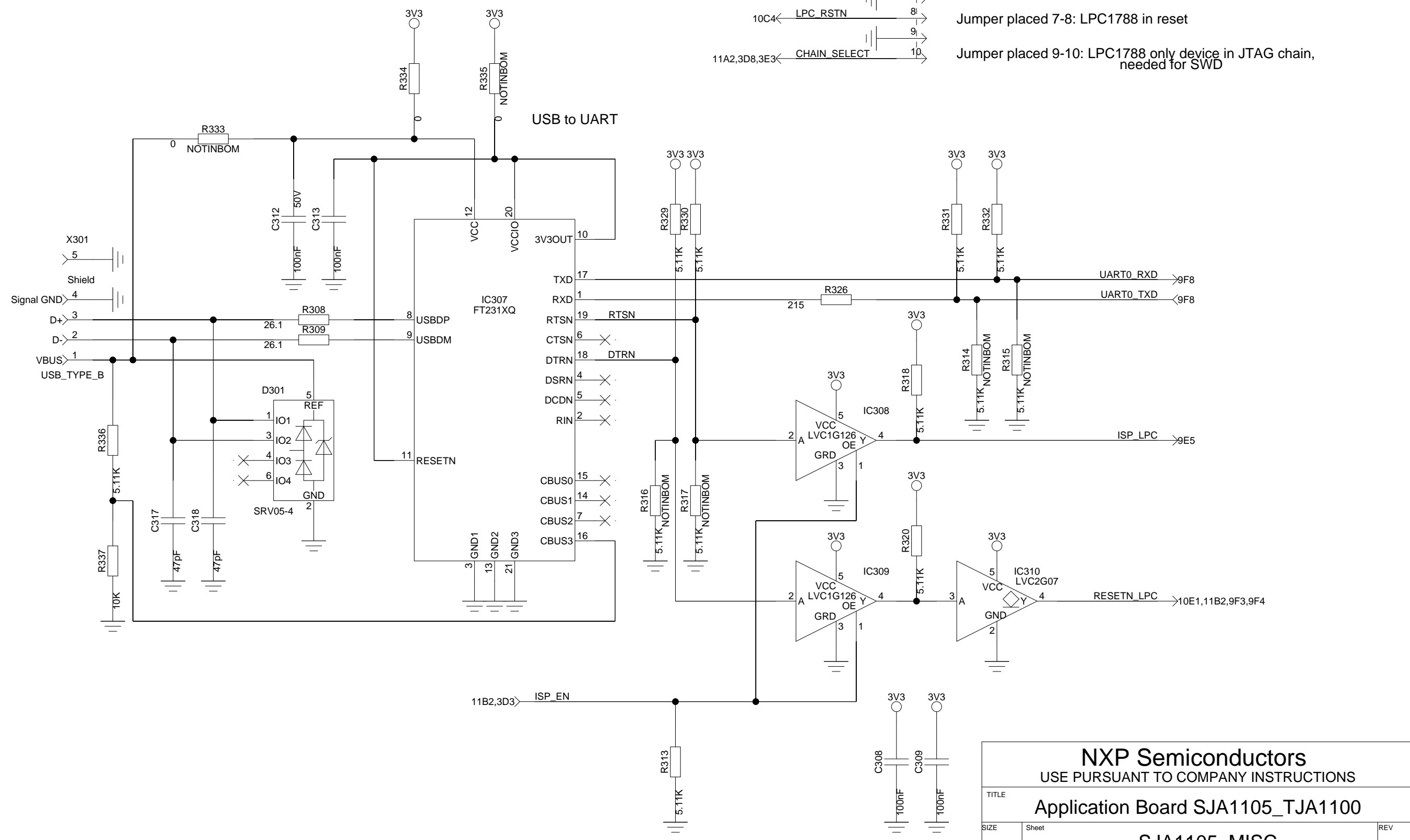
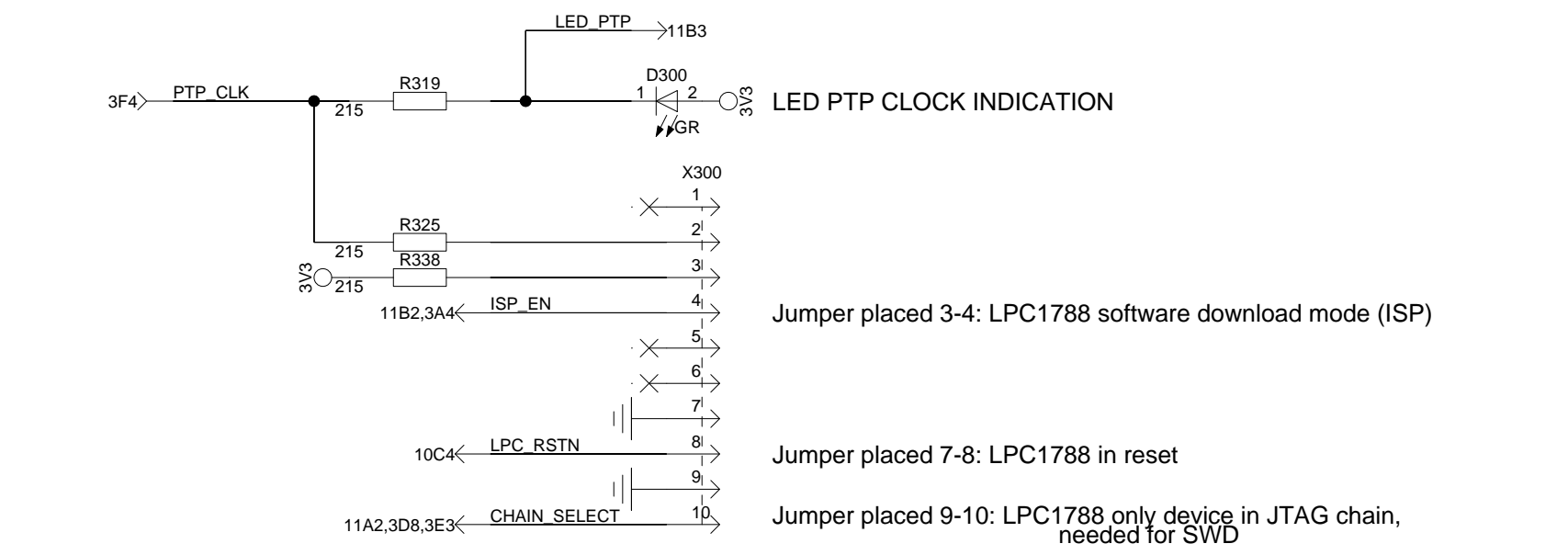
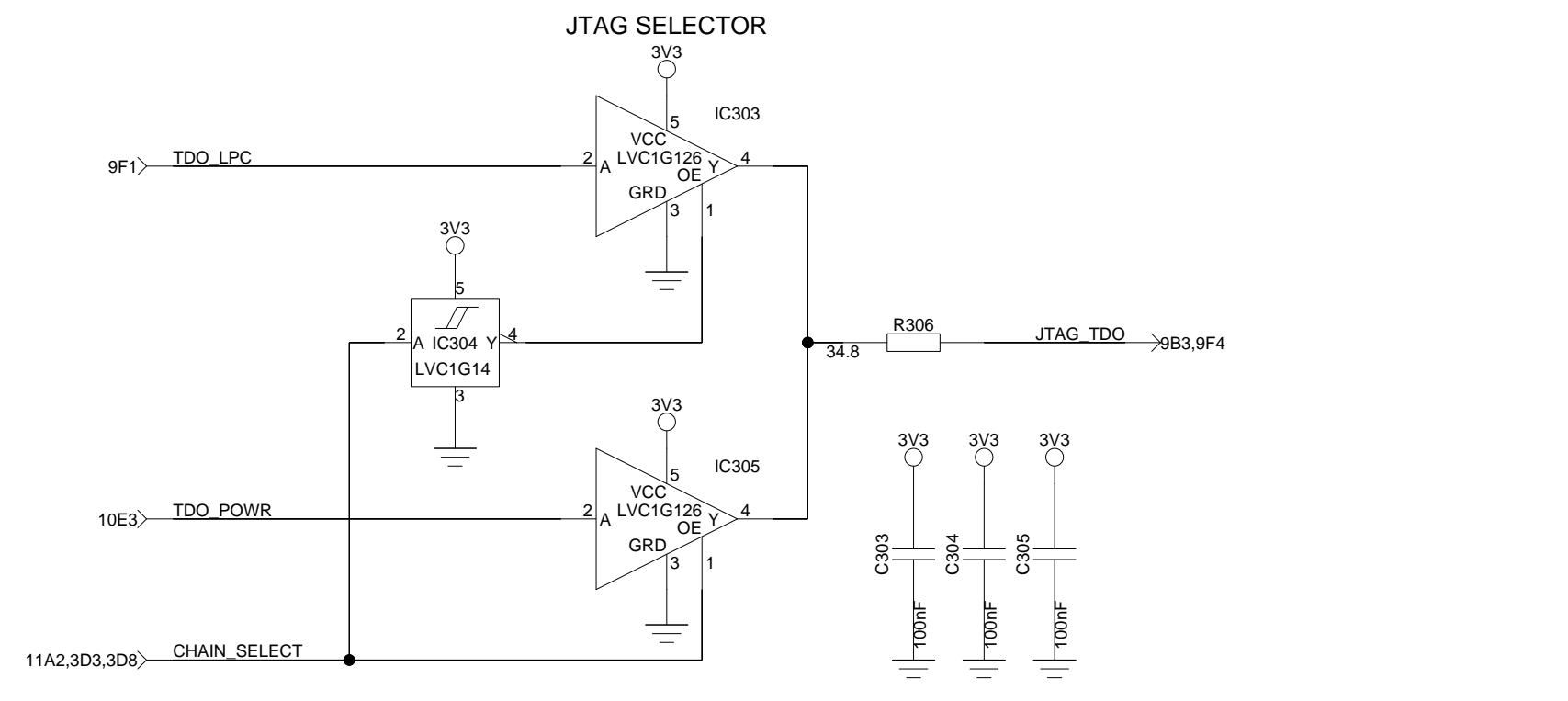
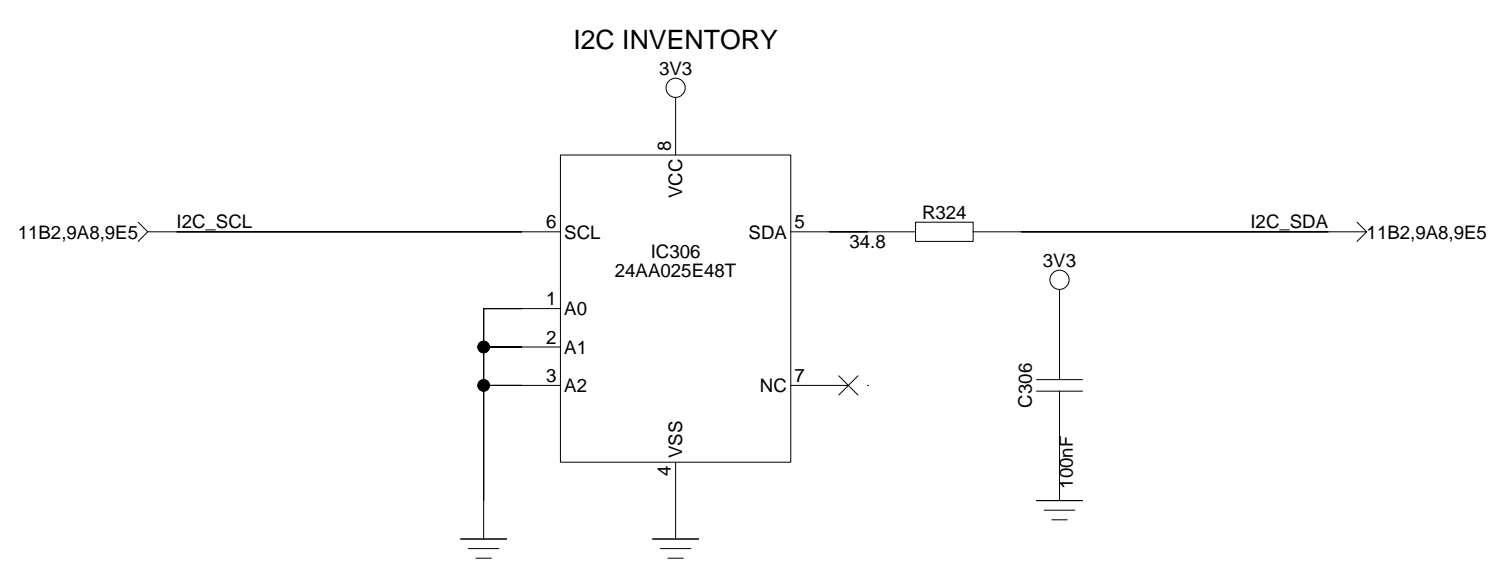
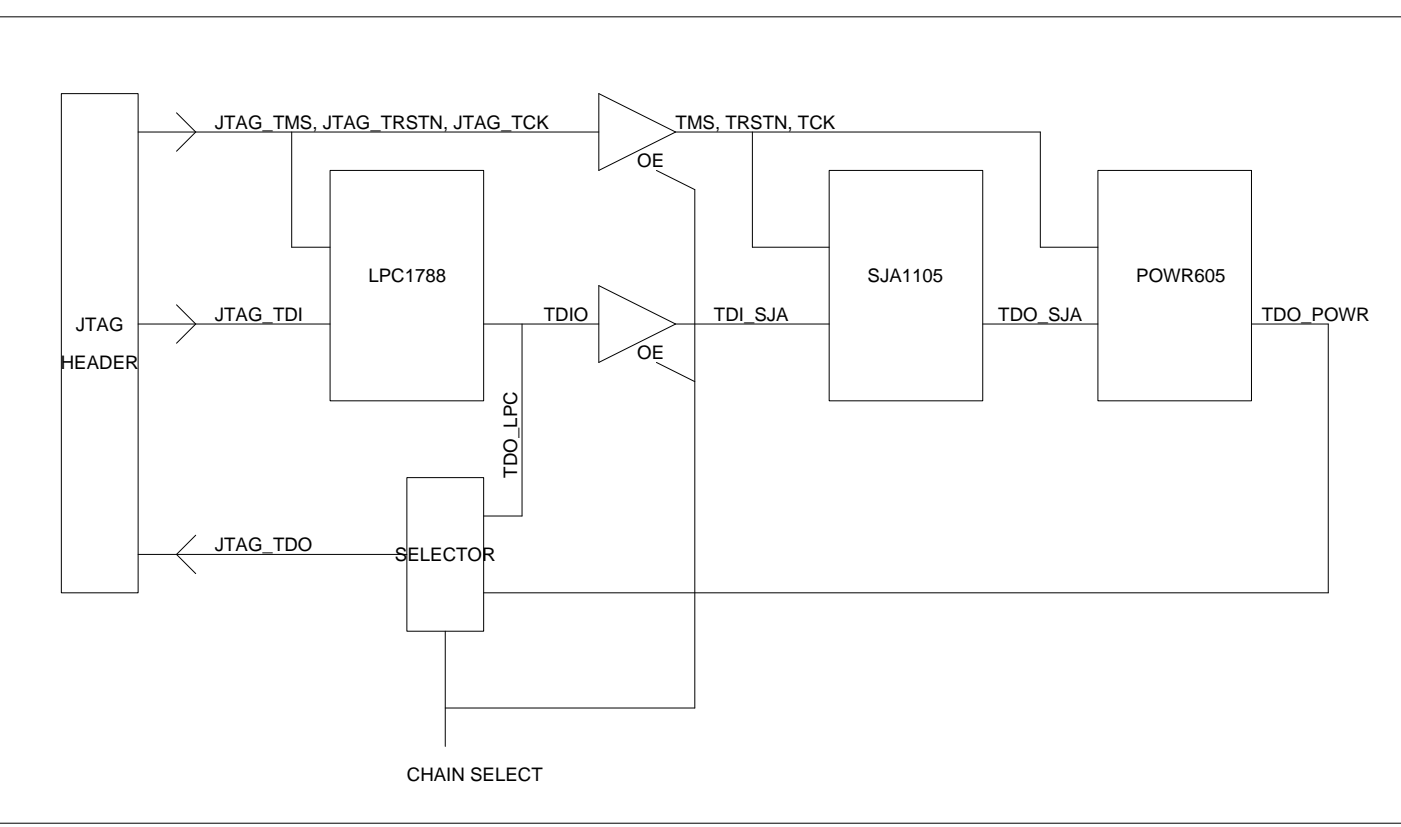
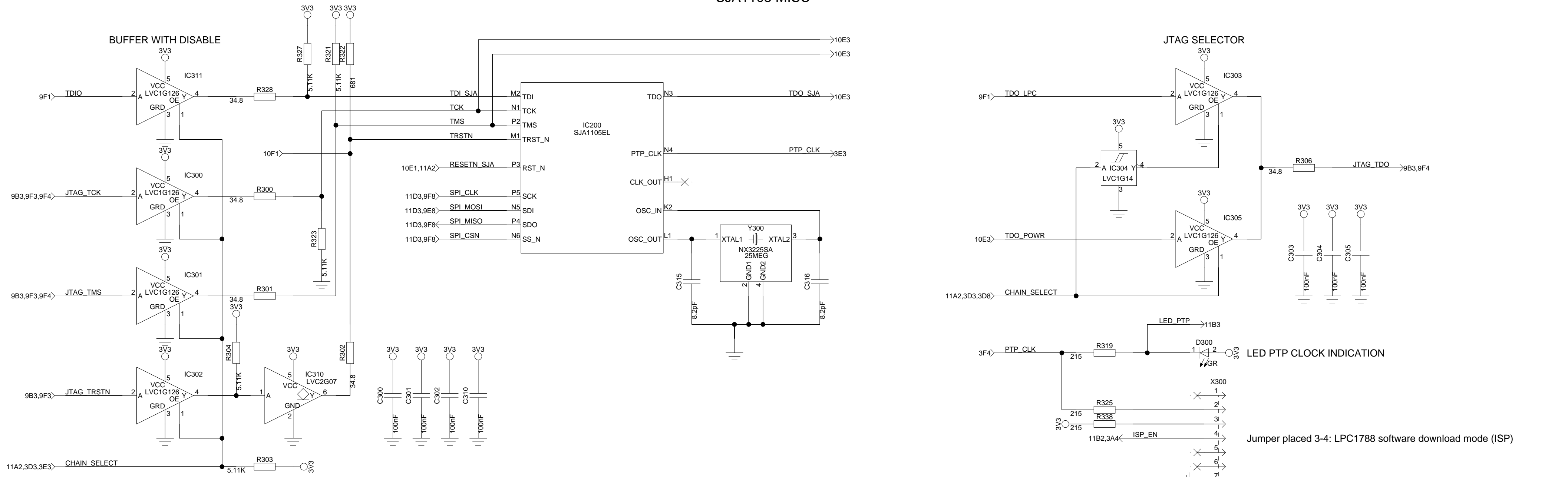
| SCHEMATIC NR# AND FUNCTION | |
|----------------------------|-------------------------|
| 1 | OVERVIEW, THIS SHEET |
| 2 | SJA1105 XMII |
| 3 | SJA1105 MISC, UART, I2C |
| 4 | SJA1105 POWER |
| 5 | TJA1100 PHY 0 |
| 6 | TJA1100 PHY 1 |
| 7 | TJA1100 PHY 2 |
| 8 | TJA1100 PHY 3 |
| 9 | LPC1788 |
| 10 | POWER, RESET |
| 11 | CLOCK DISTRIBUTION |



SJA1105 xMII

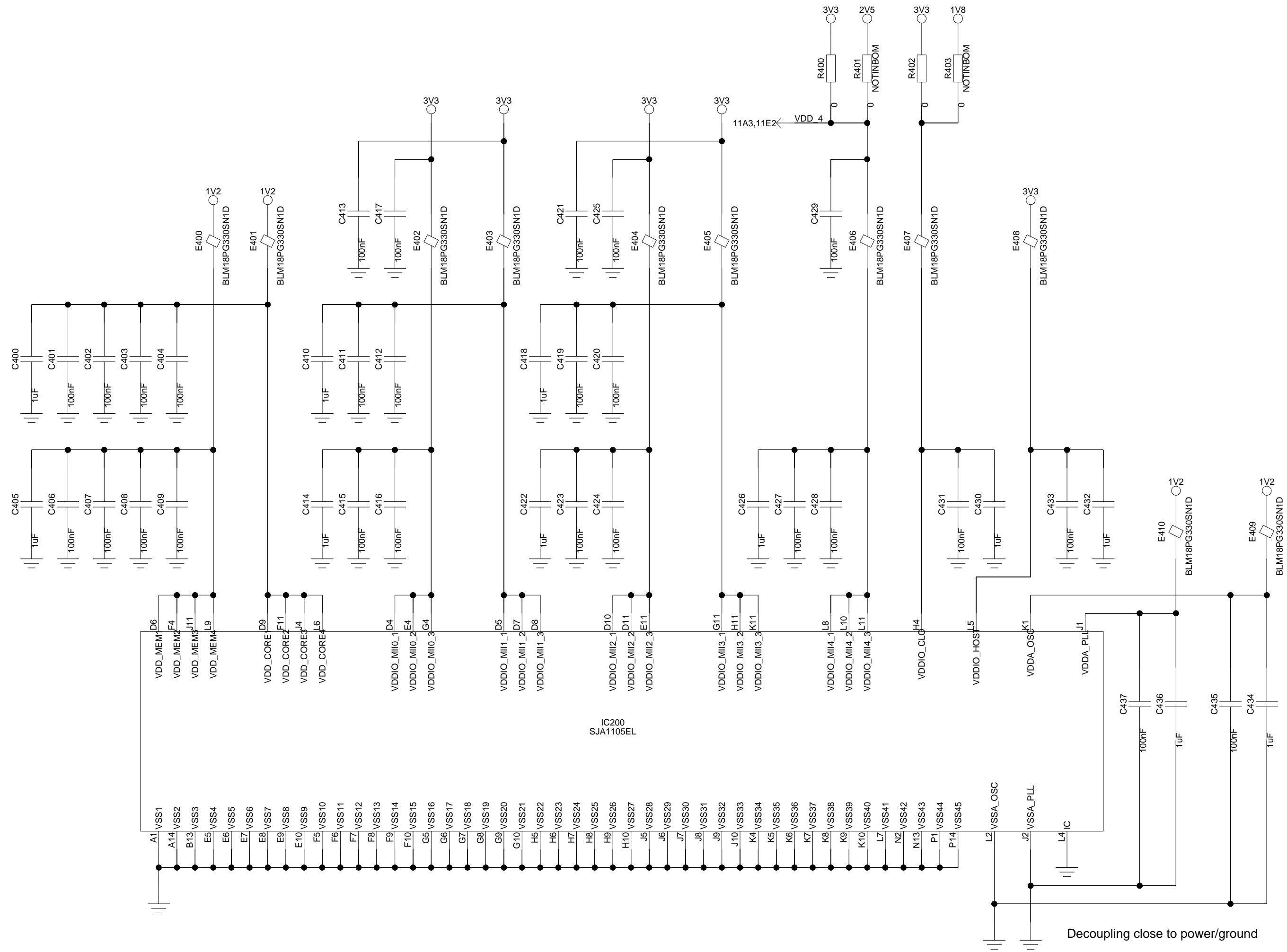


SJA1105 MISC



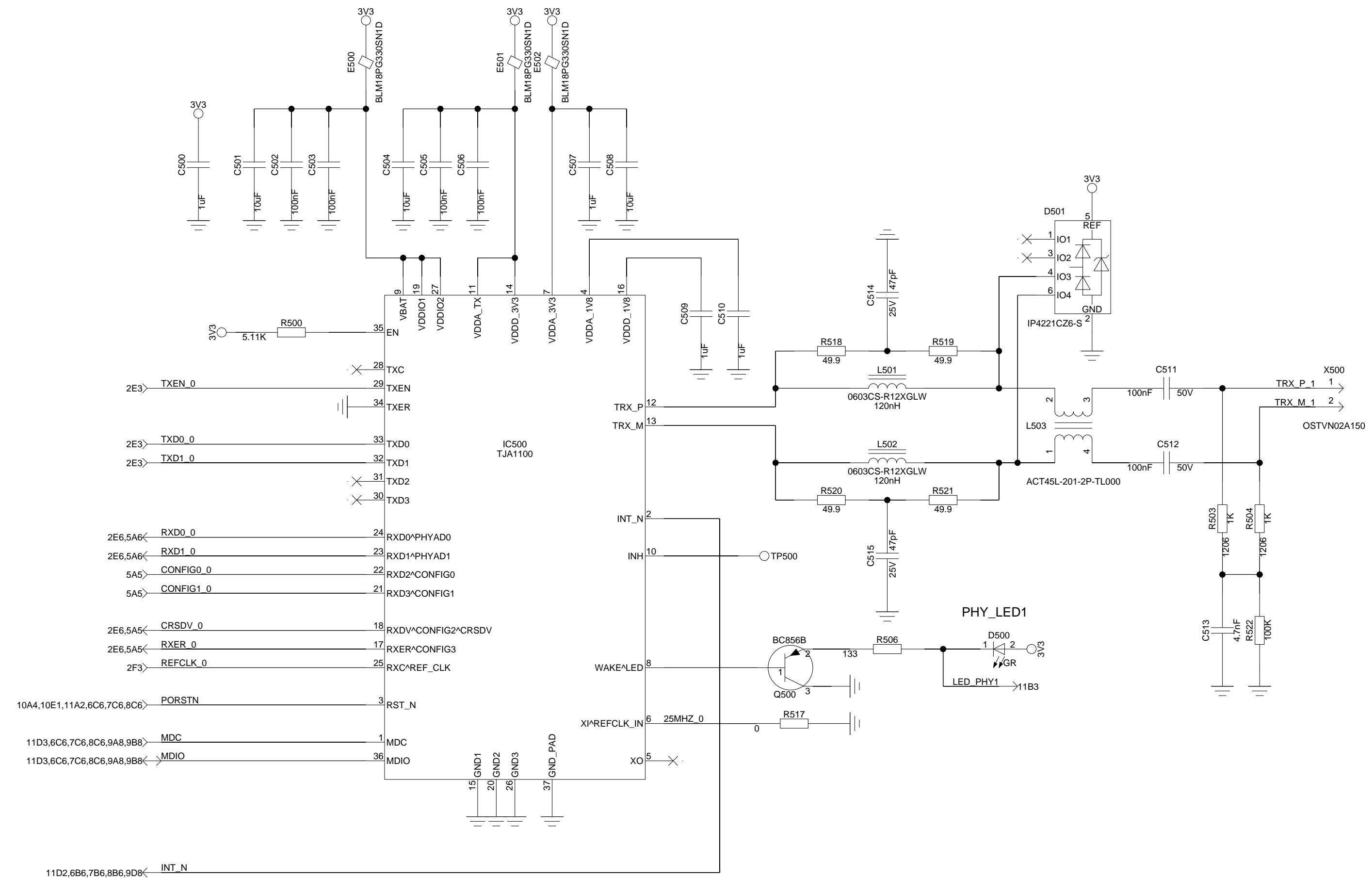
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|--|-------|------------------|--|
| NXP Semiconductors | | | |
| USE PURSUANT TO COMPANY INSTRUCTIONS | | | |
| Application Board SJA1105_TJA1100 | | | |
| SIZE | Sheet | REV | |
| A2 | | 0 | |
| DRAWN BY | | SHEET 3 of 11 | |
| AimValley B.V. | | 01/04/2016:10:56 | |

SJA1105 POWER

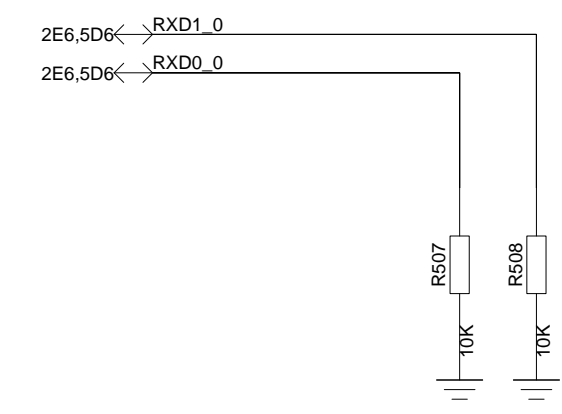


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| NXP Semiconductors | | | |
| USE PURSUANT TO COMPANY INSTRUCTIONS | | | |
| TITLE Application Board SJA1105_TJA1100 | | | |
| SIZE A2 | Sheet | SJA1105_POWER | REV 0 |
| DRAWN BY AimValley B.V. | | SHEET 4 of 11 | 01/04/2016:10:56 |

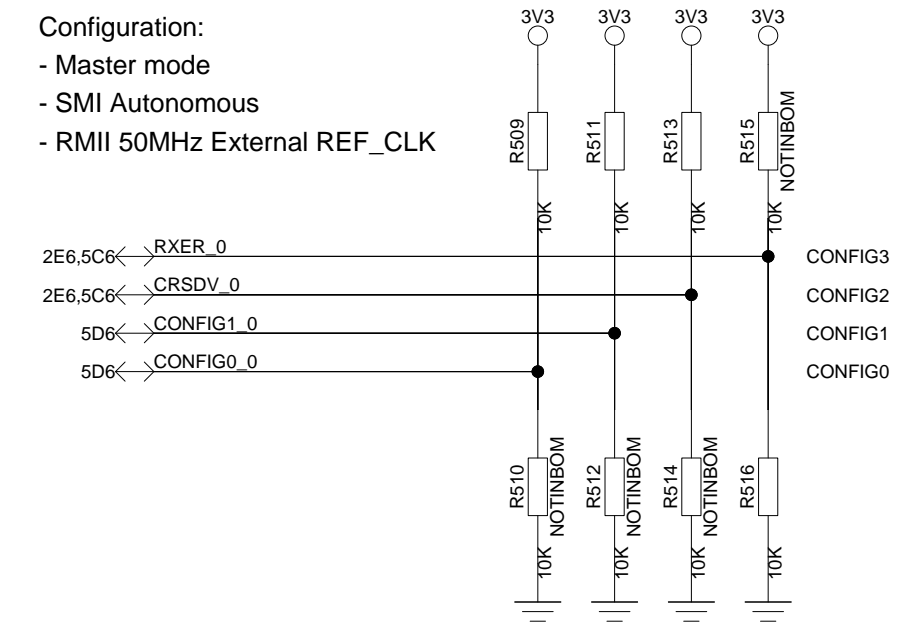
TJA1100 PHY 0



PHY Address [1:0] = 4 (offset is 4)



Configuration:
- Master mode
- SMI Autonomous
- RMII 50MHz External REF_CLK



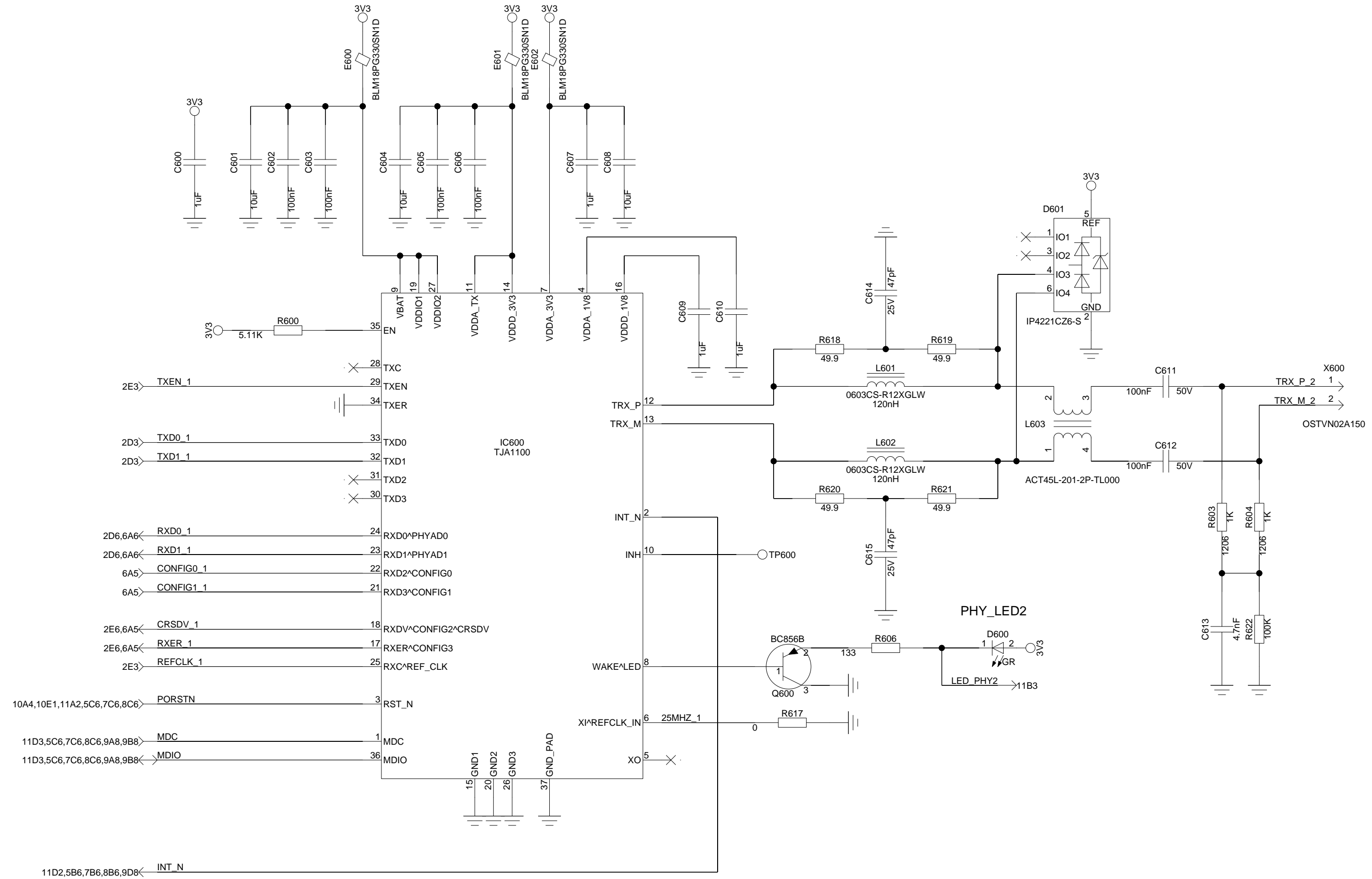
| | |
|-----------|--|
| CONFIG3/2 | L/H=RMII mode, external 50MHz oscillator |
| CONFIG1 | H/L=RMII mode, 25MHz from REFCLK_IN |
| CONFIG0 | H=Autonomous operation L=Managed operation |
| | H=PHY is master L=PHY is Slave |

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USE PURSUANT TO COMPANY INSTRUCTIONS

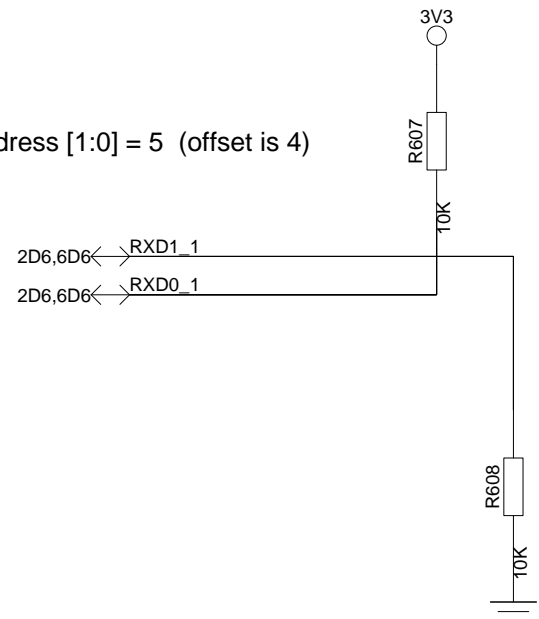
Application Board SJA1105_TJA1100

| | | | | |
|----------------|-------|---------------|-----|------------------|
| SIZE | Sheet | TJA1100_PHY_0 | REV | 0 |
| DRAWN BY | | SHEET 5 of 11 | | 08/04/2016:11:18 |
| AimValley B.V. | | | | |

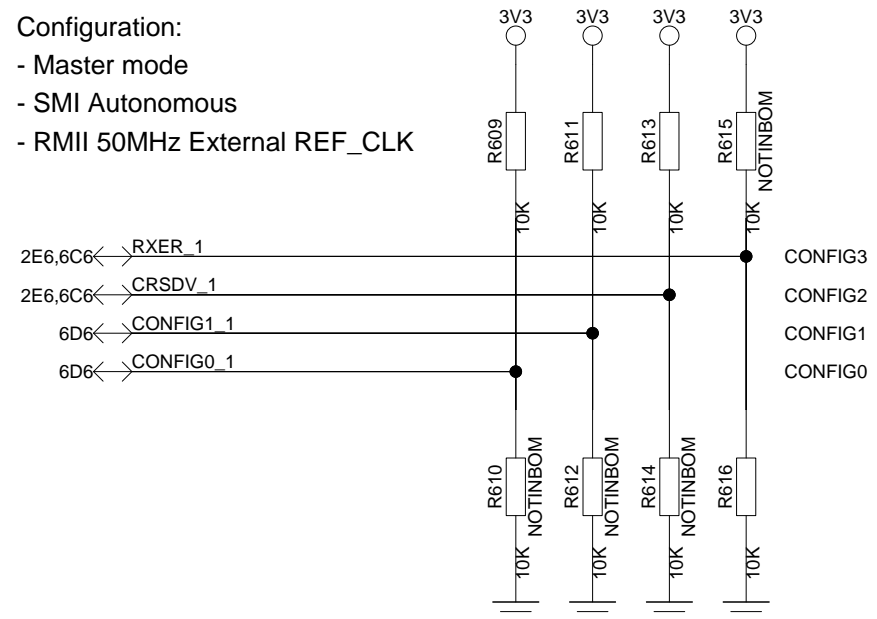
TJA1100 PHY 1



PHY Address [1:0] = 5 (offset is 4)



Configuration:
 - Master mode
 - SMI Autonomous
 - RMII 50MHz External REF_CLK



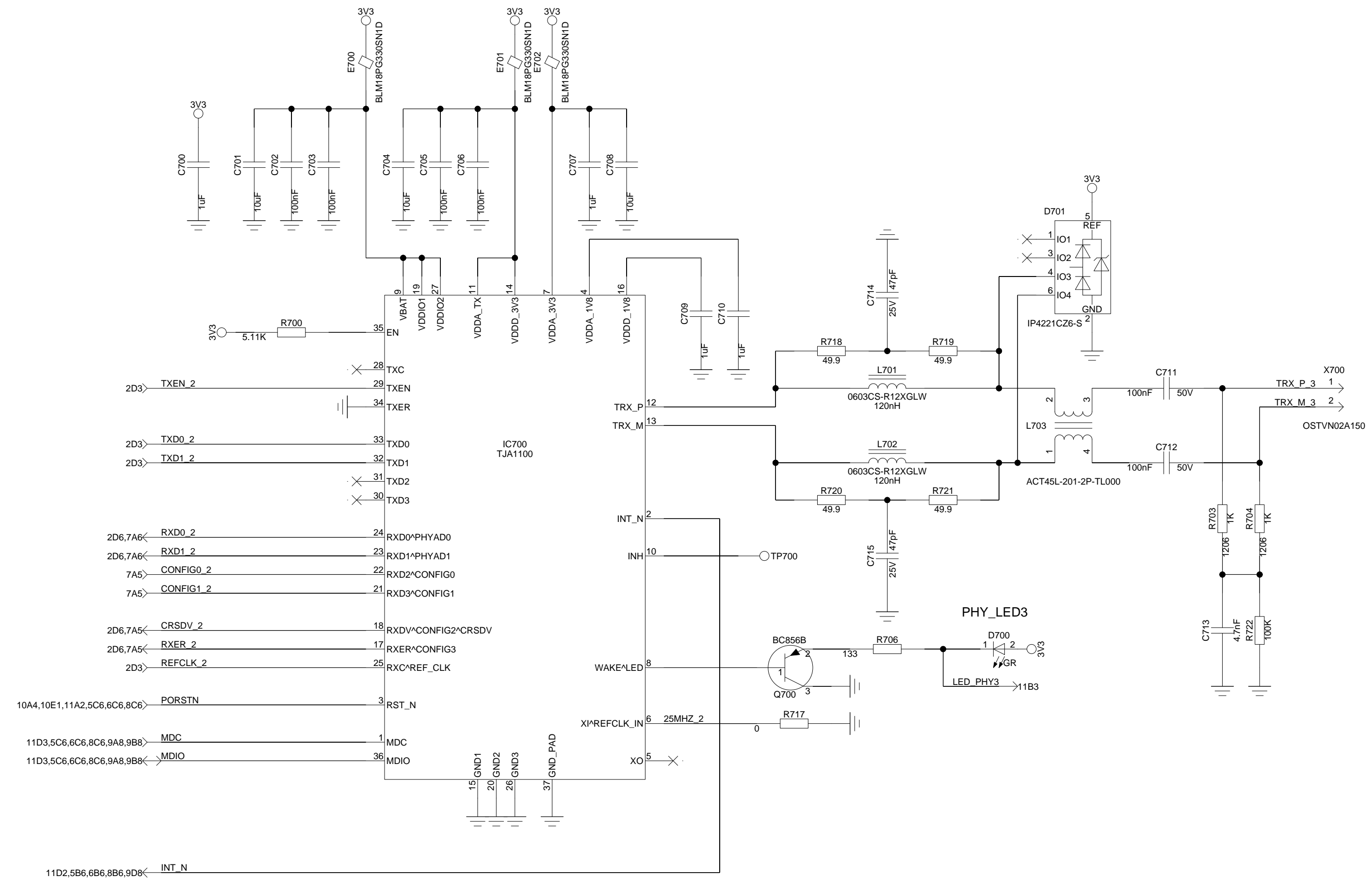
| | |
|-----------|---|
| CONFIG3/2 | L/H=RMII mode, external 50MHz oscillator |
| | H/L=RMII mode, 25MHz from REFCLK_IN |
| CONFIG1 | H=Autonomous operation L=Managed operation |
| CONFIG0 | H=PHY is master L=PHY is Slave |

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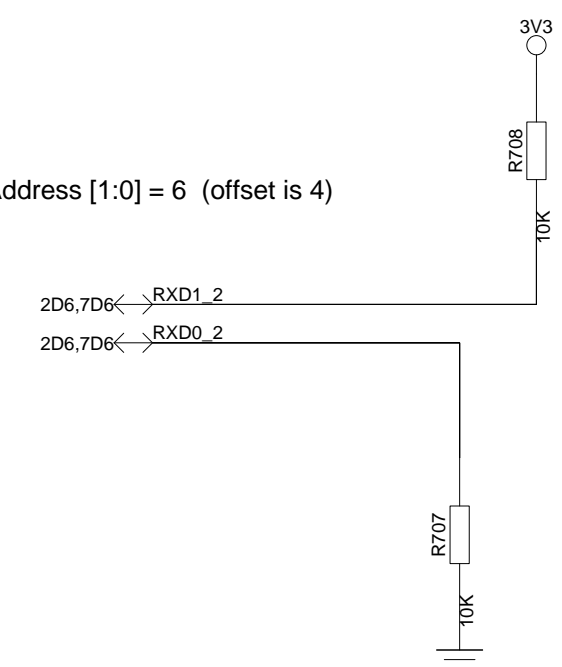
Application Board SJA1105_TJA1100

| | | | | |
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| SIZE | Sheet | TJA1100_PHY_1 | REV | 0 |
| DRAWN BY | | SHEET 6 of 11 | | 08/04/2016:11:19 |
| AimValley B.V. | | | | |

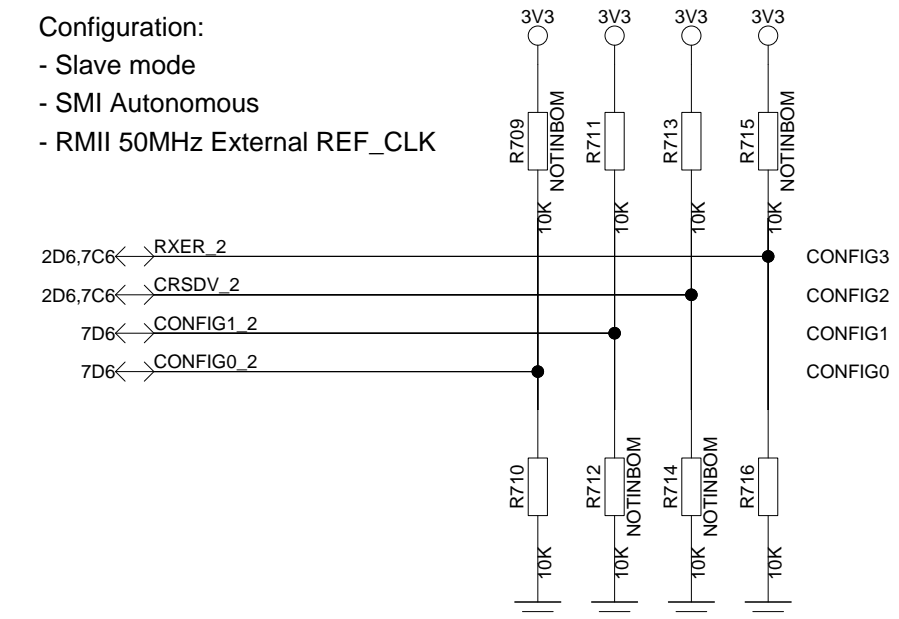
TJA1100 PHY 2



PHY Address [1:0] = 6 (offset is 4)



Configuration:
 - Slave mode
 - SMI Autonomous
 - RMII 50MHz External REF_CLK



| | |
|-----------|---|
| CONFIG3/2 | L/H=RMII mode, external 50MHz oscillator |
| | H/L=RMII mode, 25MHz from REFCLK_IN |
| CONFIG1 | H=Autonomous operation L=Managed operation |
| CONFIG0 | H=PHY is master L=PHY is Slave |

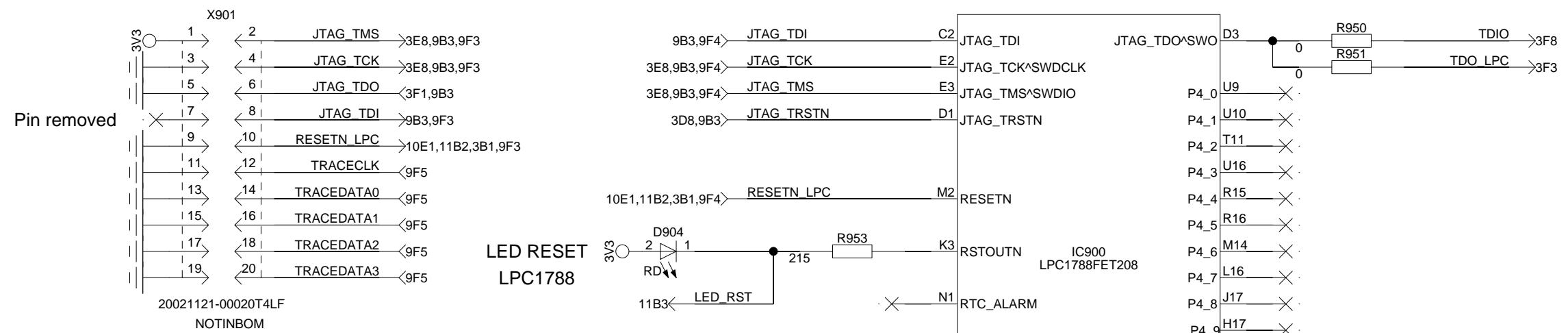
NXP Semiconductors
 USE PURSUANT TO COMPANY INSTRUCTIONS

Application Board SJA1105_TJA1100

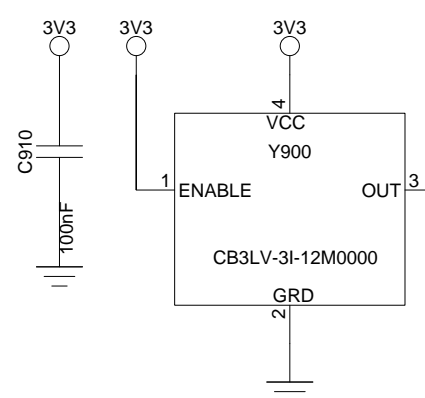
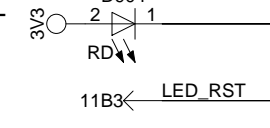
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|----------------|-------|---------------|-----|------------------|
| SIZE | Sheet | TJA1100_PHY_2 | REV | 0 |
| DRAWN BY | | SHEET 7 of 11 | | 08/04/2016:11:19 |
| AimValley B.V. | | | | |

LPC1788

Cortex Debug & ETM Connector



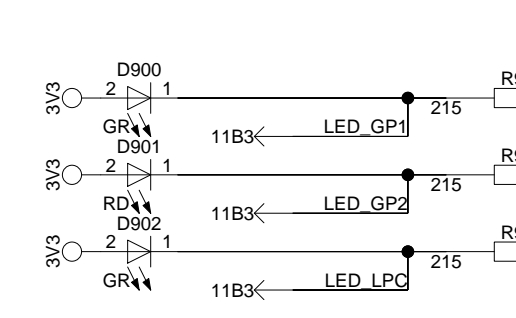
LED RESET LPC1788



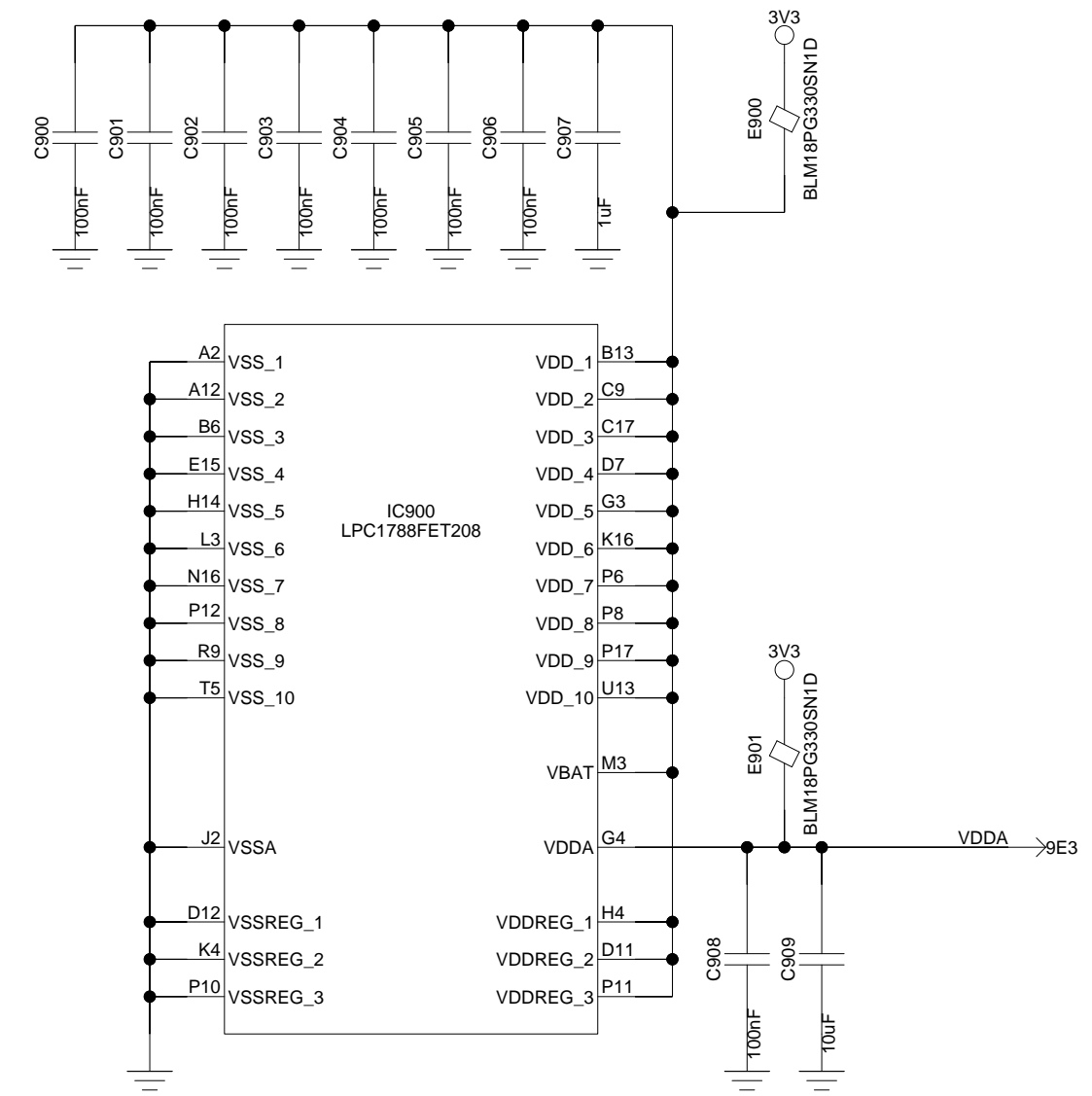
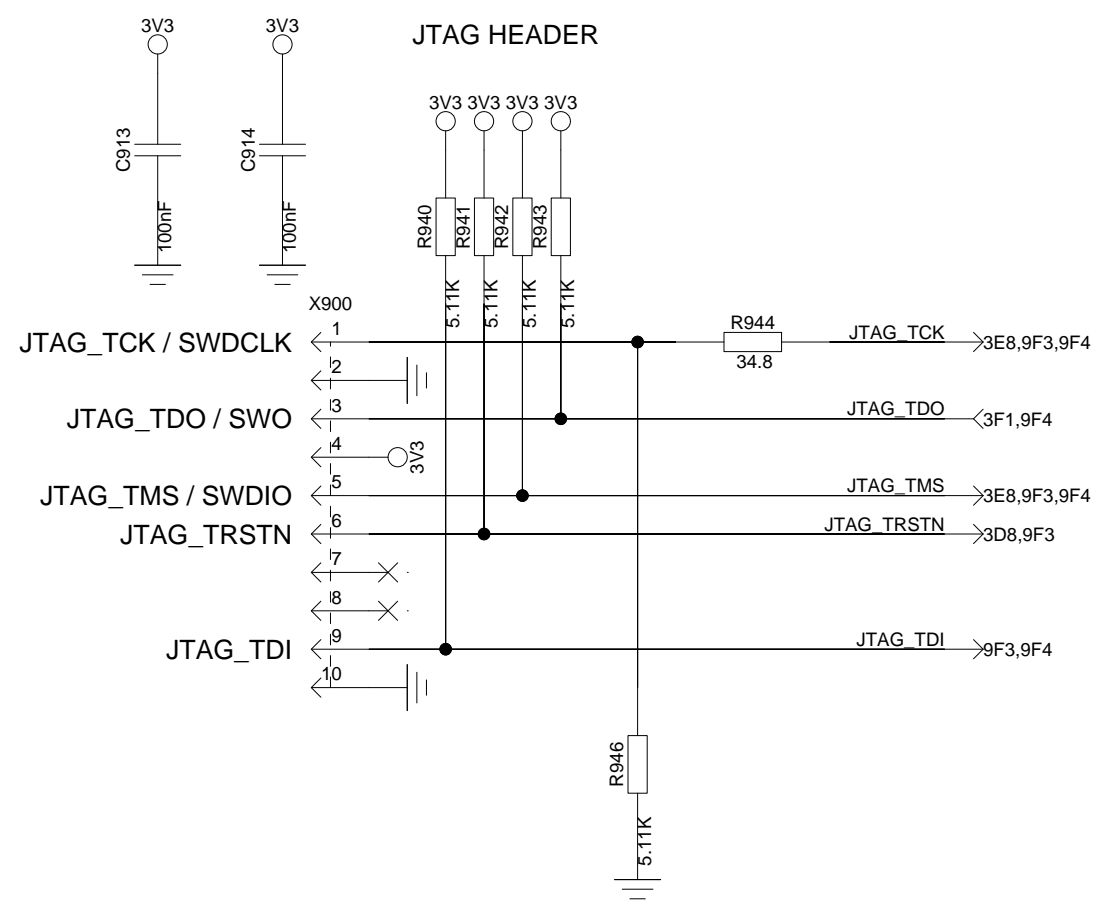
LED GENERAL PURPOSE 1

LED GENERAL PURPOSE 2

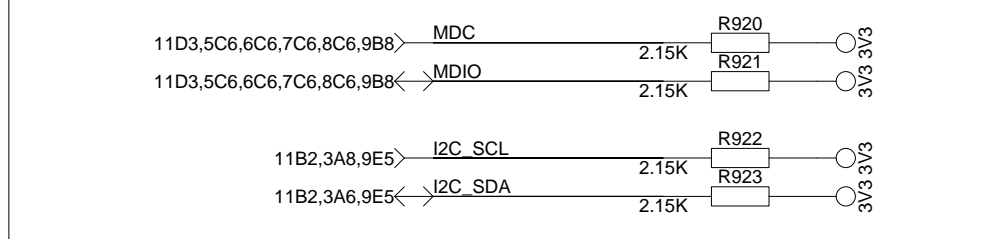
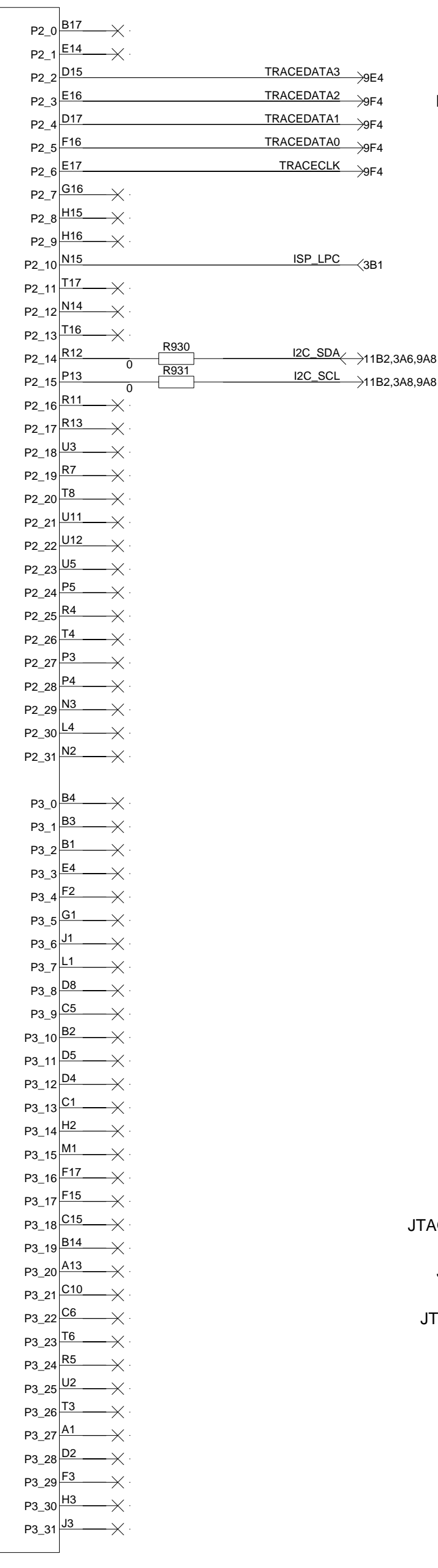
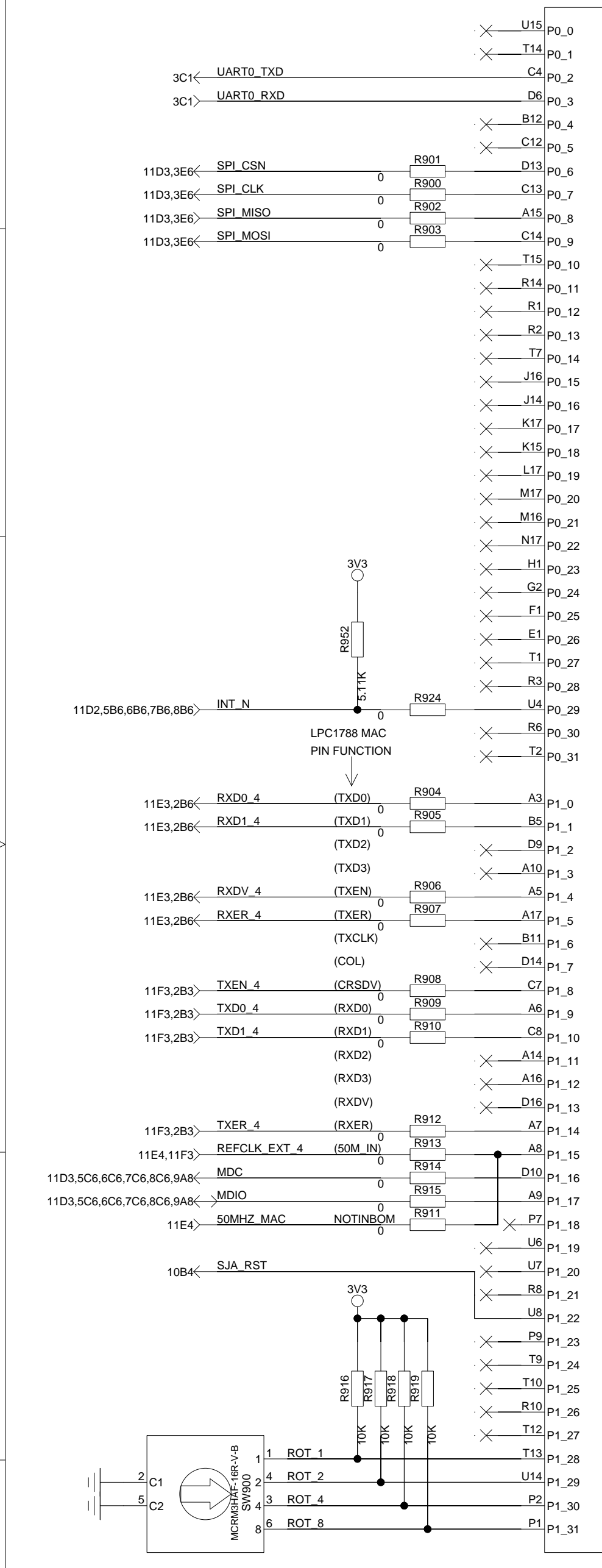
LED LPC1788 ALIVE



JTAG HEADER



IC900 LPC1788FET208

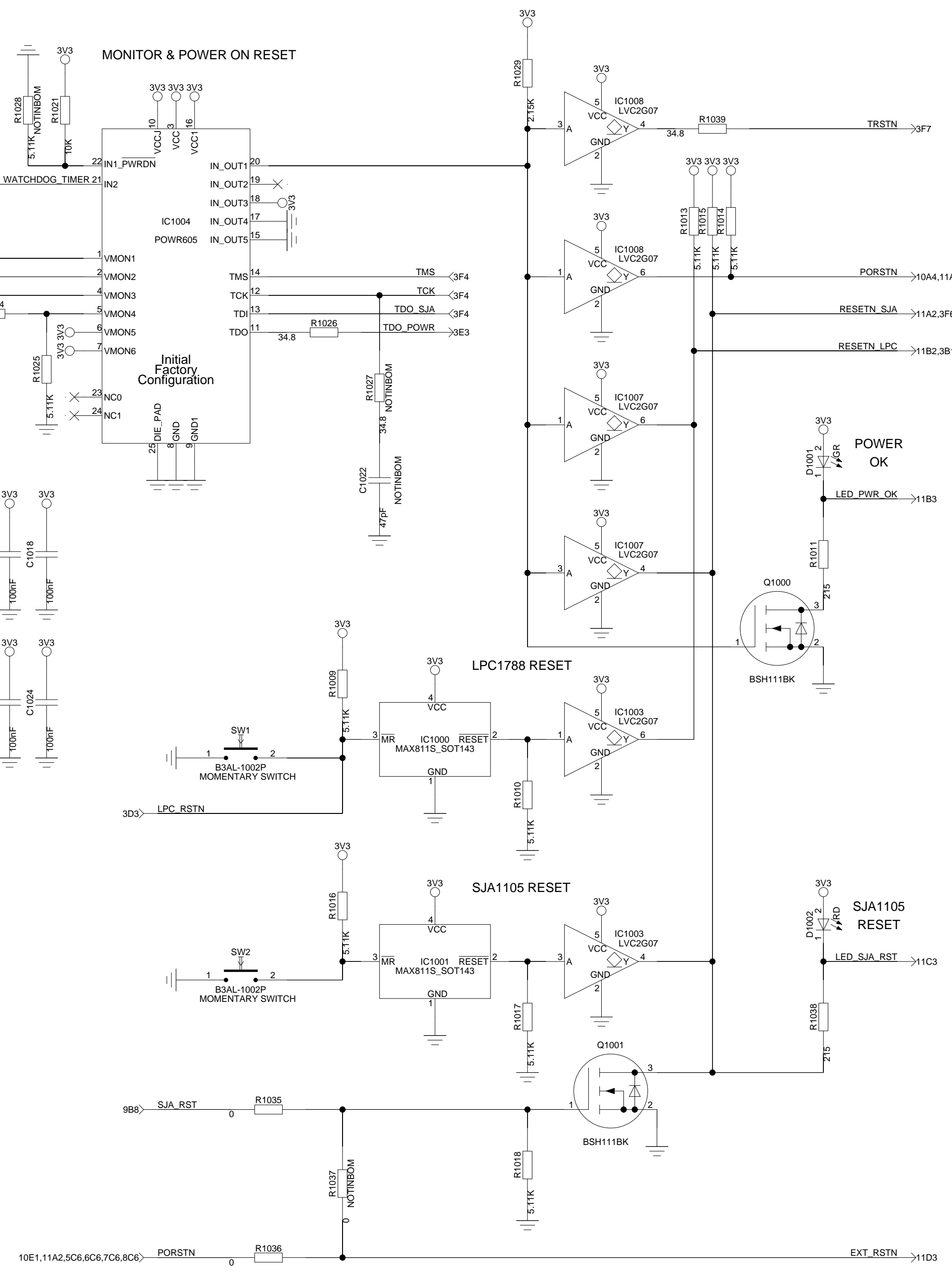
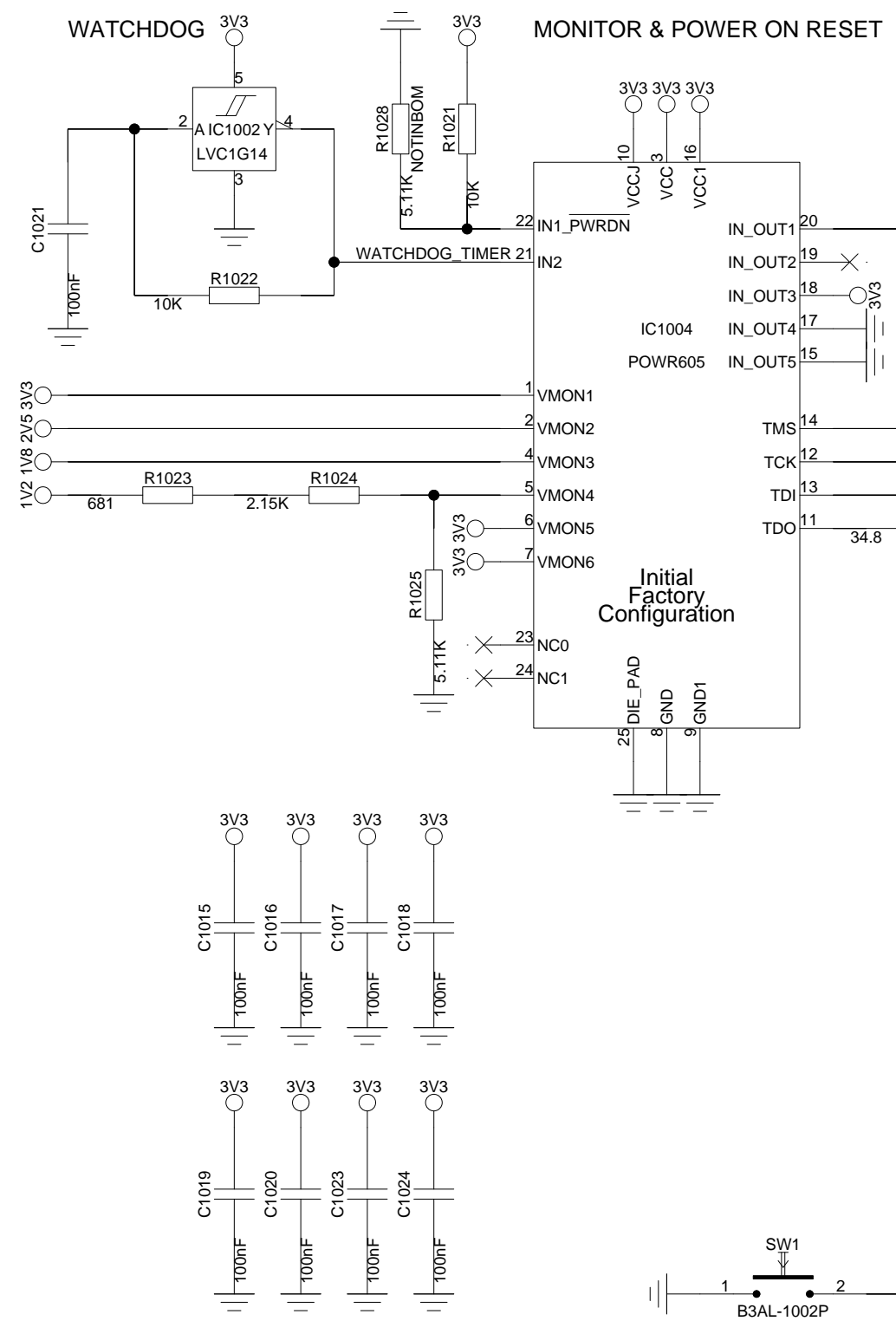
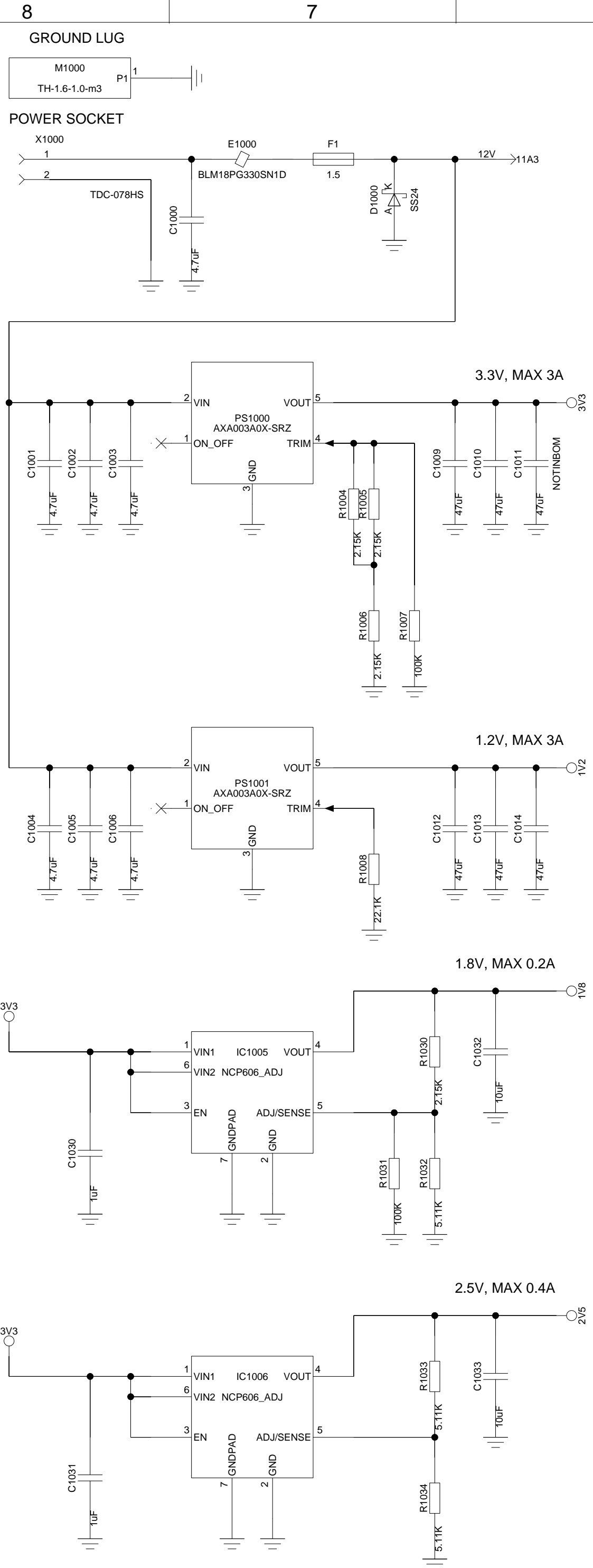


NXP Semiconductors
USE PURSUANT TO COMPANY INSTRUCTIONS

Application Board SJA1105_TJA1100

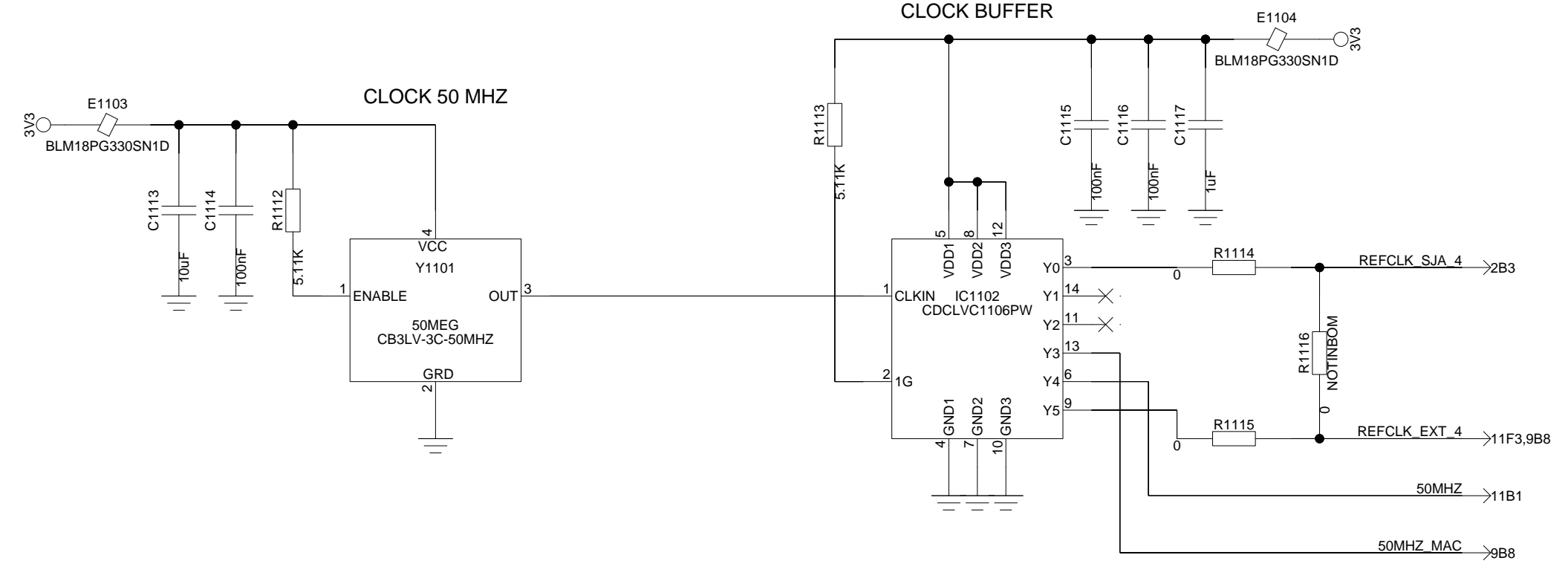
SIZE A2 Sheet REV 0

DRAWN BY AimValley B.V. SHEET 9 of 11 01/04/2016:10:56

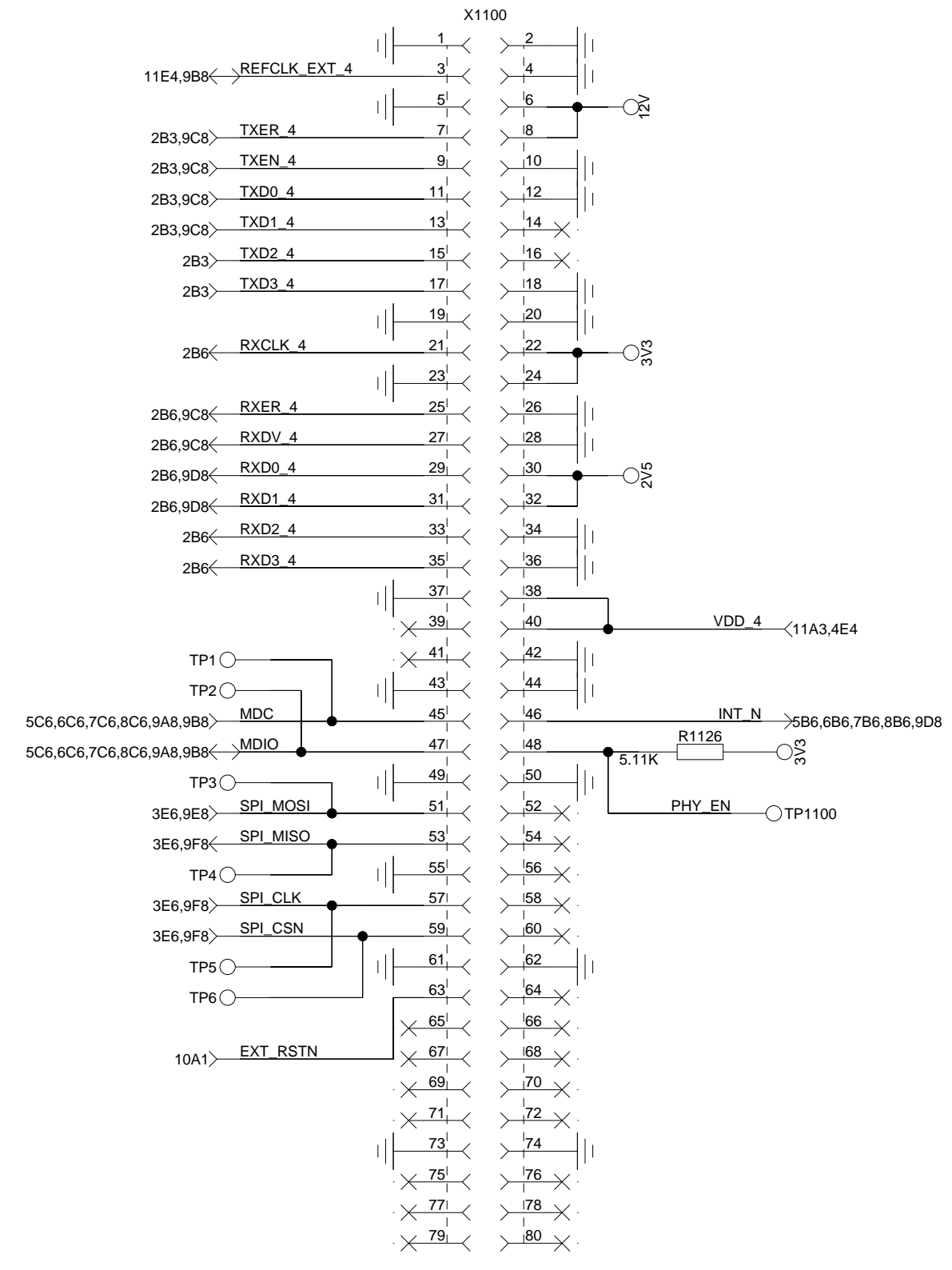


| | | | |
|--------------------------------------|-------|----------------|------------------|
| NXP Semiconductors | | | |
| USE PURSUANT TO COMPANY INSTRUCTIONS | | | |
| Application Board SJA1105_TJA1100 | | | |
| SIZE | Sheet | REV | |
| A2 | | 0 | |
| DRAWN BY AimValley B.V. | | SHEET 10 of 11 | 05/04/2016:11:33 |

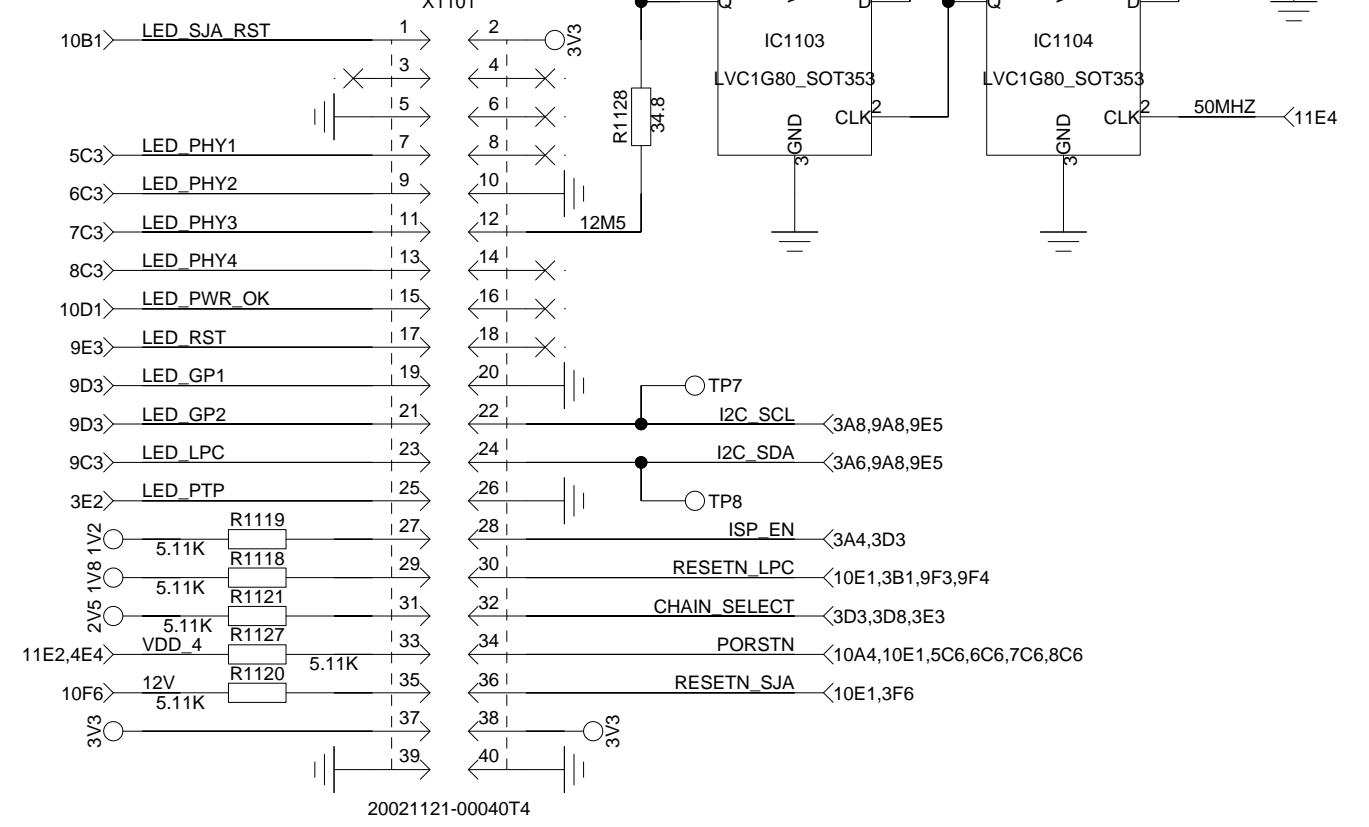
CLOCK DISTRIBUTION



EXTENSION CONNECTOR



Factory Test Connector



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TITLE
Application Board SJA1105_TJA1100

| | | | |
|-----------|---------------------------|----------|--|
| SIZE | Sheet | REV | |
| A2 | CLOCK DISTRIBUTION | 0 | |

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