



MicroPak Logic portfolio

Single, dual, or triple gate functions in small footprint leadless packages

NXP advances state-of-the-art packaging with MicroPak (XSON). Designed for use in portable applications, where board space is always an issue, XSON packages are dramatically smaller, allowing compact and slimmer overall designs. These leadless Mini Logic packages provide up to 65% space saving over traditional leaded Mini Logic (PicoGate) packages.

KEY FEATURES

- Very small footprint
- 0.5 mm, 0.35 mm, and 0.3 mm pitch options
- Low profile height (0.5 mm or 0.35 mm)
- Pb-free, RoHS, and dark green compliant
- Fully specified (-40 to +125 °C)
- Automotive options

BENEFITS

- Simplified board layout
- No bent leads
- No co-planarity issues
- Low power consumption

APPLICATIONS

- Space constrained applications
- Consumer
- Portable
- Automotive

The MicroPak packages are leadless Mini Logic packages. They house the same silicon die as the larger leaded Mini Logic (PicoGate) packages. This ensures that along with the smaller footprint identical electrical performance is assured. Signal integrity may be improved due to lower package parasitic inductance. Their tiny size saves valuable board real estate. Packages are available with 0.5 mm as well as state of the art 0.35 mm and 0.30 mm pad pitch.

The package is an ideal choice for space constrained applications where PCB space and low cost assembly is critical. With their larger pads the MicroPak packages offer easier component placement as well as improved strength, reliability, and thermal characteristics over similar sized BGA solutions.



MICROPAK PORTFOLIO

The MicroPak portfolio is very broad and includes analog switches, buffers/inverters/drivers, bus switches, counters, decoders, flip-flops, gates, multiplexers/demultiplexers, latches, level shifters, and Schmitt-trigger devices.

[illegible][illegible]

Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74AHC1G126	single buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125		•	•												
74AHCT1G126	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 8	3.4	-40~125		•	•												
74AHC2G125	dual buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125					•										
74AHCT2G125	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 8	3.4	-40~125					•										
74AHC2G126	dual buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125					•										
74AHCT2G126	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 8	3.4	-40~125					•										
74AHC2G241	dual buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125					•										
74AHCT2G241	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 8	3.4	-40~125					•										
74AHC3G04	triple inverter	2.0 - 5.5	± 8	3.1	-40~125					•										
74AHCT3G04	triple inverter; TTL enabled	4.5 - 5.5	± 8	3	-40~125					•										
74AUP1G04	single inverter	1.1 - 3.6	± 1.9	4	-40~125		•	•						•			•		•	
74AUP1G06	single inverter; open-drain	1.1 - 3.6	1.9	4.5	-40~125		•	•						•			•		•	
74AUP1G07	single buffer; open-drain	1.1 - 3.6	1.9	4.4	-40~125		•	•						•			•		•	
74AUP1G14	single inverter; Schmitt-trigger	1.1 - 3.6	± 1.9	4.7	-40~125		•	•						•			•		•	
74AUP1G34	single buffer	1.1 - 3.6	± 1.9	3.9	-40~125		•	•						•			•		•	
74AUP1G125	single buffer/line driver (3-state)	1.1 - 3.6	± 1.9	4.3	-40~125		•	•						•			•		•	
74AUP1G126	single buffer/line driver (3-state)	1.1 - 3.6	± 1.9	4.3	-40~125		•	•						•			•		•	
74AUP1G240	single inverter/line driver (3-state)	1.1 - 3.6	± 1.9	4.2	-40~125		•	•						•			•		•	
74AUP1GU04	single inverter; unbuffered	1.1 - 3.6	± 1.9	2.3	-40~125		•	•						•			•		•	
74AUP2G04	dual inverter	1.1 - 3.6	± 1.9	4	-40~125		•	•						•			•			•
74AUP2G06	dual inverter; open-drain	1.1 - 3.6	1.9	4.5	-40~125		•	•						•			•			
74AUP2G07	dual buffer; open-drain	1.1 - 3.6	1.9	4.4	-40~125		•	•						•			•			•
74AUP2G14	dual inverter; Schmitt-trigger	1.1 - 3.6	± 1.9	4.7	-40~125		•	•						•			•			•
74AUP2G34	dual buffer	1.1 - 3.6	± 1.9	3.9	-40~125		•	•						•			•			•
74AUP2G125	dual buffer/line driver (3-state)	1.1 - 3.6	± 1.9	4.3	-40~125	•			•	•			•		•			•		
74AUP2G126	dual buffer/line driver (3-state)	1.1 - 3.6	± 1.9	4.3	-40~125	•			•	•			•		•			•		
74AUP2G240	dual inverter/line driver (3-state)	1.1 - 3.6	± 1.9	4.2	-40~125	•			•	•			•		•			•		

Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74AUP2G241	dual buffer/line driver (3-state)	1.1 - 3.6	± 1.9	4.3	-40~125	•			•	•			•		•			•		
74AUP2GU04	dual inverter; unbuffered	1.1 - 3.6	± 1.9	2.3	-40~125		•	•						•			•			
74AUP3G04	triple inverter	1.1 - 3.6	± 1.9	4	-40~125	•			•	•			•		•			•		
74AUP3G07	triple buffer; open-drain	1.1 - 3.6	1.9	3.9	-40~125	•			•	•			•		•			•		
74AUP3G34	triple buffer	1.1 - 3.6	± 1.9	3.9	-40~125	•			•	•			•		•			•		
74AXP1G04	single inverter	0.7 - 2.75	± 4.5	2.6	-40~85		•							•			•		•	
74AXP1G06	single inverter; open-drain	0.7 - 2.75	4.5	3.5	-40~85		•							•			•		•	
74AXP1G125	single buffer/line driver (3-state)	0.7 - 2.75	± 4.5	2.7	-40~85		•							•			•		•	
74AXP2G07	dual buffer; open-drain	0.7 - 2.75	4.5	3.4	-40~85		•							•			•			•
74HC3GU04	triple inverter; unbuffered	2.0 - 6.0	± 5.2	6	-40~125					•										
74HC2G125	dual buffer/line driver (3-state)	2.0 - 6.0	± 5.2	10	-40~125					•										
74HCT2G125	dual buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 4.0	12	-40~125					•										
74HC3G04	triple inverter	2.0 - 6.0	± 5.2	8	-40~125					•										
74HCT3G04	triple inverter; TTL enabled	4.5 - 5.5	± 4.0	10	-40~125					•										
74HC3G06	triple inverter; open-drain	2.0 - 6.0	5.2	9	-40~125					•										
74HCT3G06	triple inverter; open-drain; TTL enabled	4.5 - 5.5	4	9	-40~125					•										
74HC3G07	triple buffer; open-drain	2.0 - 6.0	5.2	9	-40~125					•										
74HCT3G07	triple buffer; open-drain; TTL enabled	4.5 - 5.5	4	9	-40~125					•										
74HC3G34	triple buffer	2.0 - 6.0	± 5.2	9	-40~125					•										
74HCT3G34	triple buffer; TTL enabled	4.5 - 5.5	± 4.0	10	-40~125					•										
74LVC1G04	single inverter	1.65 - 5.5	± 32	2	-40~125		•	•						•			•		•	
74LVC1G06	single inverter; open-drain	1.65 - 5.5	32	2.3	-40~125		•	•						•			•		•	
74LVC1G07	single buffer; open-drain	1.65 - 5.5	32	2.2	-40~125		•	•						•			•		•	
74LVC1G34	single buffer	1.65 - 5.5	± 32	2	-40~125		•	•						•			•		•	
74LVC1G125	single buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	± 32	2.1	-40~125		•	•						•			•			
74LVC1G126	single buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	± 32	2	-40~125		•	•						•			•			
74LVC1GU04	single inverter; unbuffered	1.65 - 5.5	± 32	1.6	-40~125		•	•						•			•		•	
74LVC2G04	dual inverter	1.65 - 5.5	± 32	2.7	-40~125		•	•						•			•			•
74LVC2G06	dual inverter; open-drain	1.65 - 5.5	32	2.3	-40~125		•	•						•			•			•
74LVC2G07	dual buffer; open-drain	1.65 - 5.5	32	2.6	-40~125		•	•						•			•			•
74LVC2G34	dual buffer	1.65 - 5.5	± 32	2.2	-40~125		•	•						•			•			•
74LVC2G125	dual buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	± 32	2.3	-40~125	•			•	•			•		•			•		

Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74LVC2G126	dual buffer/line driver; TTL enabled (3-state)	1.65 - 5.5	± 32	2.4	-40~125	•			•	•			•		•			•		
74LVC2G240	dual inverter/line driver (3-state)	1.65 - 5.5	± 32	2.5	-40~125	•			•				•							
74LVC2G241	dual buffer/line driver (3-state)	1.65 - 5.5	± 32	2.6	-40~125	•			•	•			•		•			•		
74LVC2GU04	dual inverter; unbuffered	1.65 - 5.5	± 32	2.3	-40~125		•	•						•			•			
74LVC3G04	triple inverter	1.65 - 5.5	± 32	2.7	-40~125	•			•	•			•		•			•		
74LVC3G06	triple inverter; open-drain	1.65 - 5.5	32	2	-40~125	•			•	•			•		•			•		
74LVC3G07	triple buffer; open-drain	1.65 - 5.5	32	2.1	-40~125	•			•	•			•		•			•		
74LVC3G34	triple buffer	1.65 - 5.5	± 32	2.2	-40~125	•			•	•			•		•			•		
74LVC3GU04	triple inverter; unbuffered	1.65 - 5.5	± 32	2.3	-40~125	•			•	•			•		•			•		
XC7SET125	single buffer/line driver; TTL enabled (3-state)	4.5 - 5.5	± 8	3.4	-40~125		•	•												
XC7SH125	single buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125		•	•												
XC7WH126	dual buffer/line driver (3-state)	2.0 - 5.5	± 8	3.4	-40~125				•											
Decoders/demultiplexers																				
74AUP1G18	1-to-2 demultiplexer (3-state)	1.1 - 3.6	± 1.9	3.2	-40~125		•	•						•			•			
74AUP1G19	1-to-2 decoder/demultiplexer	1.1 - 3.6	± 1.9	3	-40~125		•	•						•			•			
74LVC1G19	1-to-2 decoder/demultiplexer	1.65 - 5.5	± 32	1.8	-40~125		•	•						•			•			
Multiplexers																				
74AUP1G157	single 2-input multiplexer	1.1 - 3.6	± 1.9	3.2	-40~125		•	•						•			•			
74AUP1G158	single 2-input multiplexer; inverting	1.1 - 3.6	± 1.9	3.2	-40~125		•	•						•			•			
74AUP2G157	single 2-input multiplexer	1.1 - 3.6	± 1.9	3.4	-40~125	•			•	•			•		•			•		
74LVC1G157	single 2-input multiplexer	1.65 - 5.5	± 32	2.2	-40~125		•	•						•			•			
Flip-flops																				
74AUP1G74	single D-type flip-flop with set and reset; positive-edge trigger	1.1 - 3.6	± 1.9	9.2	-40~125	•			•	•			•		•			•		
74AUP1G79	single D-type flip-flop; positive-edge trigger	1.1 - 3.6	± 1.9	9.1	-40~125		•	•						•			•		•	
74AUP1G80	single D-type flip-flop; positive-edge trigger	1.1 - 3.6	± 1.9	9.1	-40~125		•	•						•			•		•	
74AUP1G175	single D flip-flop with reset; positive-edge trigger	1.1 - 3.6	± 1.9	7.4	-40~125		•	•						•			•			
74AUP1G374	single D-type flip-flop; positive-edge trigger (3-state)	1.1 - 3.6	± 1.9	7.9	-40~125		•	•						•			•			

Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74AUP2G79	dual D-type flip-flop; positive-edge trigger	1.1 - 3.6	± 1.9	8.5	-40~125	•			•	•			•		•			•		
74AUP2G80	dual D-type flip-flop; positive-edge trigger	1.1 - 3.6	± 1.9	9.1	-40~125	•			•	•			•		•			•		
74LVC1G74	single D-type flip-flop with set and reset; positive- edge trigger	1.65 - 5.5	± 32	3.5	-40~125	•			•	•			•		•			•		
74LVC1G79	single D-type flip-flop; positive-edge trigger	1.65 - 5.5	± 32	2.2	-40~125		•	•						•			•		•	
74LVC1G80	single D-type flip-flop; positive-edge trigger	1.65 - 5.5	± 32	2.4	-40~125		•	•						•			•		•	
74LVC1G175	single D flip-flop with reset; positive-edge trigger	1.65 - 5.5	± 32	3.1	-40~125		•	•						•			•			
74LVC2G74	single D-type flip-flop with set and reset; positive- edge trigger	1.65 - 5.5	± 32	3.5	-40~125	•			•	•			•		•			•		
Gates																				
74AHC2G00	dual 2-input NAND gate	2.0 - 5.5	± 8	3.5	-40~125					•										
74AHCT2G00	dual 2-input NAND gate; TTL enabled	4.5 - 5.5	± 8	3.6	-40~125					•										
74AHC2G08	dual 2-input AND gate	2.0 - 5.5	± 8	3.2	-40~125					•										
74AHCT2G08	dual 2-Input AND gate; TTL enabled	4.5 - 5.5	± 8	3.6	-40~125					•										
74AHC2G32	dual 2-input OR gate	2.0 - 5.5	± 8	3.2	-40~125					•										
74AHCT2G32	dual 2-input OR gate; TTL enabled	4.5 - 5.5	± 8	3.3	-40~125					•										
74AUP1G00	single 2-input NAND gate	1.1 - 3.6	± 1.9	8.3	-40~125		•	•						•			•		•	
74AUP1G02	single 2-input NOR gate	1.1 - 3.6	± 1.9	8.3	-40~125		•	•						•			•		•	
74AUP1G08	single 2-input AND gate	1.1 - 3.6	± 1.9	8.2	-40~125		•	•						•			•		•	
74AUP1G09	single 2-input AND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125		•	•						•			•		•	
74AUP1G11	single 3-input AND gate	1.1 - 3.6	± 1.9	6.9	-40~125		•	•						•			•			
74AUP1G32	single 2-input OR gate	1.1 - 3.6	± 1.9	7.9	-40~125		•	•						•			•		•	
74AUP1G38	single 2-input NAND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125		•	•						•			•		•	
74AUP1G57	configurable gate; Schmitt trigger	1.1 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			•
74AUP1G58	configurable gate; Schmitt trigger	1.1 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			•
74AUP1G86	single 2-input EXCLUSIVE- OR gate	1.1 - 3.6	± 1.9	9	-40~125		•	•						•			•		•	
74AUP1G97	configurable gate; Schmitt trigger	1.1 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			•
74AUP1G98	configurable gate; Schmitt trigger	1.1 - 3.6	± 1.9	8.9	-40~125		•	•						•			•			•

Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74AUP1G332	single 3-input OR gate	1.1 - 3.6	± 1.9	6.8	-40~125		•	•						•			•			
74AUP1G386	single 3-input EXCLUSIVE-OR gate	1.1 - 3.6	± 1.9	8.6	-40~125		•	•						•			•			
74AUP1G885	dual function gate	1.1 - 3.6	± 1.9	7.6	-40~125	•			•	•			•		•			•		
74AUP1G0832	single 3-input AND-OR gate	1.1 - 3.6	± 1.9	6.7	-40~125		•	•						•			•			
74AUP1G3208	single 3-input OR-AND gate	1.1 - 3.6	± 1.9	7.4	-40~125		•	•						•			•			
74AUP1T57	configurable gate with voltage level translation	2.3 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			
74AUP1T58	configurable gate with voltage level translation	2.3 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			
74AUP1T97	configurable gate with voltage level translation	2.3 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			•
74AUP1T98	configurable gate with voltage level translation	2.3 - 3.6	± 1.9	8.7	-40~125		•	•						•			•			
74AUP1Z04	crystal driver with enable and internal resistor	1.1 - 3.6	± 1.9	5.6	-40~125		•	•						•			•			
74AUP1Z125	crystal driver with enable and internal resistor (3-state)	1.1 - 3.6	± 1.9	4.7	-40~125		•	•						•			•			
74AUP2G00	dual 2-input NAND gate	1.1 - 3.6	± 1.9	8.3	-40~125	•			•	•			•		•			•		
74AUP2G02	dual 2-input NOR gate	1.1 - 3.6	± 1.9	8.3	-40~125	•			•	•			•		•			•		
74AUP2G08	dual 2-input AND gate	1.1 - 3.6	± 1.9	8.2	-40~125	•			•	•			•		•			•		
74AUP2G32	dual 2-input OR gate	1.1 - 3.6	± 1.9	7.9	-40~125	•			•	•			•		•			•		
74AUP2G38	dual 2-input NAND gate; open-drain	1.1 - 3.6	1.9	8.5	-40~125	•			•	•			•		•			•		
74AUP2G57	dual PCB configurable multiple function gate	1.1 - 3.6	± 1.9	8.7	-40~125						•	•				•				
74AUP2G86	dual 2-input EXCLUSIVE-OR gate	1.1 - 3.6	± 1.9	9	-40~125	•			•	•			•		•			•		
74AUP2G0604	inverter with open-drain and inverter	1.1 - 3.6	± 1.9	4	-40~125		•	•						•			•			
74AUP2G3404	buffer and inverter	1.1 - 3.6	± 1.9	4	-40~125		•	•						•			•			
74AUP2G3407	buffer and buffer with open-drain	1.1 - 3.6	± 1.9	4.1	-40~125		•	•						•			•			
74AUP3G0434	dual inverter and single buffer	1.1 - 3.6	± 1.9	4	-40~125	•			•	•			•		•			•		
74AUP3G3404	dual buffer and single inverter	1.1 - 3.6	± 1.9	4	-40~125	•			•	•			•		•			•		
74AXP1G00	single 2-input NAND gate	0.7 - 2.75	± 4.5	2.7	-40~85		•							•			•		•	
74AXP1G02	single 2-input NOR gate	0.7 - 2.75	± 4.5	2.6	-40~85		•							•			•		•	
74AXP1G08	single 2-input AND gate	0.7 - 2.75	± 4.5	2.6	-40~85		•							•			•		•	
74AXP1G32	single 2-input OR gate	0.7 - 2.75	± 4.5	2.5	-40~85		•							•			•		•	

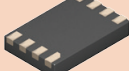
Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74AXP1G57	configurable gate; Schmitt trigger	0.7 - 2.75	± 4.5	4.6	-40~85		•							•			•			•
74AXP1G58	configurable gate; Schmitt trigger	0.7 - 2.75	± 4.5	4.5	-40~85		•							•			•			•
74AXP1G97	configurable gate; Schmitt trigger	0.7 - 2.75	± 4.5	4.5	-40~85		•							•			•			•
74AXP1G98	configurable gate; Schmitt trigger	0.7 - 2.75	± 4.5	4.5	-40~85		•							•			•			
74HC2G00	dual 2-input NAND gate	2.0 - 6.0	± 5.6	9	-40~125					•										
74HCT2G00	dual 2-input NAND gate; TTL enabled	4.5 - 5.5	± 4	12	-40~125					•										
74HC2G02	dual 2-input NOR gate	2.0 - 6.0	± 5.2	9	-40~125					•										
74HCT2G02	dual 2-input NOR gate; TTL enabled	4.5 - 5.5	± 4	12	-40~125					•										
74HC2G08	dual 2-input AND gate	2.0 - 6.0	± 5.2	9	-40~125					•										
74HCT2G08	dual 2-Input AND gate; TTL enabled	4.5 - 5.5	± 4	14	-40~125					•										
74HC2G32	dual 2-input OR gate	2.0 - 6.0	± 5.2	9	-40~125					•										
74HCT2G32	dual 2-input OR gate; TTL enabled	4.5 - 5.5	± 4.0	13	-40~125					•										
74HC2G86	dual 2-input EXCLUSIVE-OR gate	2.0 - 6.0	± 5.2	9	-40~125					•										
74HCT2G86	dual 2-input EXCLUSIVE-OR gate; TTL enabled	4.5 - 5.5	± 4.0	11	-40~125					•										
74LVC1G00	single 2-input NAND gate	1.65 - 5.5	± 32	2.2	-40~125		•	•						•			•		•	
74LVC1G02	single 2-input NOR gate	1.65 - 5.5	± 32	2.1	-40~125		•	•						•			•		•	
74LVC1G08	single 2-input AND gate	1.65 - 5.5	± 32	2.1	-40~125		•	•						•			•		•	
74LVC1G10	single 3-input NAND gate	1.65 - 5.5	± 32	2.6	-40~125		•	•						•			•			
74LVC1G11	single 3-input AND gate	1.65 - 5.5	± 32	2.6	-40~125		•	•						•			•			•
74LVC1G27	single 3-input NOR gate	1.65 - 5.5	± 32	2.6	-40~125		•	•						•			•			
74LVC1G32	single 2-input OR gate	1.65 - 5.5	± 32	2.1	-40~125		•	•						•			•		•	
74LVC1G38	single 2-input NAND gate; open-drain	1.65 - 5.5	32	2.3	-40~125		•	•						•			•		•	
74LVC1G57	configurable gate; Schmitt trigger	1.65 - 5.5	± 32	6.3	-40~125		•	•						•			•			
74LVC1G58	configurable gate; Schmitt trigger	1.65 - 5.5	± 32	6.3	-40~125		•	•						•			•			
74LVC1G86	single 2-input EXCLUSIVE-OR gate	1.65 - 5.5	± 32	2.4	-40~125		•	•						•			•			
74LVC1G97	configurable gate; Schmitt trigger	1.65 - 5.5	± 32	6.3	-40~125		•	•						•			•			
74LVC1G98	configurable gate; Schmitt trigger	1.65 - 5.5	± 32	6.3	-40~125		•	•						•			•			

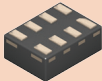
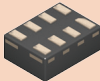
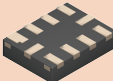
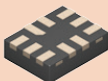
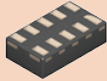
Type number	Description	V _{CC} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT833-1 (GT)	SOT886 (GM)	SOT891 (GF)	SOT902-2 (GM)	SOT996-2 (GD)	SOT1049-3 (GM)	SOT1081-2 (GF)	SOT1089 (GF)	SOT1115 (GN)	SOT1116 (GN)	SOT1160-1 (GU)	SOT1202 (GS)	SOT1203 (GS)	SOT1226 (GX)	SOT1255 (GX)
74LVC1G99	configurable gate; Schmitt trigger	1.65 - 5.5	± 32	8.4	-40~125	•			•	•			•		•			•		
74LVC1G332	single 3-input OR gate	1.65 - 5.5	± 32	2.6	-40~125		•	•						•			•			•
74LVC2G00	dual 2-input NAND gate	1.65 - 5.5	± 32	2.2	-40~125	•			•	•			•		•			•		
74LVC2G02	dual 2-input NOR gate	1.65 - 5.5	± 32	2.4	-40~125	•			•	•			•		•			•		
74LVC2G08	dual 2-input AND gate	1.65 - 5.5	± 32	2.1	-40~125	•			•	•			•		•			•		
74LVC2G32	dual 2-input OR gate	1.65 - 5.5	± 32	2.2	-40~125	•			•	•			•		•			•		
74LVC2G38	dual 2-input NAND gate; open-drain	1.65 - 5.5	32	2.1	-40~125	•			•	•			•		•			•		
74LVC2G86	dual 2-input EXCLUSIVE-OR gate	1.65 - 5.5	± 32	2.3	-40~125	•			•	•			•		•			•		
Latches/registered drivers																				
74AUP1G373	single D-type transparent latch (3-state)	1.1 - 3.6	± 1.9	8.5	-40~125		•	•						•			•			
Level shifters/translators																				
74AUP1T34	single dual supply translating buffer	1.1 - 3.6	± 1.9	15.2	-40~125		•	•						•			•		•	
74AUP1T45	single dual-supply voltage level translating transceiver (3-state)	1.1 - 3.6	± 1.9	15.6	-40~125		•	•						•			•			
74AVC1T45	single dual-supply voltage level translating transceiver (3-state)	0.8 - 3.6	± 12	2.1	-40~125		•							•			•			
74AVC2T45	dual-bit dual-supply voltage level translating transceiver (3-state)	0.8 - 3.6	± 12	2.1	-40~125	•			•				•		•			•		
74AVCH1T45	single dual-supply voltage translating transceiver with bus hold (3-state)	0.8 - 3.6	± 12	2.1	-40~125		•							•			•			
74AVCH2T45	dual-bit dual-supply voltage translating transceiver with bus hold (3-state)	0.8 - 3.6	± 12	2.1	-40~125	•			•				•		•			•		
74AXP1T57	single dual supply voltage level translating configurable multifunction gate	0.7 - 5.5	± 12	4.8	-40~85						•	•				•				
74LVC1T45	single dual-supply voltage level translating transceiver (3-state)	1.2 - 5.5	± 32	2.5	-40~125		•	•						•			•			
74LVCH1T45	single dual-supply voltage translating transceiver with bus hold (3-state)	1.2 - 5.5	± 32	2.5	-40~125		•	•						•			•			
74LVC2T45	dual-bit dual-supply voltage level translating transceiver (3-state)	1.2 - 5.5	± 32	2.5	-40~125	•			•	•			•		•			•		

[illegible]

MICROPAK PACKAGES

The MicroPak packages include 5-pin XSON and X2SON, 6-pin XSON and X2SON, 8-pin XSON and XQFN variants, as well as 10-pin XSON and XQFN variants.

Package suffix	GX	GX	GM	GF	GS	GN	GD	GM
	X2SON5	X2SON6	XSON6	XSON6	XSON6	XSON6	XSON8	XQFN8
								
Package	SOT1226	SOT1255	SOT886	SOT891	SOT1202	SOT1115	SOT996-2	SOT902-2
Width (mm)	0.80	0.80	1.00	1.00	1.00	1.00	3.00	1.60
Length (mm)	0.80	1.00	1.45	1.00	1.00	0.90	2.00	1.60
Height (mm)	0.35	0.35	0.50	0.50	0.35	0.35	0.50	0.50
Pitch (mm)	0.40	0.40	0.50	0.35	0.35	0.30	0.50	0.50

Package suffix	GT	GF	GS	GN	GM	GU	GF
	XSON8	XSON8	XSON8	XSON8	XQFN10	TSSOP8	XSON10
							
Package	SOT833-1	SOT1089	SOT1203	SOT1116	SOT1049-2	SOT1160-1	SOT1081-2
Width (mm)	1.00	1.00	1.00	1.00	1.55	1.40	1.00
Length (mm)	1.95	1.35	1.35	1.20	2.00	1.80	1.70
Height (mm)	0.50	0.50	0.35	0.35	0.50	0.50	0.50
Pitch (mm)	0.50	0.35	0.35	0.30	0.50	0.40	0.35

For more information about our MicroPak portfolio visit

www.nxp.com/products/discretes-and-logic/logic/micropak:GRP_12584

For information about automotive compliant devices visit

www.nxp.com/products/automotive-products/discrete-and-logic/logic:MC_71523

