

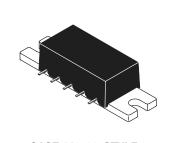
The RF Line UHF Linear Amplifier

Designed specifically for linear amplifier applications in the cellular frequency band. Internal DC blocking on RF ports reduces external component count and related circuit area. This device can be easily combined for higher power applications.

- Supply Voltage: 15 Vdc
- Third Order Intercept: 43 dBm Typ
- Power Gain: 20 dB Typ (@ f = 900 MHz)
- Excellent Phase Linearity and Group Delay Characteristics
- 50 Ohm Input/Output Impedances



1.3 W, 20 dB 800–960 MHz LINEAR AMPLIFIER



CASE 448-02, STYLE 2

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise stated)

Rating	Symbol	Value	Unit				
DC Supply Voltage	V _{CC}	18	Vdc				
RF Input Power	P _{in}	+20	dBm				
Operating Case Temperature Range	T _C	-20 to +100	°C				
Storage Temperature Range	T _{stg}	-40 to +100	°C				

ELECTRICAL CHARACTERISTICS (V_{CC} = 15 Vdc, T_C = 25°C; 50 Ω System, unless otherwise noted)

Characteris	stic	Symbol	Min	Тур	Max	Unit
Supply Current		I _{DC}	700	—	760	mA
Power Gain (1)	(f = 900 MHz)	P _G	19	20	21	dB
Absolute Phase Variation (1)	(f = 900 MHz)	Δφ		±8	±18	Deg.
Gain Flatness	(f = 800–960 MHz)	G _F	—	0.5	0.75	dB
Power Output @ 1 dB Comp.	(f = 900 MHz)	P _{out} 1 dB	30	31	—	dBm
Input VSWR	(f = 800–920 MHz) (f = 920–960 MHz)	VSWR _{in}	_	1.25:1 1.50:1	1.5:1 1.9:1	
Output VSWR	(f = 800–960 MHz)	VSWR _{out}	—	1.2:1	1.5:1	
Third Order Intercept (f1 = 879 MHz, f2 = 884 MHz)		ITO	42	43	—	dBm
Noise Figure	(f = 960 MHz)	NF	—	7.5	9.5	dB

(1) Consult factory for tighter gain and/or phase windows.

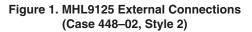


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PIN CONFIGURATION

 $\text{C1} \geq \text{0.01} \; \mu\text{F} \; \text{(CHIP)}$



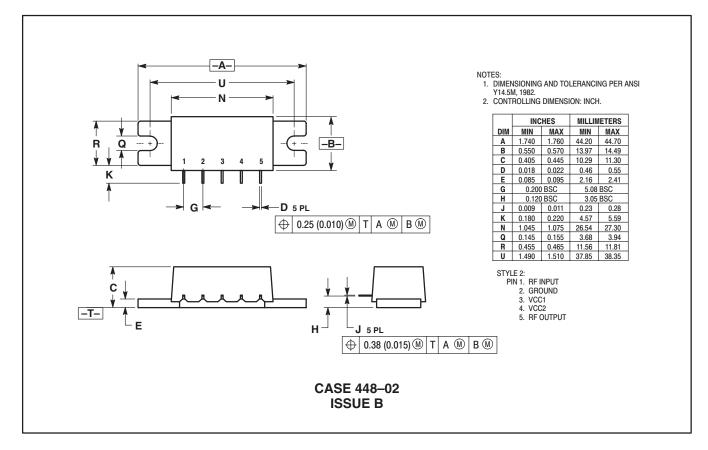
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NOTES



PACKAGE DIMENSIONS



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