. . eescale Semiconductor

Document Number: MHW8267A Rev. 2, 5/2006

Replaced by MHW8267AN. There are no form, fit or function changes with this part replacement. N suffix indicates RoHS compliant part.

Gallium Arsenide CATV Amplifier Module

Features

- Specified for 79- and 112-Channel Loading
- Excellent Distortion Performance
- Higher Output Capability
- Built-in Input Diode Protection
- GaAs FET Transistor Technology
- Unconditionally Stable Under All Load Conditions
- Output Port Ring Wave Protection

Applications

- CATV Systems Operating in the 47 to 870 MHz Frequency Range
- Output Stage Amplifier in Optical Nodes, Line Extenders and Trunk
 Distribution Amplifiers for CATV Systems
- Driver Amplifier in Linear General Purpose Applications

Description

INFORM

CHIVE

• 24 Vdc Supply, 47 to 870 MHz, CATV GaAs Forward Power Doubler Amplifier Module

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
RF Voltage Input (Single Tone)	V _{in}	+70	dBmV
DC Supply Voltage	V _{CC}	+26	Vdc
Operating Case Temperature Range	T _C	-20 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +100	°C

Table 2. ESD Maximum Ratings

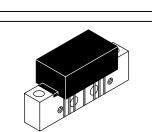
Rating	Input Value	Output Value	Unit
Surge Voltage per IEC 1000-4-5	200	200	V
Human Body Model per Mil. Std. 1686	2	2	kV

Table 3. Electrical Characteristics (V_{CC} = 24 Vdc, T_C = +45°C, 75 Ω system unless otherwise noted)

	Symbol	Min	Тур	Max	Unit	
Frequency Range		BW	40	—	870	MHz
Power Gain	870 MHz	Gp	27	27.6	28.2	dB
Slope	47-870 MHz	S	0	0.7	1.4	dB
Gain Flatness (40-870 MHz, Peak-to-Valley)		G _F	_	—	0.7	dB
Return Loss — Input		IRL				dB
(Z _o = 75 Ohms)	47-500 MHz		20	—	—	
	501-750 MHz		18		—	
	751-870 MHz		16	-	—	



© Freescale Semiconductor, Inc., 2006. All rights reserved.



CASE 1302-01, STYLE 1

MHW8267A

870 MHz 27.6 dB GAIN 112-CHANNEL GaAs CATV AMPLIFIER MODULE



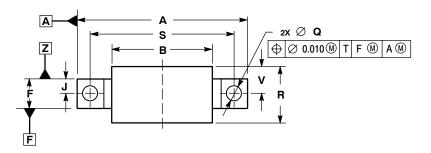
Table 3. Electrical Characteristics	V_{CC} = 24 Vdc, T_{C} = +45°C, 75 Ω sys	tem unless otherwise noted) (continued)
-------------------------------------	---	---

Characteri	Symbol	Min	Тур	Max	Unit	
Return Loss — Output	ORL				dB	
(Z _o = 75 Ohms)	47-160 MHz		20			
()	f > 160-700 MHz		18		_	
	f > 701-870 MHz		16			
Composite Second Order						dBc
(V _{out} = +48 dBmV/ch., Worst Case)	112-Channel FLAT	CSO ₁₁₂	_	-64	-62	
(V _{out} = +48 dBmV/ch., Worst Case)	79-Channel FLAT	CSO ₇₉	—	-68	-66	
(V _{out} = +56 dBmV @ 870 MHz Equiv)	112-Channel, 12db Tilt	CSO ₁₁₂		-64	-62	
(V _{out} = +58 dBmV @ 870 MHz Equiv)	79-Channel, 12db Tilt	CSO ₇₉	—	-69	-67	
Cross Modulation Distortion @ Ch 2						dBc
(V _{out} = +48 dBmV/ch., FM = 55 MHz)	112-Channel FLAT	XMD ₁₁₂	_	-57	- 55	
(V _{out} = +48 dBmV/ch., FM = 55 MHz)	79-Channel FLAT	XMD ₇₉	—	-59	-57	
(V _{out} = +56 dBmV @ 870 MHz Equiv)	112-Channel, 12db Tilt	XMD ₁₁₂	—	-52	- 50	
(V _{out} = +58 dBmV @ 870 MHz Equiv)	79-Channel, 12db Tilt	XMD ₇₉	—	-55	-52	
Composite Triple Beat						dBc
(V _{out} = +48 dBmV/ch., Worst Case)	112-Channel FLAT	CTB ₁₁₂	—	-59	-57	
(V _{out} = +48 dBmV/ch., Worst Case)	79-Channel FLAT	CTB ₇₉		-66	-64	
(V _{out} = +56 dBmV @ 870 MHz Equiv)	112-Channel, 12db Tilt	CTB ₁₁₂	—	-57	- 55	
(V _{out} = +58 dBmV @ 870 MHz Equiv)	79-Channel, 12db Tilt	CTB ₇₉	—	-62	-60	
Noise Figure	50 MHz	NF	_	5.5		dB
	550 MHz		—	5.5	—	
	750 MHz		—	5.8	—	
	870 MHz			6.0		
DC Current (V_{DC} = 24 V, T_{C} = 45°C)		I _{DC}	410	440	460	mA

MHW8267A



PACKAGE DIMENSIONS



2X U

->-

4X G

2X 6-32UNC-2B

E

Е

⊕ Ø 0.020 M T A M X

7X D

⊕ Ø 0.010 M Z T A M

С

≮ ≮

Ζ

X

NOTES: 1. DIMENSIONS ARE IN INCHES. 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.

	INCHES		MILLIN	NETERS
DIM	MIN	MAX	MIN	MAX
Α		1.775		45.085
В		1.085		27.559
c		0.840		21.336
D	0.015	0.021	0.381	0.533
E	0.465	0.510	11.811	12.954
F	0.300	0.325	7.62	8.255
G	0.100) BSC	2.540	BSC
J	0.15	6 BSC	3.962 BSC	
Κ	0.315	0.355	8.001	9.017
L	1.000 BSC		25.400 BSC	
Ν	0.165 BSC		4.191 BSC	
Ρ	0.100 BSC		2.540	BSC
Q	0.148	0.168	3.759	4.267
R		0.600		15.24
S	1.500 BSC		38.100 BSC	
U	0.200 BSC		5.080 BSC	
۷		0.250	6.3	
W	0.435		11.049	
X	0.400 BSC		10.160 BSC	
Y	0.152	0.163	3.861	4.140
Ζ	0.009	0.011	0.229	0.279

STYLE 1:
PIN 1. RF INPUT
2. GROUND
GROUND
DELETED
5. VDC
DELETED
7. GROUND
8. GROUND
9. RF OUTPUT

Ŵ

Ρ

CASE 1302-01 **ISSUE B**

Ν

(†

∠₂x Ø Y $\oplus \oslash$ 0.010 M Z T A M



How to Reach Us:

Home Page: www.freescale.com

E-mail: support@freescale.com

USA/Europe or Locations Not Listed:

Freescale Semiconductor Technical Information Center, CH370 1300 N. Alma School Road Chandler, Arizona 85224 +1-800-521-6274 or +1-480-768-2130 support@freescale.com

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) support@freescale.com

Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd. Technical Information Center 2 Dai King Street Tai Po Industrial Estate Tai Po, N.T., Hong Kong +800 2666 8080 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217 1-800-441-2447 or 303-675-2140 Fax: 303-675-2150 LDCForFreescaleSemiconductor@hibbertgroup.com Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale[™] and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © Freescale Semiconductor, Inc. 2006. All rights reserved.

RoHS-compliant and/or Pb-free versions of Freescale products have the functionality and electrical characteristics of their non-RoHS-compliant and/or non-Pb-free counterparts. For further information, see http://www.freescale.com or contact your Freescale sales representative.

For information on Freescale's Environmental Products program, go to http://www.freescale.com/epp.

