

Product Preview Silicon Lateral FET, N-Channel Enhancement-Mode MOSFET

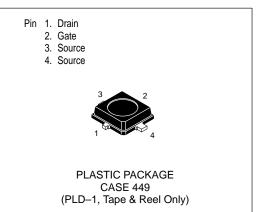
Designed for use in low voltage, moderate power amplifiers such as portable analog and digital cellular radios and PC RF modems.

- Performance Specifications at 900 MHz, 4.8 V Output Power = 36 dBm Typ Power Gain = 10 dB Typ Efficiency = 65% Typ
- Guaranteed Ruggedness at Load VSWR = 10:1
- New Plastic Surface Mount Package



HIGH FREQUENCY POWER TRANSISTOR LDMOS FET 36 dBm, 900 MHz

> SEMICONDUCTOR TECHNICAL DATA



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain–Source Voltage	V _{DSS}	20	Vdc
Drain–Gate Voltage (R_{GS} = 1.0 M Ω)	V _{DGO}	20	Vdc
Gate-Source Voltage	V _{GS}	±7.0	Vdc
Drain Current – Continuous	۱ _D	1.1	Adc
Total Device Dissipation @ T _C = 50°C Derate above 50°C	PD	12.5 125	W mW/°C
Storage Temperature Range	T _{stg}	-65 to 150	°C
Operating Junction Temperature	ТJ	150	°C

ORDERING INFORMATION

	Device	Marking	Package		
	MRF9482T1	9482	PLD–1 Tape & Reel*		
	*1 000 Unite ner 10 mm. 7 inch real				

1,000 Units per 12 mm, 7 inch reel.

NOTE: ESD data available upon request.

THERMAL CHARACTERISTICS

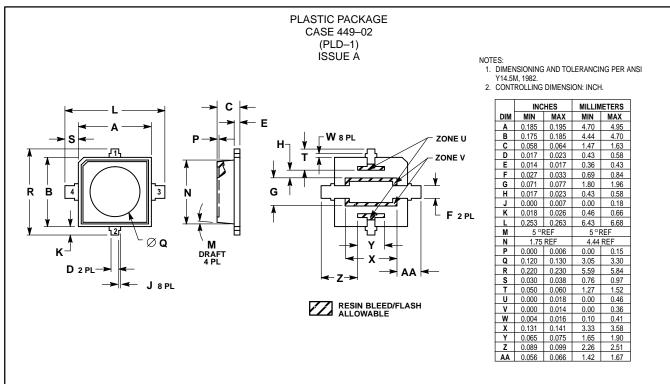
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	8.0	°C/W

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OUTLINE DIMENSIONS



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⁶For More Information On This Product, Go to: www.freescale.com