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Date	OTP Rev	OTP Changes
April 26 2019	Rev A / Rev 1.0	Initial prototype release of the OTP settings for A0 silicon
June 03 2020	Rev D / Rev 1.0	<p>Revised prototype release of the OTP settings for B0 silicon</p> <ol style="list-style-type: none"> <li>1. Added new OTP settings for capability to monitor an external PMIC for the core supply during standby transitions: PSYNC_PGOOD_EXT_OTP, EXT_STBY_DISCH_OTP, STBY_PGOOD_DLY_OTP</li> <li>2. Added new OTP bits for watchdog INIT timeout: WD_INIT_TIMEOUT_OTP</li> <li>3. Added new OTP bits for BUCK1/2/3 soft start configurability: BUCK12DVS_RAMP_OTP, BUCK3_RAMP_OTP</li> <li>4. Added new OTP bits for VPRES MOSFET slew rate control: VPRESRHS_MSB_OTP</li> <li>5. Added new OTP bits for VPRES TON control in PWM: VPRES_TON_MIN_OTP</li> <li>6. VPRES slope compensation changed to 41.4mV/us from 200mV/us</li> <li>7. VPRES minimum OFF time changed to 80ns from 40ns</li> <li>8. VPRES minimum ON time in PFM changed to 550ns from 210ns</li> <li>9. BOOST slew rate changed to 500V/us from 100V/us</li> <li>10. OTP slot for BUCK1/2 changed to slot 2 from slot 4</li> <li>11. OTP slot for BUCK3 changed to slot 1 from slot 2</li> <li>12. OTP slot for LDO1, LDO2 changed to slot 0 from slot 1</li> <li>13. OTP slot for HVLDO changed to slot 1 from slot 3</li> <li>14. Following Functional safety settings were changed to enabled from disabled: Fault Recovery, Watchdog, FCCU</li> <li>15. UV TH values for all the monitors were changed to 95.5% from 90%</li> <li>16. OV TH values for all the monitors were changed to 104.5% from 110%</li> <li>17. UV Debounce values for all the monitors were changed to 25us from 40us</li> <li>18. OV Debounce values for all the monitors were changed to 25us from 45us</li> </ol>
Oct 16 2020	Rev F / Rev 1.0	<ol style="list-style-type: none"> <li>1. OTP slot for BUCK1/2 changed to slot 3 from slot 2</li> <li>2. OTP slot for HVLDO changed to slot 2 from slot 1</li> <li>3. Following VMON thresholds were changed:               <ol style="list-style-type: none"> <li>a. VCOREMON OV: to 6% from 4.5%</li> <li>b. VDDIOMON UV: to 5% from 4.5%</li> <li>c. VDDIOMON OV: to 5% from 4.5%</li> <li>d. HVLDOMON UV: to 7% from 4.5%</li> <li>e. HVLDOMON OV: to 7% from 4.5%</li> <li>f. VMON1 OV: to 6% from 4.5% (VPRES_3.3V)</li> <li>g. VMON2 UV: to 2.5% from 4.5% (BUCK3_1.1V)</li> <li>h. VMON3 UV: to 5% from 4.5% (LDO2_1.8V)</li> <li>i. VMON3 OV: to 5% from 4.5% (LDO2_1.8V)</li> <li>j. VMON4 UV: to 5% from 4.5% (LDO1_1.8V)</li> <li>k. VMON4 OV: to 5% from 4.5% (LDO1_1.8V)</li> </ol> </li> <li>4. BOOST slope compensation changed to 67mV/us from 160mV/us</li> <li>5. BOOST compensation resistor changed to 500kOhm from 750kOhm</li> </ol>
Mar 3 2021	Rev G / Rev 1.0	<p>Production release of the OTP</p> <ol style="list-style-type: none"> <li>1. ABIST1 to RSTB Delay: Changed to 5ms Delay from No Delay (default)</li> <li>2. BUCK3 Non DVS Ramp: Changed to 3.47mV/us (power up/down) from 10.42mV/us (power up/down)</li> <li>3. Standby PGOOD Release Delay: Changed to 400us from 300us</li> <li>4. LDO1 Sequence: Changed to Slot 1 from Slot 0</li> </ol>
Sept 8 2021	Rev G / Rev 1.1	<ol style="list-style-type: none"> <li>1. Fixed typographical errors in the previously released rev G production OTP</li> </ol>