

S32M242AD

S32M242/1 Data Sheet addendum

Rev. 1 — 19 March 2025

Preliminary Data Sheet

Introduction

This addendum document is extension of Rev6 S32M2xx datasheet and includes current values for S32M242/1. All other electrical and physical specifications remain the same, see revision 6 of chip's primary Data Sheet.

S32M242 current consumption

Table 1. S32M242 current consumption

Modes	Chip	Unit	@ 25 °C			@ 85 °C			@ 105 °C			@ 125 °C		
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max
RUN Mode, core at 48 MHz	S32M242, S32M241	mA	-	41.1	48.7	-	41.6	49.3	-	42.7	51.3	-	43.8	53.9
RUN Mode, core at 80 MHz	S32M242, S32M241	mA	-	50.7	58.1	-	51.3	59.1	-	52.2	59.8	-	53.6	62.8
SLEEP Mode	S32M242, S32M241	µA	-	108	229	-	166	564	-	250	1008	-	404	1981
DEEP_SLEEP Mode (VSUP=12V)	S32M242, S32M241	µA	-	46	66	-	53	74	-	58	85	-	65	117



Legal information

Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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