

MC68HC(9)08 A Family Data Sheets

1 Description

This erratum applies to data sheets for MC68HC(9)08 A family devices. In the electrical specification section of these data sheets, the PLL jitter deviation of average bus frequency over 2 ms the unit value has been specified incorrectly. This erratum provides a replacement table entry that will appear in data sheet revisions.

2 Documents Affected

The following data sheets are affected:

MC68HC08AB16A	MC68HC08AZ32A
MC68HC908AB32	MC68HC908AZ32A
MC68HC908AS32A	MC68HC908AZ60
MC68HC908AS60	MC68HC908AZ60A

3 Correction

The correct specification is given in the following table.

Description ¹	Symbol	Min	Typ ²	Max ⁽²⁾	Unit	Notes
PLL Jitter, Deviation of Average Bus Frequency over 2 ms ³		0	—	$\pm (f_{\text{CRYST}}) \times (.025\%) \times (N/4)$	Hz	N = VCO Freq. Mult.

¹ $V_{\text{DD}} = 5.0 \text{ Vdc} \pm 0.5 \text{ V}$, $V_{\text{SS}} = 0 \text{ Vdc}$, $T_{\text{A}} = -40^{\circ}\text{C}$ to $T_{\text{A}} (\text{MAX})$, unless otherwise noted.

² Conditions for typical and maximum values are for run mode with $f_{\text{CGMXCLK}} = 8 \text{ MHz}$, $f_{\text{BUSDES}} = 8 \text{ MHz}$, $N = 4$, $L = 7$, discharged $C_{\text{F}} = 15 \text{ nF}$, $V_{\text{DD}} = 5 \text{ Vdc}$.

³ Guaranteed by not tested.

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