

Datapath Switching

Errata to **MC92501 ATM Cell Processor**

This document describes errata of the following silicon of the MC92501 ATM Cell Processor:

- MC92501GC, December, 1999

1.1 Summary Table

Table 1-1 provides a summary of MC92501 errata.

Table 1-1 Summary of MC92501 Errata

Errata #	Problem	Impact	Work Around	Silicon Revisions Affected
				GC
1	The JTAG boundary scan register bit definition is updated.	The backward compatibility of the 92501 to the 92500 is marginally reduced.	None	✓

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Errata #1: JTAG boundary scan register bit definition is updated

Description:	Because the MC92501 contains some additional signals that the MC92500 does not have, the JTAG boundary scan register bit definition of the MC92501 is updated, is changed from the MC92500.
Projected Impact:	The backward compatibility of the 92501 to the 92500 is marginally reduced.
Solution:	A fix is planned for the MC92501.
Work Around:	None
Errata Applies To:	MC92501GC

1.2 Revision History

Table 1-2 contains a brief description of the technical updates made to this document.

Table 1-2 Revision History Table

Date	Document Revision	Substantive Changes
14 October, 1999	Rev 1.1	Errata 1, 2, and 4: Changed "No fix is planned for the MC92501" to "A fix is planned for the MC92501."
15 December, 1999	Rev 1.2	Changed all occurrences of MC92501 to XC92501 to reflect the true name of the silicon. Added the word "Silicon" to "Revisions Affected" in Table 1.
15 December, 1999	Rev 1.0	Created errata document for MC92501GC silicon. Deleted errata 1, 2, 3, and 5 that applied to XC92501 GC and XC92501GCA.





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