



© NXP SEMICONDUCTORS N. V. ALL RIGHTS RESERVED	MECHANICAL OUTLINE	PRINT VERSION NOT TO SCALE
TITLE: NI-400-240	DOCUMENT NO: 98ASA10730D	REV: C
	STANDARD: NON-JEDEC	
	SOT1812-2	11 MAR 2016



NOTES:

1. CONTROLLING DIMENSION: INCH
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
3. DIMENSION H IS MEASURED .030 (0.762) AWAY FROM PACKAGE BODY.
4. INFORMATION ONLY:
CORNER BREAK (4X) TO BE .060±.005 (1.52±0.13) RADIUS OR
.06±.005 (1.52±0.13) x 45° CHAMFER.

STYLE 1

- PIN 1: DRAIN
- PIN 2: GATE
- PIN 3: SOURCE

STYLE 2

- PIN 1: GATE
- PIN 2: DRAIN
- PIN 3: SOURCE

DIM	INCH		MILLIMETER		DIM	INCH		MILLIMETER	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
A	.795	.805	20.19	20.44	R	.355	.365	9.02	9.27
B	.380	.390	9.65	9.91	S	.365	.375	9.27	9.53
C	.125	.163	3.17	4.14					
D	.275	.285	6.98	7.24	aaa	.005		0.127	
E	.035	.045	0.89	1.14	bbb	.010		0.254	
F	.004	.006	0.10	0.15	ccc	.015		0.381	
G	.600 BSC		15.24 BSC						
H	.057	.067	1.45	1.70					
K	.0995	.1295	2.53	3.29					
M	.395	.405	10.03	10.29					
N	.385	.395	9.78	10.03					
Q	∅.120	∅.130	∅3.05	∅3.30					

© NXP SEMICONDUCTORS N.V.
ALL RIGHTS RESERVED

MECHANICAL OUTLINE

PRINT VERSION NOT TO SCALE

TITLE: NI-400-240	DOCUMENT NO: 98ASA10730D	REV: C
	STANDARD: NON-JEDEC	
	SOT1812-2	11 MAR 2016