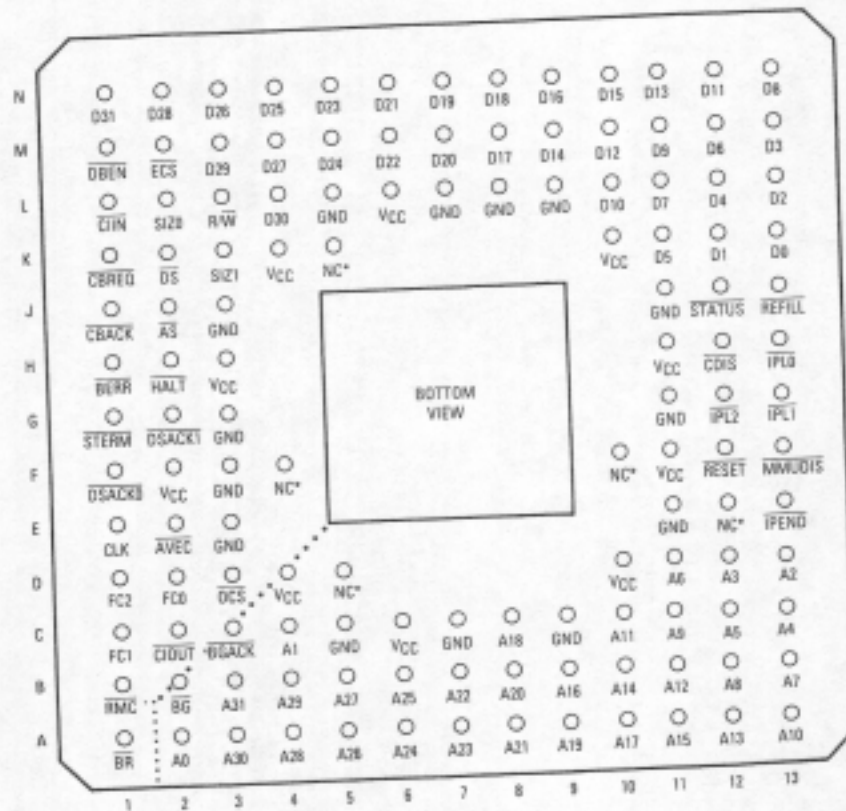


## 14.2 PIN ASSIGNMENTS — PIN GRID ARRAY (RC SUFFIX)

The VCC and GND pins are separated into three groups to provide individual power supply connections for the address bus buffers, data bus buffers, and all other output buffers and internal logic.

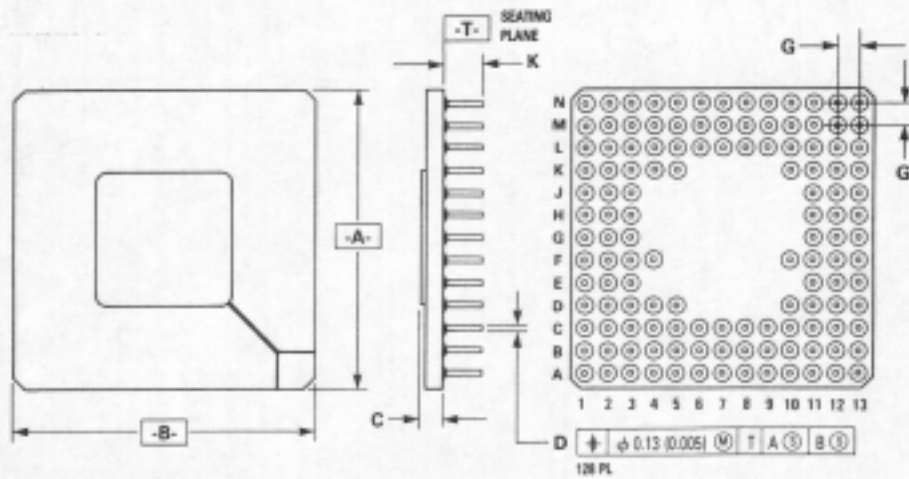
Pin Group	VCC	GND
Address Bus	C6, D10	C5, C7, C9, E11
Data Bus	L6, K10	J11, L9, L7, L5
$\overline{ECS}$ , $SI2x$ , $\overline{DS}$ , $\overline{AS}$ , $\overline{DBEN}$ , $\overline{CBREQ}$ , $R/W$	K4	J3
$\overline{FC0-FC2}$ , $\overline{RMC}$ , $\overline{OCS}$ , $\overline{CIOUT}$ , $\overline{BG}$	D4	E3
Internal Logic, $\overline{RESET}$ , $\overline{STATUS}$ , $\overline{REFILL}$ , Misc.	H3, F2, F11, H11	L8, G3, F3, G11



\*NC — Do not connect to this pin.

## 14.4 PACKAGE DIMENSIONS

MC68030  
PC Suffix Package  
Case 789C-01



NOTES:

1. A AND B ARE DATUMS AND T IS A DATUM SURFACE.
2. DIMENSIONING AND TOLERANCING PER Y14.5M, 1982.
3. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	34.04	35.05	1.340	1.380
B	34.04	35.05	1.340	1.380
C	2.54	3.81	0.100	0.150
D	0.44	0.56	0.017	0.022
G	2.54 BSC		0.100 BSC	
K	4.32	4.88	0.170	0.195