



MPC821 Pin List (Rev A)

Applies to Rev A MPC821 and later

Updated 5/14/97

<u>Pin Functions (spec name)</u>	<u>Pin Name</u>	<u>Ball</u>
ale_a	alea	K2
ale_b/dsck/at1	aleb	J1
a[0]	a[0]	B19
a[10]	a[10]	B15
a[11]	a[11]	A15
a[12]	a[12]	C14
a[13]	a[13]	B14
a[14]	a[14]	A14
a[15]	a[15]	D12
a[16]	a[16]	C13
a[17]	a[17]	B13
a[18]	a[18]	D9
a[19]	a[19]	D11
a[1]	a[1]	B18
a[20]	a[20]	C12
a[21]	a[21]	B12
a[22]	a[22]	B10
a[23]	a[23]	B11
a[24]	a[24]	C11
a[25]	a[25]	D10
a[26]	a[26]	C10
a[27]	a[27]	A13
a[28]	a[28]	A10
a[29]	a[29]	A12
a[2]	a[2]	A18
a[30]	a[30]	A11
a[31]	a[31]	A9
a[3]	a[3]	C16
a[4]	a[4]	B17
a[5]	a[5]	A17
a[6]	a[6]	B16
a[7]	a[7]	A16
a[8]	a[8]	D15
a[9]	a[9]	C15
bb*	bb*	E1
bdip*/gpl_b5*	bdip*	D2
bg*	bg*	E2
bi*	bi*	E3
br*	br*	G4
bs_a0*	bsa0*	D8
bs_a1*	bsa1*	C8
bs_a2*	bsa2*	A7



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bs_a3*	bsa3*	B8
burst*	burst*	F1
ce1_a*	ce1a*	B3
ce2_a*	ce2a*	A3
clkout	clkout	W3
cs0*	cs0*	C3
cs1*	cs1*	A2
cs2*	cs2*	D4
cs3*	cs3*	E4
cs4*	cs4*	A4
cs5*	cs5*	B4
cs6*/ce1_b*	cs6*	D5
cs7*/ce2_b*	cs7*	C4
dp0/irq3*	dp0	V3
dp1/irq4*	dp1	V5
dp2/irq5*	dp2	W4
dp3/irq6*	dp3	V4
d[0]	d[0]	W14
d[10]	d[10]	V11
d[11]	d[11]	U11
d[12]	d[12]	T13
d[13]	d[13]	V13
d[14]	d[14]	V10
d[15]	d[15]	T10
d[16]	d[16]	U10
d[17]	d[17]	T12
d[18]	d[18]	V9
d[19]	d[19]	U9
d[1]	d[1]	W12
d[20]	d[20]	V8
d[21]	d[21]	U8
d[22]	d[22]	T9
d[23]	d[23]	U12
d[24]	d[24]	V7
d[25]	d[25]	T8
d[26]	d[26]	U7
d[27]	d[27]	V12
d[28]	d[28]	V6
d[29]	d[29]	W5
d[2]	d[2]	W11
d[30]	d[30]	U6
d[31]	d[31]	T7
d[3]	d[3]	W10
d[4]	d[4]	W13
d[5]	d[5]	W9
d[6]	d[6]	W7
d[7]	d[7]	W6



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d[8]	d[8]	U13
d[9]	d[9]	T11
extal	extal	N1
extclk	extclk	N2
baddr28	baddr28	M3
baddr29	baddr29	M2
baddr30/reg*	baddr30	K4
as*	as*	L3
gpl_a0*/gpl_b0*	gpl_a0*	D7
oe*/gpl_a1*/gpl_b1*	gpl_a1*	C6
gpl_a2*/gpl_b2*/cs2*	gpl_a2*	B5
gpl_a3*/gpl_b3*/cs3*	gpl_a3*	C5
upwaita/gpl_a4*	gpl_a4*	C1
gpl_a5*	gpl_a5*	D3
upwaitb/gpl_b4*	gpl_b4*	B1
hreset*	hreset*	N4
ip_a0	ipa0	T5
ip_a1	ipa1	T4
ip_a2/iois16_a*	ipa2	U3
ip_a3	ipa3	W2
ip_a4	ipa4	U4
ip_a5	ipa5	U5
ip_a6	ipa6	T6
ip_a7	ipa7	T3
ip_b0/iwp0/vfls0	ipb0	H2
ip_b1/iwp1/vfls1	ipb1	J3
ip_b2/iois16_b*/at2	ipb2	J2
ip_b3/iwp2/vf2	ipb3	G1
ip_b4/lwp0/vf0	ipb4	G2
ip_b5/lwp1/vf1	ipb5	J4
ip_b6/dsdi/at0	ipb6	K3
ip_b7/ptr*/at3	ipb7	H1
irq0*	irq0*	V14
irq1*	irq1*	U14
rsv*/irq2*	irq2*	H3
cr*/irq3*	irq3*	F2
kr*/retry*/irq4*/spkrout	irq4*	K1
frz/irq6*	irq6*	G3
irq7*	irq7*	W15
kapwr	kapwr	R1
op2/modck1/sts*	modck1	L1
op3/modck2/dsdo	modck2	M4
op0	op0	L4
op1	op1	L2
l1tclkb/tout4*/clk8/pa[0]	pa[0]	U19
l1rxdb/pa[10]	pa[10]	J17
l1txdb/pa[11]	pa[11]	G16



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txd2/pa[12]	pa[12]	F17
rxd2/pa[13]	pa[13]	E17
txd1/pa[14]	pa[14]	D17
rxd1/pa[15]	pa[15]	C18
tin4/brgo4/clk7/pa[1]	pa[1]	T19
brgclk2/l1rclkb/tout3*/clk6/pa[2]	pa[2]	R18
tin3/brgo3/clk5/pa[3]	pa[3]	P17
tout2*/clk4/pa[4]	pa[4]	P19
tin2/l1tclka/brgo2/clk3/pa[5]	pa[5]	N18
brgclk1/tout1*/clk2/pa[6]	pa[6]	M17
tin1/l1rclka/brgo1/clk1/pa[7]	pa[7]	M19
l1rdxa/pa[8]	pa[8]	L17
l1txda/pa[9]	pa[9]	K18
rstrt1*/pb[14]	pb[14]	U18
brgo3/pb[15]	pb[15]	R17
l1st4/l1rqa/pb[16]	pb[16]	N16
l1st3/l1rqb/pb[17]	pb[17]	P18
l1st2/rts2*/pb[18]	pb[18]	N17
l1st1/rts1*/pb[19]	pb[19]	N19
smrxd2/l1clkoa/pb[20]	pb[20]	L16
smtx2/l1clkob/pb[21]	pb[21]	K16
smsyn2*/sdack2*/pb[22]	pb[22]	L19
smsyn1*/sdack1*/pb[23]	pb[23]	K17
smrxd1/pb[24]	pb[24]	J18
smtx1/pb[25]	pb[25]	J16
brgo2/i2cscl/pb[26]	pb[26]	F19
brgo1/i2csda/pb[27]	pb[27]	E19
brgo4/spimiso/pb[28]	pb[28]	D19
spimosi/pb[29]	pb[29]	E16
spiclk/pb[30]	pb[30]	C19
reject1*/spisel*/pb[31]	pb[31]	C17
tgate1*/cd1*/pc[10]	pc[10]	K19
cts1*/pc[11]	pc[11]	J19
l1st4/l1rqa/pc[12]	pc[12]	F18
l1st3/l1rqb/pc[13]	pc[13]	E18
l1st2/rts2*/dreq2*/pc[14]	pc[14]	D18
l1st1/rts1*/dreq1*/pc[15]	pc[15]	D16
l1rsynca/pc[4]	pc[4]	T17
sdack1*/l1tsynca/pc[5]	pc[5]	T18
l1rsynca/pc[6]	pc[6]	R19
sdack2*/l1tsynca/pc[7]	pc[7]	M16
tgate2*/cd2*/pc[8]	pc[8]	M18
cts2*/pc[9]	pc[9]	L18
pd[10]/ld3	pd[10]	W18
pd[11]/ld4	pd[11]	T16
pd[12]/ld5	pd[12]	R16
pd[13]/ld6	pd[13]	V18



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pd[14]/ld7	pd[14]	V19
pd[15]/ld8	pd[15]	U17
pd[3]/shift/clk	pd[3]	W16
pd[4]/load/hsync	pd[4]	U16
pd[5]/frame/vsync	pd[5]	U15
pd[6]/loe/lcd_ac	pd[6]	V16
pd[7]/ld0	pd[7]	T15
pd[8]/ld1	pd[8]	W17
pd[9]/ld2	pd[9]	V17
poreset*	poreset*	R2
rstconf*	rstconf*	P3
spare1	spare1	B7
spare2	spare2	H18
spare3	spare3	V15
spare4	spare4	H4
sreset*	sreset*	P2
ta*	ta*	C2
tck/dsck	tck	H16
tdi/dsdi	tdi	H17
tdo/dsdo	tdo	G17
tea*	tea*	D1
texp	texp	N3
tms	tms	G18
trst*	trst	G19
ts*	ts*	F3
tsiz0/reg*	tsiz0	B9
tsiz1	tsiz1	C9
vddL	vddl	A8
vddL	vddl	H19
vddL	vddl	M1
vddL	vddl	W8
vddsyn	vddsyn	T1
vsssyn	vsssyn	U1
vsssyn1	vsssyn1	V1
wait_a*	wait_a*	R3
wait_b*	wait_b*	R4
we0*/bs_b0*/iord*	we0*	C7
we1*/bs_b1*/iowr*	we1*	A6
we2*/bs_b2*/pcoe*	we2*	B6
we3*/bs_b3*/pcwe*	we3*	A5
rd/wr*	wr*	B2
xfc	xfc	T2
xtal	xtal	P1
vddh	vddh	F4
vddh	vddh	F16
vddh	vddh	P4
vddh	vddh	P16



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vddh	vddh	T14
vddh	vddh	E5-E15
vddh	vddh	F5, F15
vddh	vddh	G5, G15
vddh	vddh	H5, H15
vddh	vddh	J5, J15
vddh	vddh	K5, K15
vddh	vddh	L5, L15
vddh	vddh	M5, M15
vddh	vddh	N5, N15
vddh	vddh	P5, P15
vddh	vddh	R5-R15
GND	gnd	F6 - F14
GND	gnd	G6 - G14
GND	gnd	H6 - H14
GND	gnd	J6 - J14
GND	gnd	K6 - K14
GND	gnd	L6 - L14
GND	gnd	M6 - M14
GND	gnd	N6 - N14
GND	gnd	P6 - P14
no connection	n/c	D6
no connection	n/c	D13
no connection	n/c	D14
no connection	n/c	U2
no connection	n/c	V2