

# SOT1031-1

plastic thermal enhanced very very thin quad flat package;  
no leads; 48 terminals; resin based; body 7 x 7 x 0.7 mm

8 February 2016

Package information

## 1. Package summary

<b>Terminal position code</b>	Q (quad)
<b>Package type descriptive code</b>	HWQFN48R
<b>Package type industry code</b>	HWQFN48R
<b>Package style descriptive code</b>	HWQFN (thermal enhanced very very thin quad flatpack; no leads)
<b>Package style suffix code</b>	R (resin based)
<b>Package body material type</b>	P (plastic)
<b>Mounting method type</b>	S (surface mount)
<b>Issue date</b>	1-12-2007

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	6.9	-	7	7.1	mm
E	package width	6.9	-	7	7.1	mm
A	seated height	[tbd]	-	0.7	0.8	mm
A <sub>2</sub>	package height	[tbd]	-	0.7	[tbd]	mm
n <sub>2</sub>	actual quantity of termination	-	-	48	-	

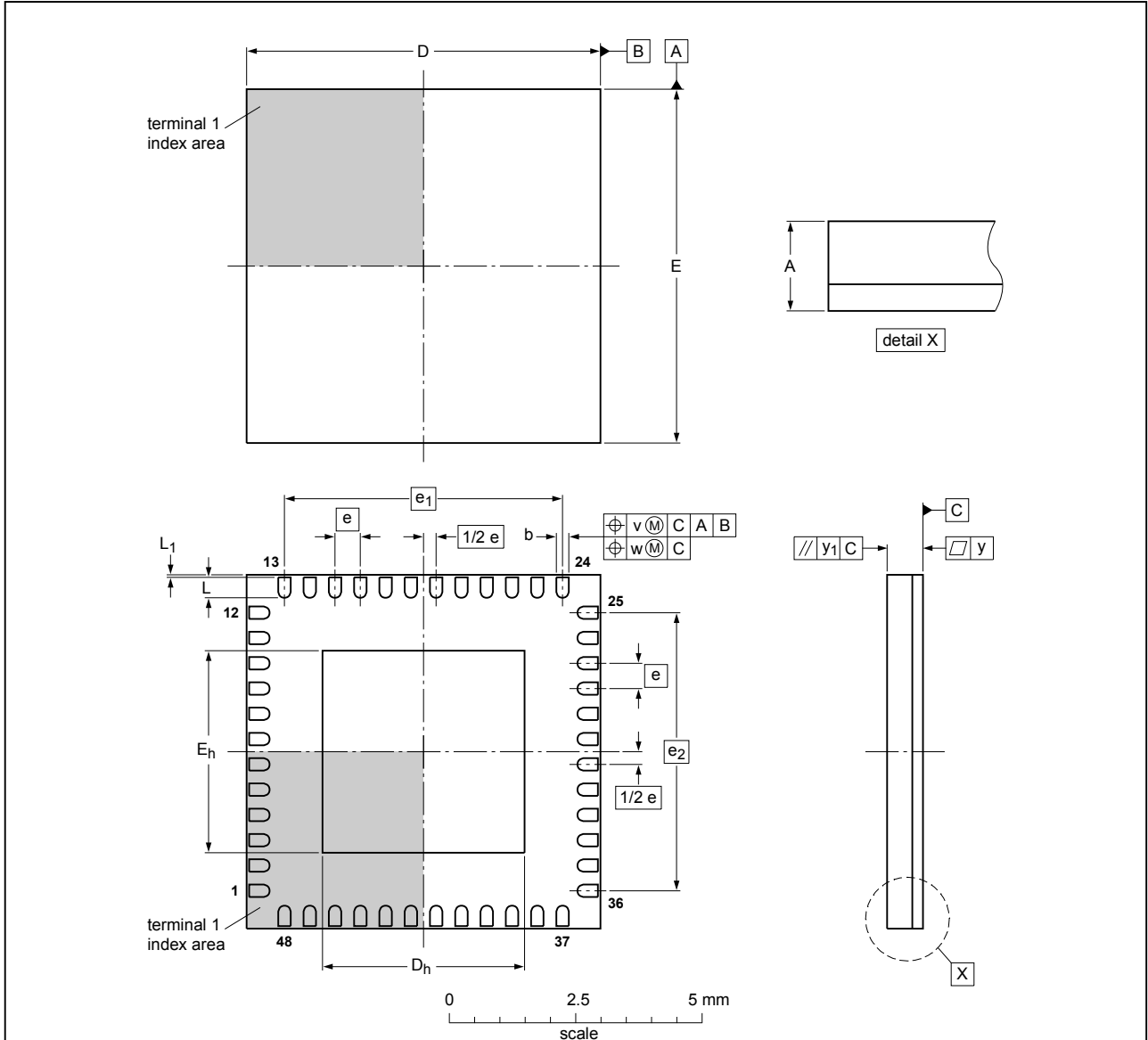


plastic thermal enhanced very very thin quad flat package; no leads; 48 terminals; resin based; body 7 x 7 x 0.7 mm

## 2. Package outline

HWQFN48R: plastic thermal enhanced very very thin quad flat package; no leads  
48 terminals; resin based; body 7 x 7 x 0.7 mm

SOT1031-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max	b	D	D <sub>h</sub>	E	E <sub>h</sub>	e	e <sub>1</sub>	e <sub>2</sub>	L	L <sub>1</sub>	v	w	y	y <sub>1</sub>
mm	0.8	0.27 0.23	7.1 6.9	4.1 3.9	7.1 6.9	4.1 3.9	0.5	5.5	5.5	0.42 0.38	0.1 0.0	0.1	0.05	0.05	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT1031-1	---	---	---			07-09-14 07-12-01

Fig. 1. Package outline HWQFN48R (SOT1031-1)

plastic thermal enhanced very very thin quad flat  
package; no leads; 48 terminals; resin based;  
body 7 x 7 x 0.7 mm

### 3. Legal information

#### Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

plastic thermal enhanced very very thin quad flat  
package; no leads; 48 terminals; resin based;  
body 7 x 7 x 0.7 mm

## 4. Contents

---

1. Package summary.....	1
2. Package outline.....	2
3. Legal information.....	3

---

© NXP Semiconductors N.V. 2016. All rights reserved

For more information, please visit: <http://www.nxp.com>  
For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)  
Date of release: 8 February 2016

---