

HWQFN32, thermal enhanced, very-very thin quad flat package, no leads, step-cut wettable flank, 0.5 mm pitch, 5 mm x 5 mm x 0.68 mm body

22 November 2021

Package information

1 Package summary

Terminal position code	Q (quad)
Package type descriptive code	HWQFN32
Package style descriptive code	HWQFN (thermal enhanced very very thin quad flatpack; no leads)
Package body material type	P (plastic)
Mounting method type	S (surface mount)
Issue date	27-10-2021
Manufacturer package code	98ASA01733D

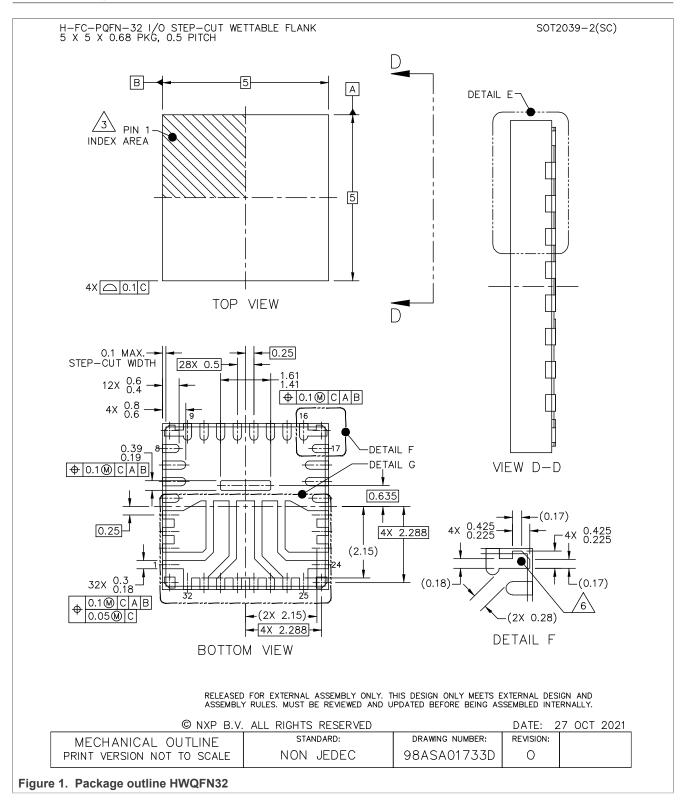
Table 1. Package summary

Parameter	Min	Nom	Мах	Unit
package length	-	5	-	mm
package width	-	5	-	mm
package height	-	0.68	0.75	mm
nominal pitch	-	0.5	-	mm

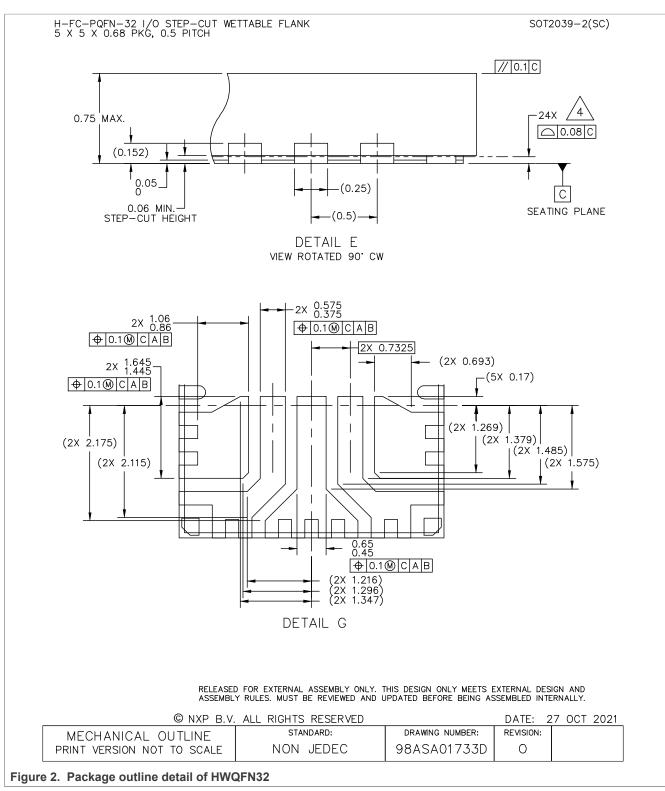


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2 Package outline

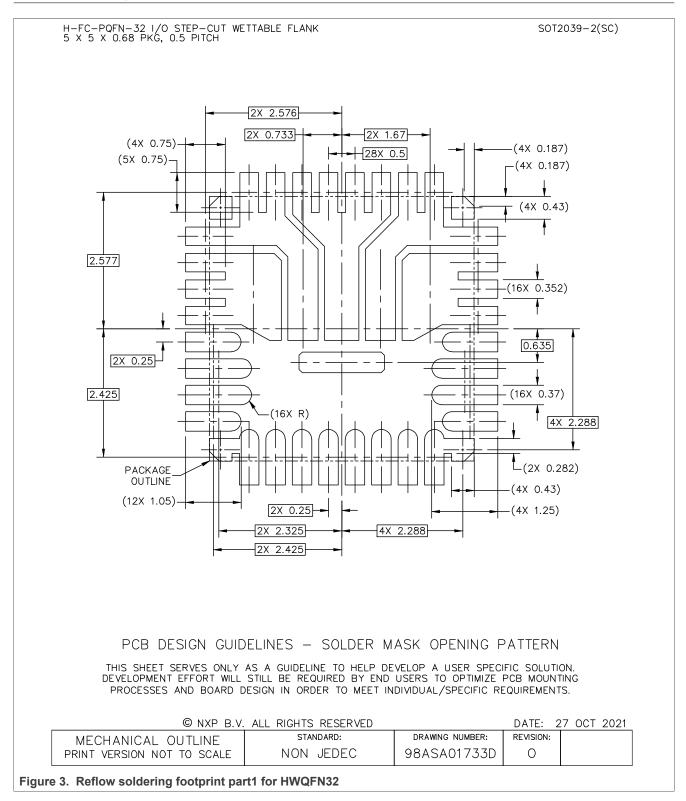


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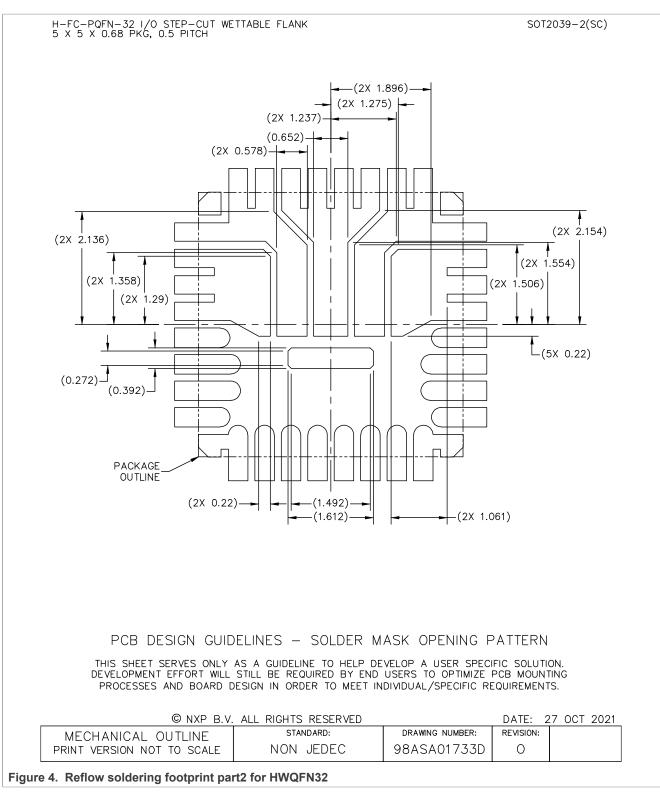


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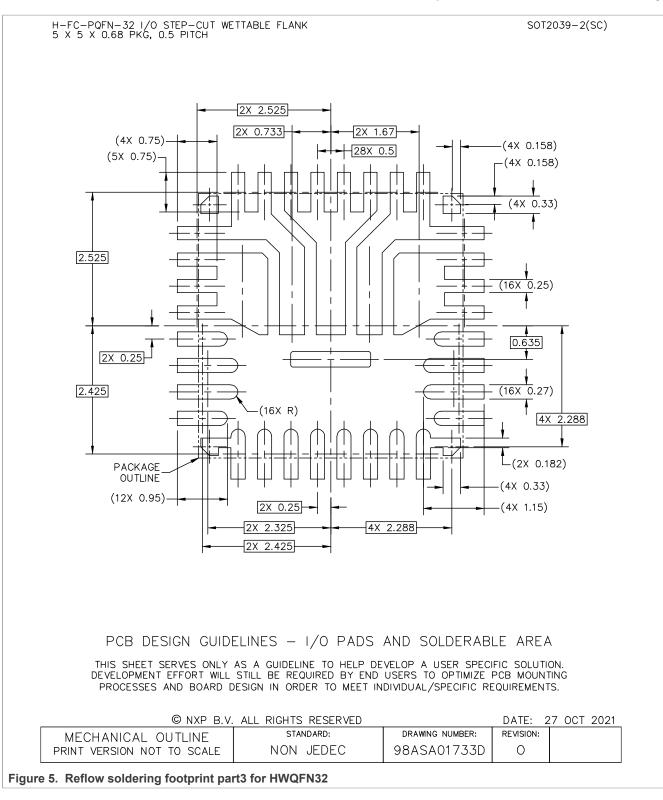
3 Soldering



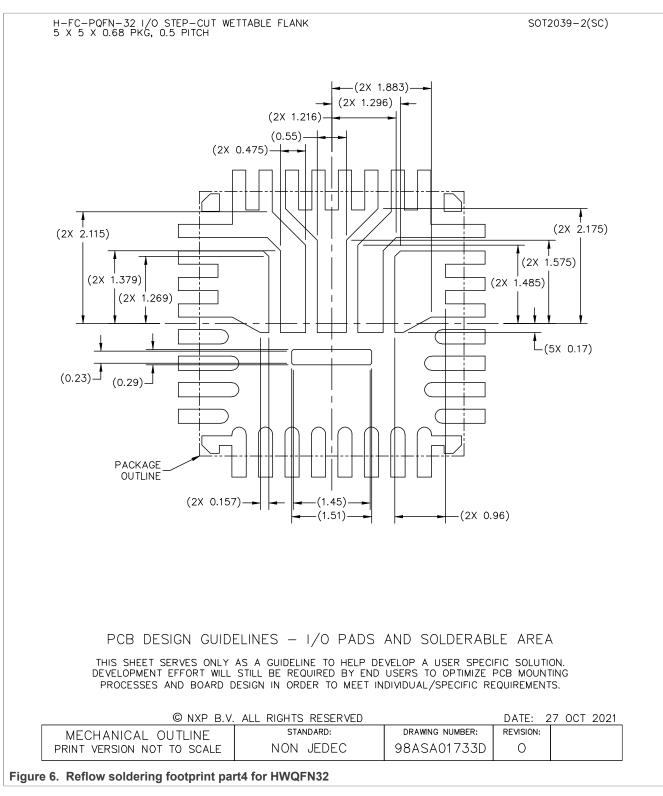
HWQFN32, thermal enhanced, very-very thin quad flat package, no leads, step-cut wettable flank, 0.5 mm pitch, 5 mm x 5 mm x 0.68 mm body



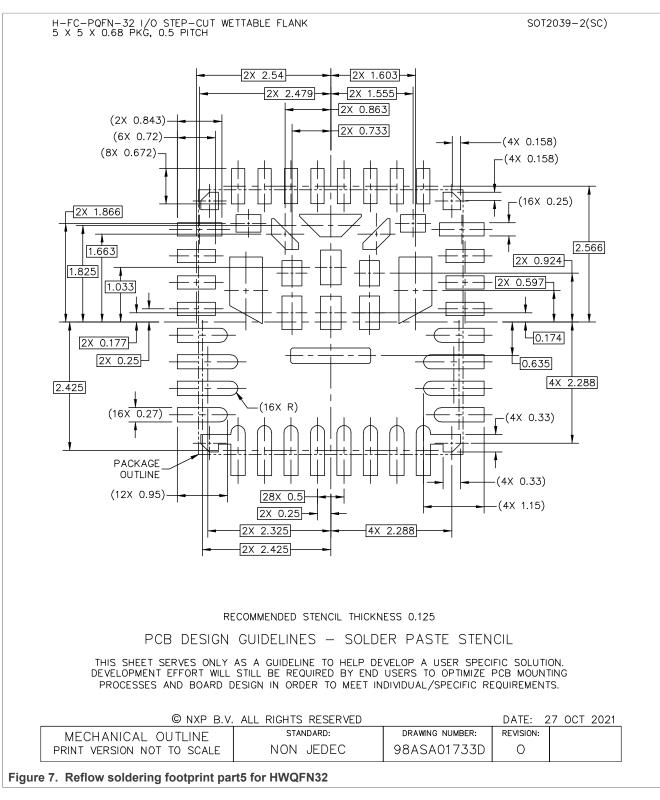
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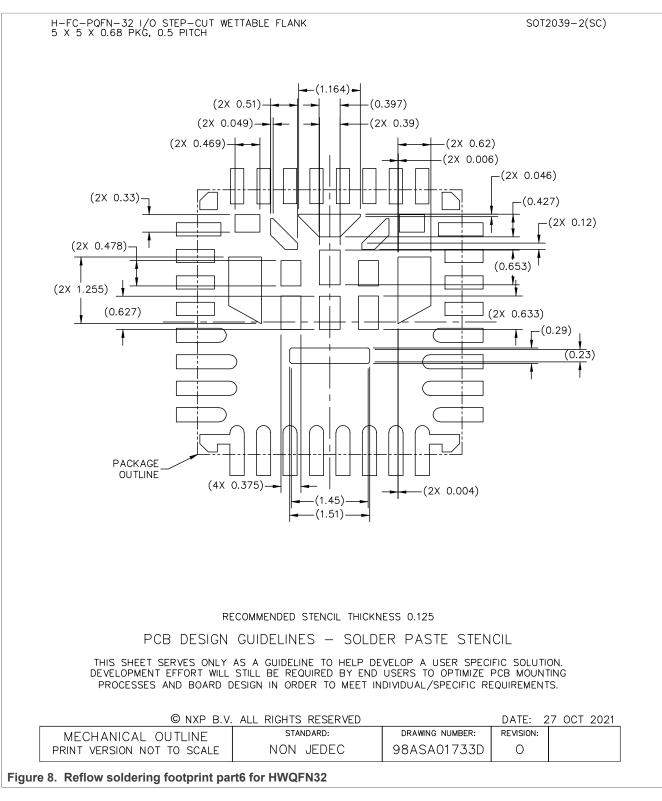
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SOT2039-2(SC)
Package information

9/12

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	H-FC-PQFN-32 I/O STEP-CUT WE 5 X 5 X 0.68 PKG, 0.5 PITCH	TTABLE FLANK		SOT2039-2(SC)
	NOTES:			
	1. ALL DIMENSIONS ARE IN MILLI	METERS.		
	2. DIMENSIONING AND TOLERANC			
	$\sqrt{3}$, PIN 1 FEATURE, SHAPE, SIZE			
	4. COPLANARITY APPLIES TO LE			
	5. MIN. METAL GAP FOR LEAD T		1.2 MM	
	6. ANCHORING PADS.		5.2 WIVI.	
-	© NXP B.V.	ALL RIGHTS RESERVED		DATE: 27 OCT 2021
	MECHANICAL OUTLINE	standard: NON JEDEC	drawing number: 98ASA01733D	REVISION:
	PRINT VERSION NOT TO SCALE		90A3AU1/33D	0
Figure	9. Package outline note HWQFN	32		

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4 Legal information

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Contents

1	Package summary1
2	Package outline2
3	Soldering4
4	Legal information11

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