

SOT2123-1

LFBGA292, low profile, fine-pitch ball grid array package, 292 terminals, 0.8 mm pitch, 17 mm x 17 mm x 1.24 mm body

11 June 2020

Package information

1 Package summary

Terminal position code	B (bottom)
Package type descriptive code	LFBGA292
Package style descriptive code	LFBGA (low profile fine-pitch ball grid array)
Package body material type	P (plastic)
Mounting method type	S (surface mount)
Issue date	06-11-2014
Manufacturer package code	MV-A300950-00

Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	16.9	17	17.1	mm
package width	16.9	17	17.1	mm
seated height	1.17	1.24	1.4	mm
nominal pitch	-	0.8	-	mm
actual quantity of termination	-	292	-	



LFBGA292, low profile, fine-pitch ball grid array package, 292 terminals, 0.8 mm pitch, 17 mm x 17 mm x 1.24 mm body

2 Package outline

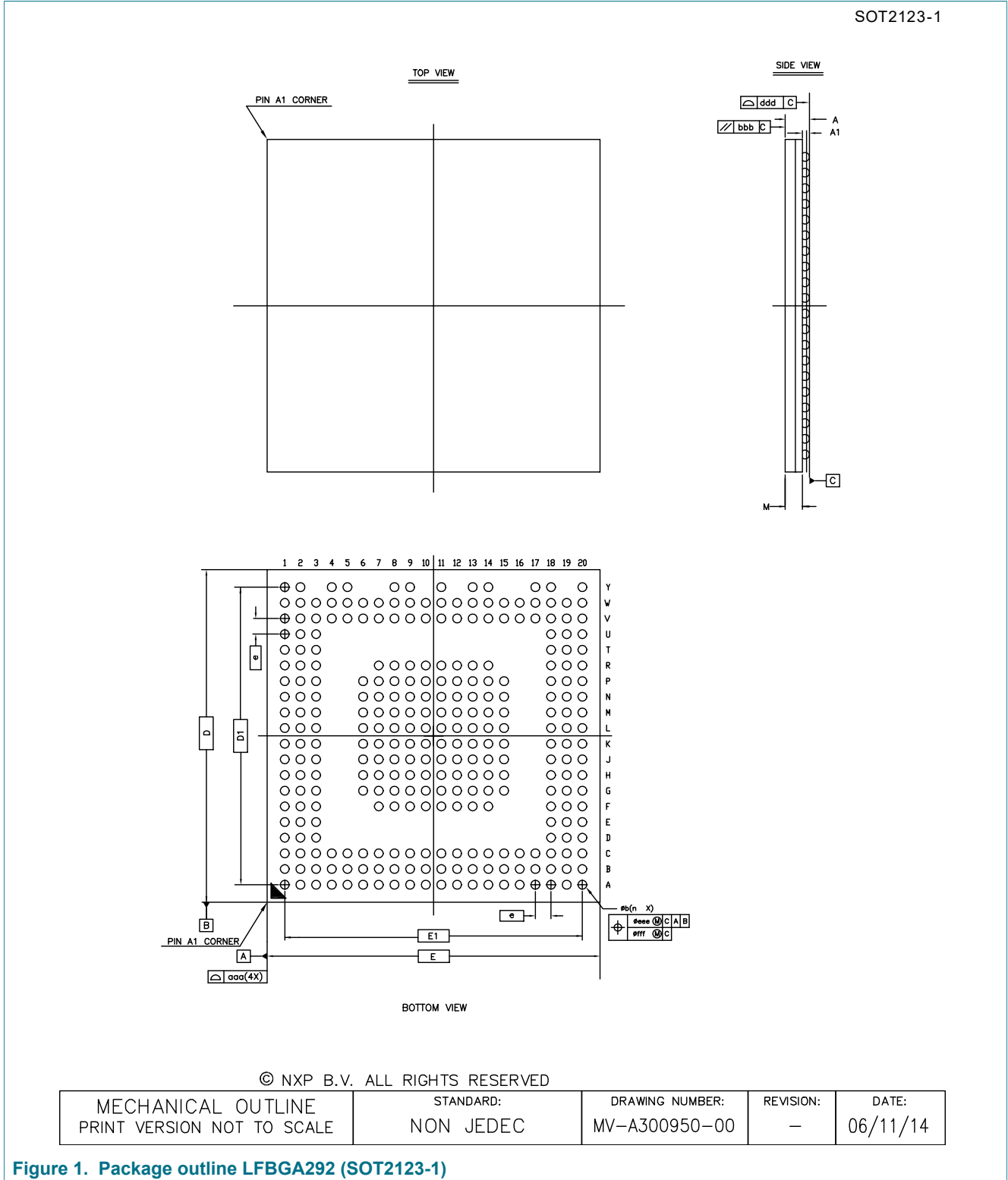


Figure 1. Package outline LFBGA292 (SOT2123-1)

LFBGA292, low profile, fine-pitch ball grid array package, 292 terminals, 0.8 mm pitch, 17 mm x 17 mm x 1.24 mm body

SOT2123-1

	Symbol	Common Dimensions			
		MIN.	NOM.	MAX.	
Body Size:	X	E	16.90	17.00	17.10
	Y	D	16.90	17.00	17.10
Ball Pitch :	e	0.80			
Total Thickness :	A	1.17	1.24	1.40	
Body Thickness :	M	0.84	0.89	0.94	
Stand Off :	A1	0.30	0.35	0.40	
Ball Width :	b	0.40	0.45	0.50	
Package Edge Tolerance :	aaa	0.10			
Mold Flatness :	bbb	0.10			
Coplanarity:	ddd	0.14			
Ball Offset (Package) :	eee	0.15			
Ball Offset (Ball) :	fff	0.08			
Ball Count :	n	292			
Edge Ball Center to Center :	X	E1	15.20		
	Y	D1	15.20		

© NXP B.V. ALL RIGHTS RESERVED

MECHANICAL OUTLINE PRINT VERSION NOT TO SCALE	STANDARD: NON JEDEC	DRAWING NUMBER: MV-A300950-00	REVISION: -	DATE: 06/11/14
--	------------------------	----------------------------------	----------------	-------------------

Figure 2. Package outline note LFBGA292 (SOT2123-1)

3 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Contents

1 Package summary1
2 Package outline2
3 Legal information4