Package information

1. Package summary

Terminal position code Q (quad)

Package type descriptive code PLCC28

Package type industry code PLCC28

Package style descriptive code PLCC (plastic leaded chip carrier)

Package style suffix code NA (not applicable)

Package body material typeP (plastic)IEC package outline code112E08JEDEC package outline codeMS-018JEITA package outline codeEDR-7319

Mounting method type S (surface mount)

Issue date 15-11-2001

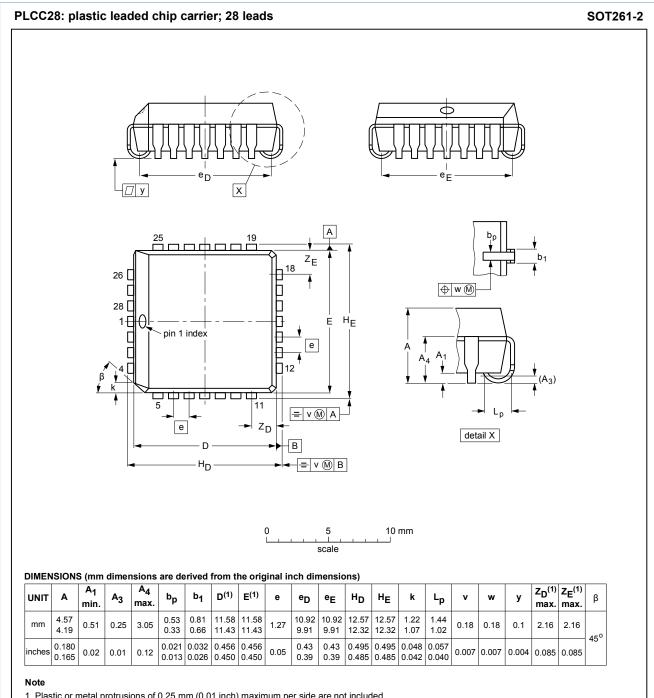
Table 1. Package summary

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	11.43	-	11.48	11.58	mm
E	package width	11.43	-	11.48	11.58	mm
Α	seated height	4.19	-	4.57	4.57	mm
A ₂	package height	[tbd]	-	3.86	[tbd]	mm
n ₂	actual quantity of termination	-	-	28	-	



plastic leaded chip carrier; 28 leads

2. Package outline



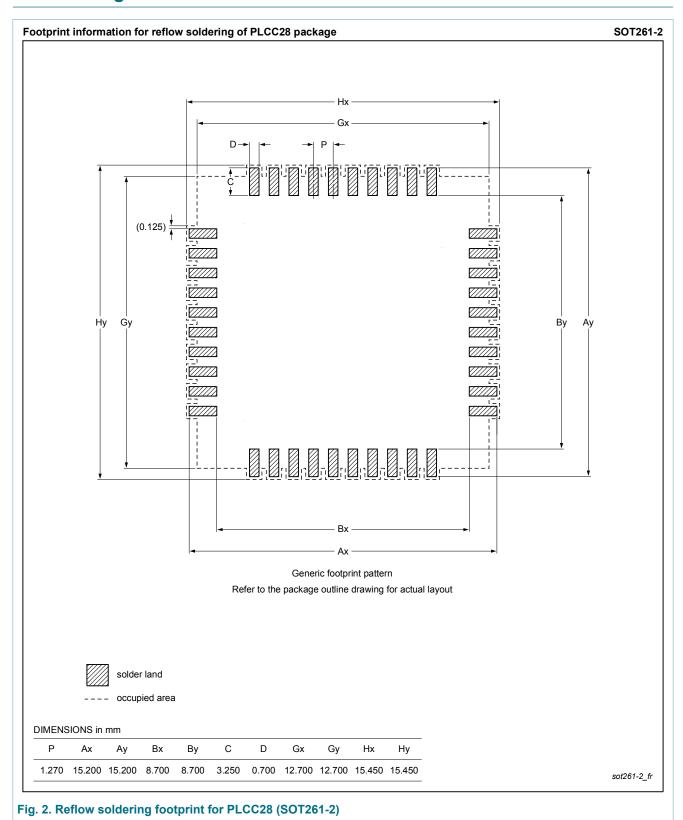
1. Plastic or metal protrusions of 0.25 mm (0.01 inch) maximum per side are not included.

OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT261-2	112E08	MS-018	EDR-7319			99-12-27 01-11-15

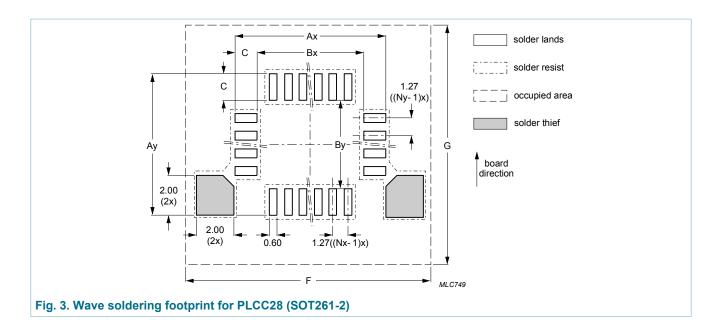
Fig. 1. Package outline PLCC28 (SOT261-2)

plastic leaded chip carrier; 28 leads

3. Soldering



plastic leaded chip carrier; 28 leads



4/6

plastic leaded chip carrier; 28 leads

4. Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

plastic leaded chip carrier; 28 leads

5. Contents

1.	Package summary	1
2.	Package outline	2
3.	Soldering	3
4.	Legal information	5

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 8 February 2016

[©] NXP Semiconductors N.V. 2016. All rights reserved