

DSP56311

User's Manual Addendum

This document provides updates to revision 2 of the *DSP56311 User's Manual (DSP56311UM)*. The changes are organized by the chapters that are affected.

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1 Core

☞ On page 4-32, change the note following **Table 4-11** with the following:

Note: The lowest 10 K of X data RAM and 10 K of Y data RAM are shared memory that can be accessed by the core and the EFCOP but not by the DMA controller.

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