



Errata and Added Information to
**MC68PM302 Integrated Multiprotocol Processor
 with PCMCIA Interface Reference Manual Rev 1**

February 11, 1997

Section 5 - PCMCIA Controller

1. PC_ABUF pin not shown in Figure 5-2
2. p5-10, “but rather drive the ABUF pin” should be changed to “but rather drive the PC_ABUF pin.”
3. Figure 5-5. The “ABUF” label should be changed to PC_ABUF
4. p5-25, ABUF description change “..but instead will drive an address buffer control line..” to “...but instead will drive the PC_ABUF signal with the appropriate timing.”

Section 6 - Signal Description

1. p6-2, Fig 6-1, Replace “PC_ABUF” with “PC_ABUF “
2. p6-3, table 6-1. Replace “PC_ABUF” with “PC_ABUF “
3. p6-6, BUSW signal paragraph. Change “After reset, this pin becomes PC_ABUF.” to “After reset, this pin becomes PC_ABUF described on page 6-30.”
4. p6-30, Add a signal “PC_ABUF “

Description:

This pin is BUSW (described on page 6-6) during reset. After reset, this pin is driven high. If the ABUF bit is set in the PCMR register, this pin will assert during PCMCIA accesses to enable the upper address bits (PC_A23-PC_A12) to be driven directly onto the 68K bus. See page 5-10 for more information.

Section 7 - Electrical Characteristics

1. AC Electrical Spec changes at 3.3V

in Section 7.6.4 in the AC Electrical Specifications Table, only 5.0V values are shown. The only value which changes at 3.V is Spec 47, the Asynchronous Input Setup Time (t_{AS1}). This

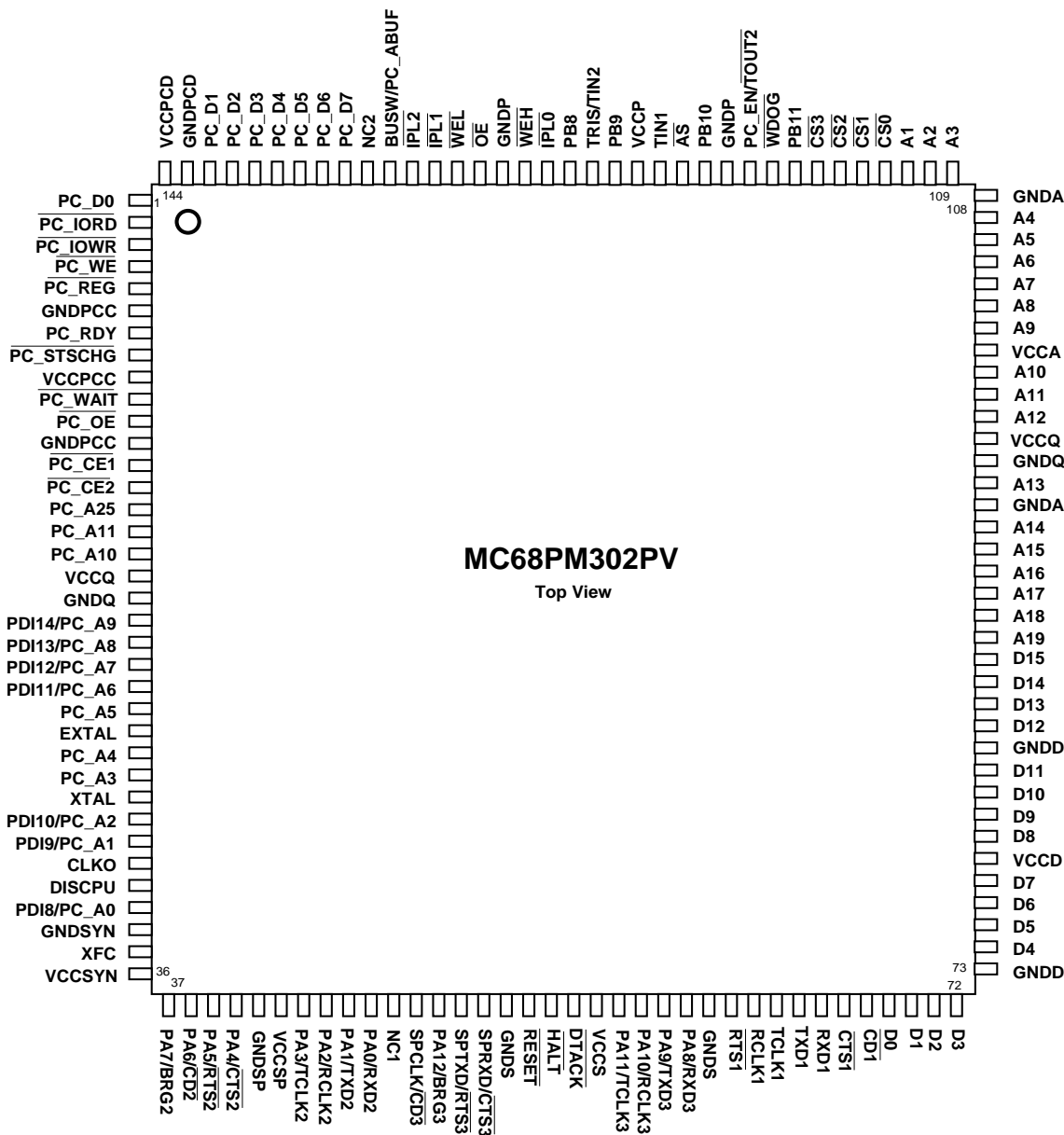
value changes from 10ns to 12ns at 3.3V. All other specifications remain the same at 3.3V.

2. p.7-46. Replace “PC_ABUF” with “PC_ABUF “

Section 8 - Mechanical Data and Ordering Information.

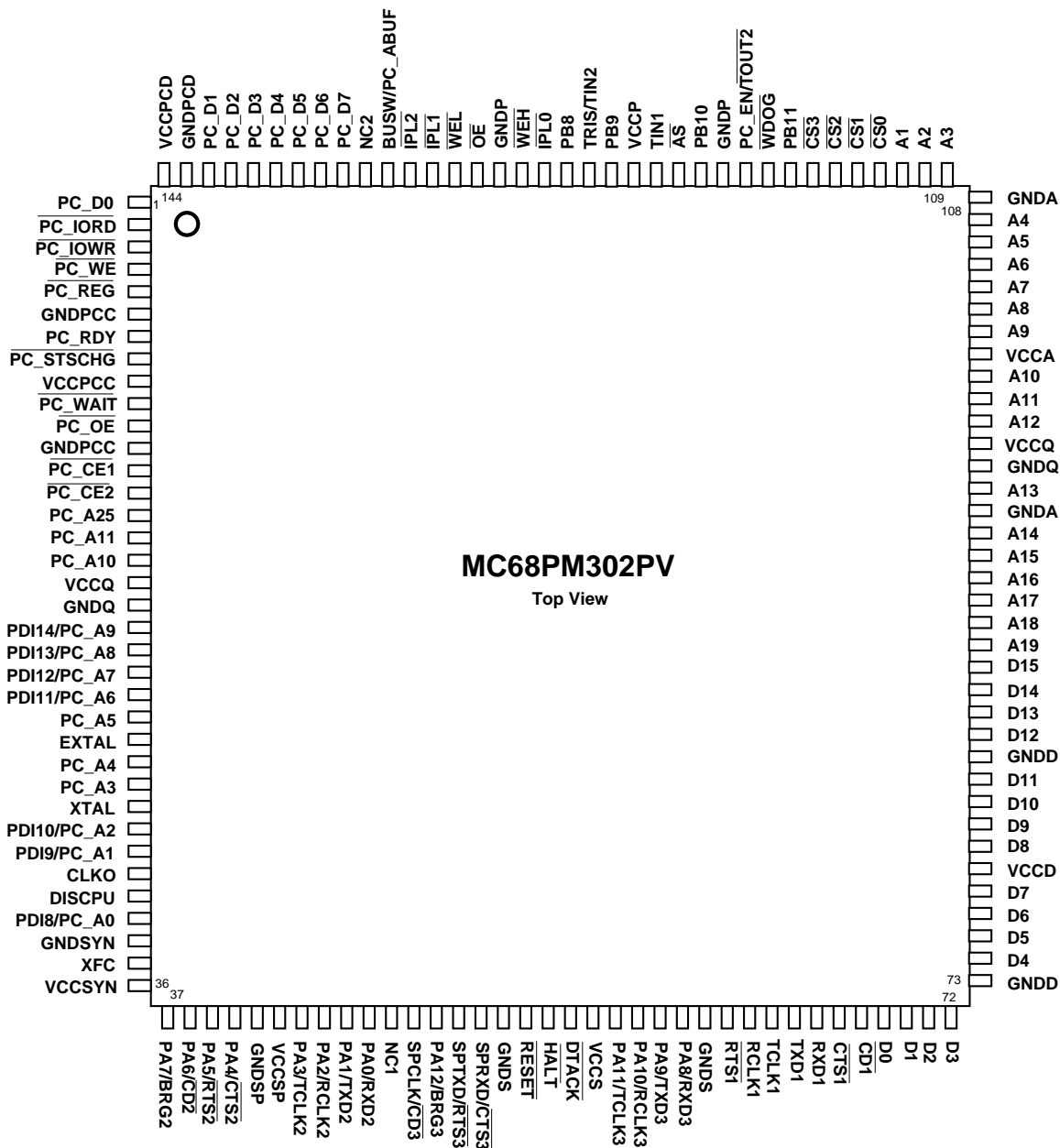
The pin assignment figure for the surface mount (TQFP) package on page 8-2 has two pins mislabeled. The OE and WEL pins are reversed in the figure. The following figure shows the correct pin locations.

This errata to the initial release of the MC68PM302RM/AD Reference Manual provides corrections to the original printed text. This document and other information on this product is maintained on the AESOP BBS, which can be reached at (800) 843-3451 (from the U.S. and Canada) or (512) 891-3650. Configure modem for up to 14.4Kbaud, 8 bits, 1 stop bit, and no parity. Terminal software should support VT100 emulation. Internet access is provided by telneting to pirs.aus.sps.mot.com [129.38.233.1] or through the World Wide Web at http://.pirs.aus.sps.mot.com.



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