## Document Information

<table>
<thead>
<tr>
<th>Information</th>
<th>Content</th>
</tr>
</thead>
<tbody>
<tr>
<td>Keywords</td>
<td>i.MX, Windows 10 IoT</td>
</tr>
<tr>
<td>Abstract</td>
<td>This document contains important information about the package contents, supported features, known issues and limitations in this release.</td>
</tr>
</tbody>
</table>
1 Overview

i.MX Windows 10 IoT 1.4.0 release includes all necessary code, documents, and tools to assist users in building and running Windows 10 IoT on the i.MX boards from scratch.

Pre-built images are also included for a quick trial on the following platforms:

- i.MX 8M Mini EVK
- i.MX 8M Nano EVK
- i.MX 8M Plus EVK
- i.MX 8M Quad EVK
- i.MX 8QuadXPlus MEK (Silicon Revision C0)
- i.MX 93 EVK

1.1 References

For more information about Windows 10 IoT Enterprise, see Microsoft online documentation.

The following quick start guides available on the NXP website contain basic information on the board and setting it up:

- i.MX 8M Quad Evaluation Kit Quick Start Guide
- i.MX 8M Quad Evaluation Kit Quick Start Guide
- i.MX 8M Mini Evaluation Kit Quick Start Guide
- i.MX 8M Nano Evaluation Kit Quick Start Guide
- i.MX 8M Plus Evaluation Kit Quick Start Guide

Documentation is available online at nxp.com

1.2 BSP change history

This chapter lists changes in releases including new features and defect fixes.

- 7/2023 W1.4.1
  - Supported boards:
    - MCIMX8M-EVK Evaluation Kit
    - 8MINILPD4-EVK Evaluation Kit
    - 8MNANOLPD4-EVK Evaluation Kit
    - 8MPLUSLPD4-EVK Evaluation Kit
    - MCIM8QXP-MEK (Silicon Revision C0)
    - MCIMX93-EVK (Silicon Revision A0)
  - New features:
    - General
      - make-winpe-enterprise.cmd: While creating a Windows image, the option for disabling window transparency in GUI is now selected by default. With this change, the script option /
disable_transparency was removed, and a new option /enable_transparency (for enabling the window transparency) is now supported.

- **Wi-Fi driver:**
  - Wi-Fi board based on 88W8997 SoC is supported. Wi-Fi functionality is enabled.

- **Ethernet (imxnetmini.sys) driver:**
  - PHY initialization engine is updated. Read modify write (MII_REG_RMW) operation of PHY registers is added into the set of commands supported in ACPI table _DSD ConfigCmds property.

- **USB in UEFI**
  - USB is enabled in UEFI by default. USB hub, keyboard, mouse, and mass storage devices can be used in UEFI.

- **GPU/display driver:**
  - X86 support: Existing X86 D3D11 apps can now run with GPU acceleration.
  - D3D9 support: A beta D3D9 driver is included. This driver is disabled by default and is intended for customers to test existing WPF apps.
  - Dual-monitor support is added for i.MX 8M Plus (LVDS and HDMI display interfaces).
  - HDMI-audio interface is supported for i.MX 8M Plus.

- **Audio driver:**
  - HDMI-audio driver is added for i.MX 8M Plus (imxaud_hdmi).

- **Fixes:**
  - **Ethernet in UEFI:**
    - MCIMX93-EVK: Ethernet PHY RTL8211FDI-VD-CG initialization sequence in ACPI table is updated.
    - MCIMX93-EVK: Ethernet MAC location in fuses is updated (NET2)

  - **USB in UEFI:**
    - MCIMX8M-EVK: USB Initialization is updated. PHY suspend is disabled (ERR011231).

  - **SPI driver:**
    - The case when a write transfer is sometimes not completed is fixed.
    - Upper limitation ReferenceClockHz/2 for communication speed is added. ReferenceClockHz is specified in ACPI.

  - **GPU/display driver:**
    - 1280x800 resolution with 71 MHz pixel clock is supported in the HDMI and LVDS display clock driver.
    - When IMX-LVDS-HDMI or IMX-MIPI-HDMI converter is used but the monitor is disconnected, the UEFI display driver reports an error and does not initialize the display controller. It is fixed, now the UEFI driver initializes to the default resolution when the monitor is disconnected from the converter.

- **3/2023: W1.4.0**
  - **Supported boards:**
    - MCIMX8M-EVK Evaluation Kit
    - 8MMINILPD4-EVK Evaluation Kit
    - 8MNANOLPD4-EVK Evaluation Kit
    - 8MPLUSLPD4-EVK Evaluation Kit
    - MCIM8QXP-MEK (Silicon Revision C0)
    - MCIMX93-EVK (Silicon Revision A0)

- **New features:**
  - **General**
    - 8MPLUSLPD4-EVK: The size of SDRAM was increased to 6 GB. Note: This change requires windows10.0-kb5019275-arm64_c6c2abc31137d43e762304bd1542ba413d2b8b9e.msu to be installed. The patch is applied in installation script (make-winpe-enterprise.cmd) by default.
    - U-Boot updated to version 2022.04-00346.
    - ATF updated to version v2.6.
- The CPU core count in ACPI (Dsdt-Platform.asl) is controlled by the PcdCoreCount setting now.
- make-winpe-enterprise.cmd: A mechanism for the application of updates to the Windows image is added, see Quick Start Guide for details.
- make-winpe-enterprise.cmd: The /patch_sdport parameter was removed. There is an updated version of the system "sdport.sys" driver in windows10.0-kb5019275-arm64_c6c2abc31137d43e762304bd1542ba413d2b8b9e.msu patch. The patch is applied in the installation script (make-winpe-enterprise.cmd) by default.
- flash-bootloader.cmd: Support for abbreviations of board names is added.

- **Power Management**
  - The Deepest Runtime Idle Platform State (DRIPS) is supported, see `powercfg /sleepstudy`.

- **Audio driver:**
  - A new Wm8962codec driver is added to support the WM8962 device.

- **SPI driver:**
  - A new imxlpspi driver is added to support the LPSPI IP block.

- **Camera driver:**
  - A new MIPI CSI-2 DWC driver is added into BSP to enable video capture on i.MX 93.
  - A new driver for the X-RPI-CAM-MIPI camera board (AP1302 ISP + AR0144 camera) is added to enable video capture on i.MX 93.

- **Malone VPU driver:**

- **GPU/display driver:**
  - Support of rotated modes and display modes smaller than the native mode for HDMI monitor on i.MX 8M Plus.
  - The IMX-DSI-OLED1 panel driver is added to the MIPI-DSI driver for i.MX 8M Nano.
  - The display controller and display interface are disabled when entering power down mode and enabled upon wakeup.
  - UEFI display driver is supported for i.MX 93. Supported displays:
    - MIPI-DSI with Mipi2Hdmi display bridge supporting resolution up to 1080p.
    - LVDS panel BOE ev121wxm-n12 with fixed resolution 1280x800.

- **IMU driver:**
  - A new IMU sensor driver is added. It combines the Linear accelerometer and the Gyroscope sensor.

- **FlexCAN driver:**
  - The FlexCAN device is supported on i.MX 93.

- **UART driver:**
  - LPUART is supported for i.MX 93 in interrupt mode. No flow control pins are available.
  - RTS-CTS flow control is enabled for UART3 on i.MX 8MMini\Nano\Plus.

- **Fixes:**
  - **GPU/display driver:**
    - Performance of NV12 format processing on i.MX 8M Plus and i.MX 8M Nano is improved to make video playback performance better.
– i.MX 8M Plus: 85.5 MHz and 65 MHz pixel clocks added to HDMI to support 1366x768@60 and 1024x768@60 resolutions.
– Audio driver:
  – Fixed “DPC execution time exceeds system limit” assertion when the microphone is used.
– I2C driver:
  – Fixed a memory leak at unloading imxlpi2c driver related to ACPI Dsd Buffer allocation.

- 12/2022: W1.3.0

– Supported boards:
  – MCIMX8M-EVK Evaluation Kit
  – 8MMINILPD4-EVK Evaluation Kit
  – 8MNANOLPD4-EVK Evaluation Kit
  – 8MPLUSLPD4-EVK Evaluation Kit
  – MCIM8QXP-MEK (Silicon Revision C0)

– New features:
  – General
    – UEFI.fit image is merged to U-Boot FIT image on 8M platforms.
    – UEFI binary is compressed before inserted to uefi.fit on 8M platforms.
    – The firmware name is suffixed with "_uuu" if it is compiled with UUU tools.
    – Removed "-t signed". The firmware is always signed when using "-t secured_efi".
    – Custom HAB/AHAB signing keys can be now used by defining the "KEY_ROOT" path.
  – Audio driver:
    – Driver imxaud.sys has been split into imxaud.sys using multi-channel SAI peripheral for i.MX 8M and imxaudsc.sys using single-channel SAI peripheral for i.MX 8QXP.
  – I2C driver:
    – A new imxpli2c driver for the LPI2C peripheral is included. Interrupt and polling modes have been supported.
    – A new iMXLpi2cLib driver for the LPI2C peripheral in UEFI is included.
  – ENET driver:
    – QoS driver is supported on i.MX 8M Plus.
  – I2C sensor drivers:
    – e-Compass FXOS8700 - Accelerometer and Magnetometer is supported on the i.MX 8QXP MEK board.
    – Gyroscope FXAS2100 is supported on the i.MX 8QXP MEK board.
    – Ambient Light Sensor ISL29023 is supported on i.MX 8QXP MEK board.
  – GPU/display driver:
    – GPU driver is added for i.MX 8QXP.
    – HDMI display interface is supported for i.MX 8M Plus.
    – GPU driver version is increased to 1.4.
    – GPU driver support for video processing is added for i.MX 8M Nano/Plus.
    – The source code for the GPU driver kernel part (galcore) is included in the BSP.
  – UART driver:
    – LPUART is supported for i.MX 8QXP in interrupt mode. No flow control is available for LPUART0 and LPUART2. LPUART1 (m2 slot) has RTS, CTS pins wired.
  – Fixes:
    – GPU/display driver:
      – Unsupported resolution and pixel clock (for example 1280x800, pclk=68.9 MHz) display an error message. As a workaround, the algorithm was changed to set the closest possible pixel clock and display a warning message.
– i.MX 8M: Resolution 720p is set by default when a display is not connected or it does not support EDID.

– USB-C:
  – i.MX 8M Plus: USB 3.0 devices were incorrectly detected as USB 2.0. USB Type-C multiplexer/demultiplexer switch "Selection control" pin polarity setting has been added to the ACPI table.

• 10/2022: W1.2.1

– **New features:**
  – Wi-Fi and Bluetooth driver:
    – Wi-Fi AzureWave AW-CB178NF board based on 88W8897 SoC has been supported. Wi-Fi and Bluetooth functionality has been enabled.

• 8/2022: W1.2.0

– **Supported boards:**
  – MCIMX8M-EVK evaluation kit
  – 8MMINILPD4-EVK evaluation kit
  – 8MNANOD4-EVK evaluation kit
  – 8MPLUSLPD4-EVK evaluation kit

– **New features:**
  – **General**
    – Visual Studio 2019 has been supported, but version 2017 is no longer supported.
    – Firmware update: buildme64.sh switch –cap added to build the Firmware capsule. Capsule update working from UEFI shell with capsule stored on SD card: fs0: CapsuleApp.efi fs3: FirmwareCapsuleIMX.cap
    – make-winpe-enterprise.cmd parameters have been updated, see User's Guide for details.
    – i.MX 8M CPU frequency changed from 1 GHz to 1.5 GHz
  – **GPU driver:**
    – GPU driver has been updated to v1.3.
    – GPU driver added for i.MX 8M Plus and i.MX 8M Nano SOCs.
    – GPU driver support for video processing has been added for i.MX 8M.
  – **Camera driver:**
    – OV5640 camera has been supported for all EVK boards.
    – OV10635 camera has been supported for all EVK boards.
    – YUV422 YUY2 and YUV420 NV12 camera color formats have been supported. The i.MX 8M EVK does not support YUV420 NV12 format.
  – **Display driver:**
    – Windows driver with fixed display mode supported for LVDS display interface for i.MX 8M Plus.
    – Windows driver with fixed display mode supported for MIPI-DSI display interface for i.MX 8M Nano.
  – **VPU driver:**
    – VPU decoder has been supported for i.MX 8M Quad, i.MX 8M Mini, and i.MX 8M Plus EVK boards.

– **Fixes:**
  – **WM8960 driver**: I2C is correctly released when the WM8960 device is removed.

• 6/2022: W1.1.0

Public release for i.MX 8M Nano and i.MX 8M Plus platforms.

– **Supported boards:**
  – MCIMX8M-EVK evaluation kit
  – 8MMINILPD4-EVK evaluation kit
  – 8MNANOD4-EVK evaluation kit
New features:
- Camera driver: OV5640 camera in J1502 connector has been supported on i.MX 8M EVK board.
- FlexCAN driver: FlexCAN device has been supported on i.MX 8M Plus EVK by the imxcan.sys driver.
- I2C driver: I2C expander (PCA6416) has been supported in IMXBoardInit module and options SelectCAN1InsteadOfI2C5 and SelectCAN2InsteadOfPDMStream3 allow configuring corresponding selectors on the Base Board.
- GPU driver: GPU driver has been updated to v1.1.
- ENET driver: HW checksum offload has been supported in the NDIS miniport driver.

Fixes:
- Audio driver: A failure during uninstallation in the Device manager has been fixed.
- Display driver: IMX-LVDS-HDMI and IMX-MIPI-HDMI converters: If a native HDMI display resolution exceeds the upper limit, the fixed maximum available resolution is set instead. 1920x1080@60 Hz in case of IMX-MIPI-HDMI and 1280x720@60 Hz in case of IMX-LVDS-HDMI.
- SD driver: Configuration “fixed device” has been changed to “removable device”, which allows you to safely remove the SD card by the “Eject” option.

4/2022: W1.0.0
Public release for i.MX 8M and i.MX 8M Mini platforms.

Supported boards: MCIMX8M-EVK evaluation kit 8MMINILPD4-EVK evaluation kit

New features:
- VPU driver: Supported codecs HEVC, VP9, H.264, VP8. MPEG-2 and MPEG-4 codecs supported on i.MX 8M only.

Fixes:
- UART driver: The UART driver failure during uninstallation in the Device manager has been fixed.
- I2C driver: The issue in iMXI2cRead function (when ReadBufferSize == 1) in UEFI has been fixed.
- buildme 64.sh: The script has been updated. Updates in UEFI source code were included in firmware.bin only if firmware was built with -c parameter (clean build).
- PCIe: PCIE ATU (Address Translation Unit) setup for PCIE BAR memory-mapped registers in UEFI drivers has been fixed. After this fix, the system works as expected in UEFI and relevant Storage drivers appear in Windows.

3/2022: W0.9.1
Public preview release for i.MX 8M platform.

Fixes:
- eMMC driver: eMMC tuning parameters add to the Dsdt-Sdhc.asl.
- BSP deployment: Removed invalid characters from make-winpe-enterprise.cmd.

1/2022: W0.9.0
Private preview release for i.MX 8M platform.

Supported boards: The existing BSP with support for the MCIMX8M-EVK NXP board.

2 BSP supported features

The following table displays the features supported in this BSP release. If no board is explicitly mentioned, the feature is shared across all boards listed in Supported Hardware in the Release contents section; otherwise, the feature is only supported on the boards listed.
### Board name
- **MCIMX8M-EVK**
  - Board revision: 700-38820 REV A
  - Schema revision: SCH-38820 REV A2
  - BSP name: MX8M_EVK
- **8MINILPD4-EVK**
  - Board revision: 700-31407 REV A3 (base board)
  - Schema revision: SCH-31407 REV C4 (base board)
  - BSP name: MX8M_MINI_EVK
  - Board revision: 700-47712REV X2 (cpu board)
  - Schema revision: SCH-47712 REV A2 (cpu board)
- **8MNANOLPD4-EVK**
  - Board revision: 700-31407 REV X5 (base board)
  - Schema revision: SCH-31407 REV C2 (base board)
  - BSP name: MX8M_NANO_EVK
  - Board revision: 700-38823 REV A (cpu board)
  - Schema revision: SCH-38823 REV A2 (cpu board)
- **8MPLUSLPD4-EVK**
  - Board revision: 700-46368 REV A (cpu board)
  - Schema revision: SCH-46368 REV A3 (cpu board)
  - BSP name: MX8M_PLUS_EVK
  - Board revision: 700-46370 REV B (base board)
  - Schema revision: SCH-46370 REV B1 (base board)
- **MCIM8QXP-MEK**
  - Board revision: 700-29918 REV C1 (base board)
  - Schema revision: SCH-29918 REV C1 (base board)
  - BSP name: MX93_11X11_EVK
  - Board revision: 700-29683 REV C2 (cpu board)
  - Schema revision: SCH-29683 REV D5 (cpu board)
  - BSP name: MX8QXP_MEK
  - Board revision: 700-29918 REV C1 (base board)
  - Schema revision: SCH-29918 REV C1 (base board)
- **MCIMX93-EVK**
  - Board revision: 700-51943 REV X2 (cpu board)
  - Schema revision: SCH-51943 REV B (cpu board)
  - BSP name: MX93_11X11_EVK
  - Board revision: 700-51961 REV X7 (base board)
  - Schema revision: SCH-51961 REV B (base board)

### Feature
<table>
<thead>
<tr>
<th>Feature</th>
<th>Supported board</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Boot Image</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>U-Boot</td>
<td>All i.MX</td>
<td>- Clock, Anatop regulator, ENET, UART, MMC/SD, eMMC4.3/4.4/4.5.</td>
</tr>
<tr>
<td>OP-TEE</td>
<td>All i.MX</td>
<td>- OP-TEE OS is required on the boot partition with the TEE file for OP-TEE enablement.</td>
</tr>
<tr>
<td><strong>Machine-specific layer</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Interrupt</td>
<td>All i.MX</td>
<td>- GIC</td>
</tr>
<tr>
<td>Clock</td>
<td>All i.MX</td>
<td>- Controls the system frequency and clock tree distribution.</td>
</tr>
<tr>
<td>Timer</td>
<td>All i.MX</td>
<td>- System timer tick and broadcast timer support.</td>
</tr>
<tr>
<td>GPIO</td>
<td>All i.MX</td>
<td>- GPIO is initialized in earlier phase according to hardware design.</td>
</tr>
<tr>
<td>IOMUX</td>
<td>All i.MX</td>
<td>- Provides the interfaces for I/O configuration.</td>
</tr>
<tr>
<td>SCFW</td>
<td>i.MX 8QXP</td>
<td>- Clock/Power/Security is controlled by the &quot;System Control Firmware&quot;. The &quot;imxscfw&quot; driver controls communication channel with this firmware.</td>
</tr>
</tbody>
</table>
## Feature

<table>
<thead>
<tr>
<th>Feature</th>
<th>Supported board</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>DMA engine</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SDMA</td>
<td>i.MX 8M</td>
<td>- SDMA HAL</td>
</tr>
<tr>
<td>Character device drivers</td>
<td></td>
<td></td>
</tr>
<tr>
<td>UART</td>
<td>i.MX 8M/Mini/Nano/Plus</td>
<td>- DMA (default) and Interrupt mode is configurable in ACPI - UART2 is not available in Windows, used by the Cortex-M4 processor.</td>
</tr>
<tr>
<td>LPUART</td>
<td>i.MX 8QXP/93</td>
<td>- Interrupt mode</td>
</tr>
<tr>
<td>Networking drivers</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ENET</td>
<td>All i.MX</td>
<td>- i.MX 8 supports Atheros AR8031 PHY with 10/100/1000 bps mode</td>
</tr>
<tr>
<td>ENET QOS</td>
<td>i.MX 8M Plus</td>
<td>- ENET QOS is available on i.MX 8M Plus. - RTL8211 PHY is supported.</td>
</tr>
<tr>
<td>PCIe</td>
<td>All i.MX</td>
<td>- i.MX 8 supports M.2 interface.</td>
</tr>
<tr>
<td>Sound drivers</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SAI</td>
<td>All i.MX</td>
<td>- Supports both transmit to and receive from the audio codec.</td>
</tr>
<tr>
<td>WM8524 codec</td>
<td>i.MX 8M/Mini/Nano</td>
<td>- Supports playback</td>
</tr>
<tr>
<td>WM8960 codec</td>
<td>i.MX 8M Plus/8QXP</td>
<td>- Supports playback and record.</td>
</tr>
<tr>
<td>WM8962 codec</td>
<td>i.MX 93</td>
<td>- Supports playback and record.</td>
</tr>
<tr>
<td>HDMI audio</td>
<td>i.MX 8M Plus</td>
<td>- Supports playback. HDMI device must be audio capable.</td>
</tr>
<tr>
<td>USB drivers</td>
<td></td>
<td></td>
</tr>
<tr>
<td>USB Host</td>
<td>All i.MX</td>
<td>- Supports USB-A and USB-C connectors.</td>
</tr>
<tr>
<td>Display/GPU</td>
<td></td>
<td></td>
</tr>
<tr>
<td>HDMI</td>
<td>i.MX 8M/Plus</td>
<td>- Up to 1080p</td>
</tr>
<tr>
<td>LVDS display interface</td>
<td>i.MX 8M Plus/8QXP</td>
<td>i.MX 8M Plus: - Single-channel mode up to 720p - Dual-channel mode up to 1080p (or 1920x1200@60 Hz) - Default mode set to 720p. i.MX 8QXP: - Single-channel mode up to 1080p - Default mode set to 1080p</td>
</tr>
<tr>
<td>IMX-LVDS-HDMI (LVDS to HDMI converter)</td>
<td>i.MX 8M Plus.i.MX 8QXP</td>
<td>- Single-channel mode. Default resolution – see above.</td>
</tr>
<tr>
<td>MIPI-DSI display interface</td>
<td>i.MX 8M Mini/Nano</td>
<td>- i.MX 8M Nano: supports Windows GPU driver up to 1080p (or 1920x1200@60 Hz). Default resolution set to 720p. i.MX 8M Mini: no Windows driver, only firmware support up to 1080p. Default resolution with IMX-MIPI-HDMI converter depends on the monitor native mode – 1080p in most cases.</td>
</tr>
</tbody>
</table>
### IMXWNR

**i.MX Windows 10 IoT Release Notes**

**Feature** | **Supported board** | **Comment**
--- | --- | ---
IMX-MIPI-HDMI (MIPI-DSI to HDMI converter) | i.MX 8M Mini/Nano | - Default resolution – see above.

**GPU** | i.MX 8M/Plus/Nano/8QXP | - HW acceleration for 3D rendering through D3D11 API, therefore accelerates D2D, XAML, UWP, WinUI, Windows desktop UI, and D3D11 apps.

**Multiple displays** | i.MX 8M Plus | - i.MX 8M Plus: HDMI and LVDS.

**Camera**

Camera (SoCs with CSI Bridge) | i.MX 8M/Mini | - Video preview at 720p 30 fps YUYV.

Camera (SoCs with ISI) | i.MX 8M Plus/Nano | - Video preview at 720p 30 fps YUYV and NV12

OV5640 camera | All i.MX | - Second camera configurable in UEFI in <BOARD>.dsc file.

OV10635 camera | All i.MX | - Selectable in UEFI in <BOARD>.dsc file. - i.MX 8M/Mini/Nano/Plus requires an external 12 V PSU and a manual reset every time Windows OS is rebooted. - i.MX 8QXP powers camera via mini SAS connector.

X-RPI-CAM-MIPI camera | i.MX 93 | -

**Video**

VPU full feature | i.MX 8M | - Supported codecs HEVC, VP9, H.264, VP8, MPEG-2, and MPEG-4 codecs.

VPU limited feature | i.MX 8 Mini/Plus | - Supported codecs HEVC, VP9, H.264, VP8.

**General drivers**

uSDHC | All i.MX | - Supports SD, SDXC, eMMC.

I2C | All i.MX | - Supports I2C master mode.

SPI | All i.MX | - Supports SPI master mode.

FlexCAN | i.MX 8M | - FlexCAN low-level driver.

RTC | i.MX 8M/Mini/Nano/Plus | - Basic Set/GetTime UEFI support, reset/battery time retention.

**Power management**

Device power management | All i.MX | - Sample PoFx callbacks are implemented in i2c and pwm drivers. Devices entering D3 (power down) and D0 (active) states, WakeUp sample callbacks in i2c driver.

Processor power management | All i.MX | - PEP (Power Engine Plugin) driver is included in this release. Set usePpm to 1 in imxpep.cpp to enable processor power management, and contact NXP for the latest Pep version.

USB Power delivery | All i.MX | - The initial USB Power delivery contract is negotiated in Uboot. See tcpc_port_config structure.
### Feature | Supported board | Comment
---|---|---
| | | initialization in imx8mp_evk.c, imx8mq_evk.c, imx8mm_evk.c, and imx8mn_evk.c files for actual setting of voltage and current for given board.

**Power button shutdown**

8M/Mini/Nano

- Short pressing of the power (ONOFF) button on the EVK boards initiates the graceful WinOS shutdown.
- Long pressing >5sec of the power (ONOFF) button initiates the immediate emergency power-off

**8M Nano overdrive**

8M Nano

- PcdPmicOverDriveEnable setting in firmware's MX8M_NANO_EVK.dsc increases Nano CPU 1.2GHz to 1.4GHz, GPU 400 MHz to 600 MHz.
- Using Nano overdrive requires passive CPU cooler or thermal management driver

### 3 Known issues/limitations

Read through all hardware-related reference material and ensure that you have made all the necessary hardware modifications before using the software.

<table>
<thead>
<tr>
<th>Limitation/Workaround</th>
<th>SoC</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Boot</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> Supported boot media are SD and eMMC only. <strong>Workaround:</strong> No workaround.</td>
<td>All</td>
</tr>
<tr>
<td><strong>UEFI</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> Non-volatile (NV) non-authorized UEFI variables are not persistent after reboot. <strong>Workaround:</strong> No workaround.</td>
<td>All</td>
</tr>
<tr>
<td><strong>Camera</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> Only i.MX 8MQ EVK SCH-29615 rev. B4 is supported. Earlier board revisions use different I2C for the camera. <strong>Workaround:</strong> For i.MX 8MQ EVK SCH-29615 rev. B3, choose I2C1 camera device instead of I2C2 in mu_platform_nxp/NXP/&lt;BOARD&gt;/AcpiTables/Dsdt Camera_&lt;CONFIGURATION&gt;.asl.</td>
<td>i.MX 8M</td>
</tr>
<tr>
<td><strong>Limitation:</strong> OV10635 requires an external 12V power source and cannot be reconfigured by software without a power cycle. <strong>Workaround:</strong> After powering off the board, unplug the power supply from the camera and wait a few seconds before powering the camera and EVK board again.</td>
<td>i.MX 8M, i.MX 8M Mini, i.MX 8M Nano, i.MX 8M Plus</td>
</tr>
<tr>
<td><strong>Display/GPU</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> Some monitors/displays may fail reading EDID using on board HDMI interface because of incompatible voltage level shifter on 8MPLUSLPD4-EVK revision A. <strong>Workaround:</strong> Use 8MPLUSLPD4-EVK revision B1 or newer.</td>
<td>i.MX 8M Plus</td>
</tr>
<tr>
<td>Limitation/Workaround</td>
<td>SoC</td>
</tr>
<tr>
<td>-------------------------------------------------------------------------------------</td>
<td>----------------------------------------------------------------------</td>
</tr>
<tr>
<td><strong>Limitation:</strong> GPU driver support of D3D9 is disabled by default. <strong>Workaround:</strong> GPU D3D9 driver can be enabled by updating registers value: reg add &quot;HKEY_LOCAL_MACHINE\SYSTEM\CurrentControlSet\Control\Class{4d36e968-e325-11ce-bfc1-08002be10318}\0000&quot; /v UserModeDriverName /t REG_MULTI_SZ /d umd9.dll\0umd11.dll\0umd12.dll /r; reg add &quot;HKEY_LOCAL_MACHINE\SYSTEM\CurrentControlSet\Control\Class{4d36e968-e325-11ce-bfc1-08002be10318}\0000&quot; /v UserModeDriverNameWow /t REG_MULTI_SZ /d umd9.dll\0umd11.dll\0umd12.dll /r.</td>
<td>i.MX 8M i.MX 8M Plus i.MX 8M Nano i.MX 8QXP</td>
</tr>
<tr>
<td><strong>Limitation:</strong> Using Remote Desktop to connect to the device results in black screen. <strong>Workaround:</strong> Apply the following register value to run the older XDDM driver for Remote Desktop: reg add &quot;HKLM\System\CurrentControlSet\Control\Terminal Server\WinStations&quot; /v &quot;LoadWddmIDDDriver&quot; /t REG_DWORD /d 0x0 /f</td>
<td>i.MX 8M</td>
</tr>
<tr>
<td><strong>GPIO</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> EXP_IO pins on EXP CN connector cannot be used as GPIOs because they are connected to PCA6416 I/O expander. <strong>Workaround:</strong> Use the GPIO pin on the EXP CN connector connected directly to the SoC’s pin with GPIO functionality instead of connecting to the PCA6416 I/O expander.</td>
<td>i.MX 8M Mini i.MX 8M Nano i.MX 8M Plus</td>
</tr>
<tr>
<td><strong>SDHC</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> The imxusdhc.sys in crash dump mode does not read HW-specific settings from ACPI so these values are hardcoded in the SdhcSlotInitialize() method. <strong>Workaround:</strong> Keep these values synchronized with values in Dsdt-Sdhc.asl ACPI table.</td>
<td>All</td>
</tr>
<tr>
<td><strong>Limitation:</strong> SDCARD insertion/removal can cause a bug check if the delay between insertion/removal is too short. <strong>Workaround:</strong> Wait a few seconds before insertion/removal.</td>
<td>All</td>
</tr>
<tr>
<td><strong>UART/LPUART</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> The RTS-CTS hardware flow control is not available for all UARTs. <strong>Workaround:</strong> Use UART with routed RTS-CTS pins.</td>
<td>All</td>
</tr>
<tr>
<td><strong>USB</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Limitation:</strong> The size of SDRAM was increased to 6 GB. This change requires windows10-0-kb5019275-arm64_c6c2abc31137d43e762304bd1542ba413d2b8b9e.msu to be installed. <strong>Workaround:</strong> The patch is applied in installation script (make-winpe-enterprise.cmd) by default.</td>
<td>i.MX 8M Plus</td>
</tr>
<tr>
<td><strong>PCIe</strong></td>
<td></td>
</tr>
<tr>
<td>Limitation/Workaround</td>
<td>SoC</td>
</tr>
<tr>
<td>-----------------------</td>
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</tr>
<tr>
<td><strong>Limitation:</strong> PCIe DMA supports 32-bit addressing only. Connected PCIe devices using memory &gt; 4GB may work incorrectly. <strong>Workaround:</strong> Allocate memory for PCIe devices below 4G or copy data to lower address space in the device driver.</td>
<td>i.MX 8M i.MX 8M Mini i.MX 8M Plus i.MX 8QXP</td>
</tr>
</tbody>
</table>

**VPU**

**Limitation:** All MScale SoC’s (hantro VPU) decoding attempts could fail, due to runtime requirements of physical contiguous memory. This memory may not be available because of RAM fragmentation. **Workaround:** Reboot the board and play video on the freshly booted system.

### 4 Revision history

<table>
<thead>
<tr>
<th>Revision number</th>
<th>Date</th>
<th>Substantive changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>W0.9.0</td>
<td>1/2022</td>
<td>Private preview release for i.MX 8M platform.</td>
</tr>
<tr>
<td>W0.9.1</td>
<td>3/2022</td>
<td>Public preview release for i.MX 8M platform.</td>
</tr>
<tr>
<td>W1.0.0</td>
<td>4/2022</td>
<td>Public release for i.MX 8M and i.MX 8M Mini platforms.</td>
</tr>
<tr>
<td>W1.1.0</td>
<td>6/2022</td>
<td>Public release for i.MX 8M Nano and i.MX 8M Plus platforms.</td>
</tr>
<tr>
<td>W1.2.0</td>
<td>9/2022</td>
<td>Sections 1.2 and 3 are updated.</td>
</tr>
<tr>
<td>W1.2.1</td>
<td>10/2022</td>
<td>Section 1.2 is updated.</td>
</tr>
<tr>
<td>W1.3.0</td>
<td>12/2022</td>
<td>i.MX 8QuadXPlus MEK board support added.</td>
</tr>
<tr>
<td>W1.4.0</td>
<td>3/2023</td>
<td>i.MX 93 EVK board support added.</td>
</tr>
<tr>
<td>W1.4.1</td>
<td>7/2023</td>
<td>Section 1.2 is updated.</td>
</tr>
</tbody>
</table>
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## Contents

1  **Overview** .............................................................. 2  
2  **BSP supported features** ...................................... 7  
3  **Known issues/limitations** ........................................ 11  
4  **Revision history** .................................................. 13  
5  **Legal information** .................................................. 14  

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