

RN00425

PN76AC firmware release notes v03.01.01

Rev. 1.0 — 29 June 2026

Release notes

Document information

Information	Content
Keywords	PN76AC, FW, NFC
Abstract	PN76AC release notes



1 Introduction

This document provides information on the content of the firmware v03.01.01 delivery for PN76AC. It includes the following sections:

- Firmware release content list
- Firmware revision history

Note: For more information, refer to the PN76AC product page on nxp.com [ref.\[1\]](#).

2 Scope of the release

This release package is the initial production release of the PN76AC firmware v03.01.01 in secure binary format.

The release is tested with PN76AC customer evaluation board consisting of a PN76AC IC with a 45 mm x 45 mm RF antenna, an MCXW72 as the host controller, and NCI middleware release v01.01.00.

3 Test environment used for the release

3.1 Default SW/HW configuration

The table below details the default configuration about some important parameters:

Parameters	Values
Board used	OM76ACB customer evaluation board.
Antenna	45 x 45 mm
Clock configuration	XTAL
Firmware version	v03.01.01
Host controller	FRDM-MCXW72
Middleware Package	NciLib v01.01.00

4 Features released

Table 1. RF features list

Mode	Protocol	Techno	NFCEE	Other	Completeness
R/W – NFC Forum	ISO-DEP	NFC-A	DH	Frame RF IF 106 kb/s	Functional verified
				ISO-DEP RF IF 106 kb/s	Functional verified
				ISO-DEP RF IF 212, 424, 848 kb/s	Functional verified
	FeliCa	NFC-B	DH	Frame RF IF 106 kb/s	Functional verified
				ISO-DEP RF IF 106 kb/s	Functional verified
				ISO-DEP RF IF 212, 424, 848 kb/s	Functional verified
Card Emulation	ISO-DEP	NFC-A	HCE	ISO-DEP RF IF 106 kb/s	Functional verified
				ISO-DEP RF IF 212, 424, 848 kb/s	Functional verified

Table 2. Other FW features released

Sl.no	Feature	Completeness
1	Secure FW download	Functional verified
2	ULPCD Feature	Functional verified
3	Standby mode	Functional verified
4	PRBS	Functional verified
6	Dynamic Power Control (DPC)	Functional verified
7	Automatic Waveshape Control	Functional verified
8	Clock management (PLL / XTAL)	Functional verified

Table 3. Certifications

Sl.no	Feature	Completeness
1	NFC Forum CR15 - Digital Compliance (Internal)	To be verified
2	NFC Forum CR15 - Analog Compliance (Internal)	To be verified

5 Firmware release content

The release contains a firmware subpackage intended for use with the PN76AC IC.

The zip file of this release contains:

- Firmware (code and data) in secure format *PN76AC.esfwu* to perform firmware update on the PN76AC IC (part of the customer evaluation board)
- C file *phDnldNfc_UpdateSeq.c* containing the secure firmware image sequence of the above binary in C programming format
- SCR and license file

Table 4. FW version corresponding to this release note

FW version	Antenna type	IC revision	ROM version	Flash Version
v03.01.01	45 mm x 45 mm	PN76AC 0x53	03	01.01

6 Firmware release history

Table 5. Firmware release history

Firmware Version	Release purpose
v03.01.01	Initial production release
v03.01.00	RFP release
v03.00.03	EAR release

6.1 v03.01.01

Table 6. Initial production release

Sl No.	Description
1	Extend CORE_RESET_NTF with more detailed information about the reset and boot reason.

Table 6. Initial production release...continued

SI No.	Description
2	Add ULPCD wake-up notification. By enabling this notification the host is immediately notified about a wake-up from ULPCD, before the actual polling loop starts.
3	Resolve CTS capturing.

6.2 v03.01.00

Table 7. RFP release

SI No.	Description
1	Solved GPIO3 handling of stopping RF-polling cycle in active mode.
2	Solved wrong handling of wait time before enter standby.
3	Solved case where PN76AC goes back to standby if the standby enabled bit is cleared while GPIO3 is high.
4	Solved ULPCD check period incorrectly handled if set very short.
5	Solved incorrectly sent <code>RF_INTF_ACTIVATED_NTF</code> for <code>RF_DISCOVER_SELECT_CMD</code> .
6	New NFC Cockpit related ULPCD API added.
7	Improve active power consumption in case ULPCD is enabled. CLIF initialization is moved after receiving <code>RF_DISCOVER_CMD</code> .

6.3 v03.00.03

EAR release

7 Precautionary notes

Table 8. Precautions and recommendations

Limitation	Recommendation
TX driver may be damaged due to overcurrent.	Do not disable DPC on PN76AC.

8 Abbreviations

Table 9. Abbreviations

Abbreviation	Description
DH	device host
FW	firmware
HIF	Host Interface
MW	middleware
NCI	NFC controller interface
NFC	near-field communication
NFCC	NFC controller
NFCEE	NFC execution environment
LPCD	low power card detection

Table 9. Abbreviations...continued

Abbreviation	Description
ULPCD	ultra low power card detection
PRBS	pseudo random binary sequence
RF	radio frequency
R/W	NFC reader/writer mode

9 References

- [1] Web page – PN76AC – Plug-and-Play NFC Controller Connecting to NXP's MCU and MPU Portfolio ([link](#))

10 Revision history

Table 10. Revision history

Document ID	Release date	Description
RN00425 v.1.0	29 June 2026	Initial version

Legal information

Definitions

Draft — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <https://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Suitability for use in non-automotive qualified products — Unless this document expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

HTML publications — An HTML version, if available, of this document is provided as a courtesy. Definitive information is contained in the applicable document in PDF format. If there is a discrepancy between the HTML document and the PDF document, the PDF document has priority.

Translations — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

NXP B.V. — NXP B.V. is not an operating company and it does not distribute or sell products.

Licenses

Purchase of NXP ICs with NFC technology — Purchase of an NXP Semiconductors IC that complies with one of the Near Field Communication (NFC) standards ISO/IEC 18092 and ISO/IEC 21481 does not convey an implied license under any patent right infringed by implementation of any of those standards. Purchase of NXP Semiconductors IC does not include a license to any NXP patent (or other IP right) covering combinations of those products with other products, whether hardware or software.

Trademarks

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners.

NXP — wordmark and logo are trademarks of NXP B.V.

Tables

Tab. 1.	RF features list	2	Tab. 6.	Initial production release	3
Tab. 2.	Other FW features released	3	Tab. 7.	RFP release	4
Tab. 3.	Certifications	3	Tab. 8.	Precautions and recommendations	4
Tab. 4.	FW version corresponding to this release note	3	Tab. 9.	Abbreviations	4
Tab. 5.	Firmware release history	3	Tab. 10.	Revision history	5

Contents

1 Introduction 2

2 Scope of the release 2

3 Test environment used for the release 2

3.1 Default SW/HW configuration 2

4 Features released 2

5 Firmware release content 3

6 Firmware release history 3

6.1 v03.01.01 3

6.2 v03.01.00 4

6.3 v03.00.03 4

7 Precautionary notes 4

8 Abbreviations 4

9 References 5

10 Revision history 5

Legal information 6

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© 2026 NXP B.V.

All rights reserved.

For more information, please visit: <https://www.nxp.com>

[Document feedback](#)

Date of release: 29 June 2026
 Document identifier: RN00425