

PCM Codec-Filter Features

The chart below gives a feature-by-feature comparison of the PCM Codec-Filters.

	MC145502/3/5/6	MC145554/57 MC145564/67	MC14LC5480 MC145484	MC145481	MC145482	MC145483
PCM Data Coding Format	Mu-Law or A-Law Pin Selectable	Mu-Law (MC145554/64) A-Law (MC145557/67)	Mu-Law or A-Law Pin Selectable	Mu-Law or A-Law Pin Selectable	13-Bit Linear, 2s Complement	13-Bit Linear, 2s Complement
Power Supply	+/-5 V or 10 V to 12 V	+/-5 V	5 V	3 V	5 V	3 V
Typical Power Dissipation	40 mW	40 mW	15 mW	8 mW	25 mW	8 mW
PCM Digital Interface Clock Format	Long Frame Sync	Long Frame, Short Frame	Long Frame, Short Frame, IDL, GCI	Long Frame, Short Frame, IDL, GCI	Long Frame, Short Frame	Long Frame, Short Frame
Master Clock Freq. - kHz	128, 1536, 1544, 2048, 2560	1536, 1544, 2048	256, 512, 1536, 1544, 2048, 2560, 4096	256, 512, 1536, 1544, 2048, 2560, 4096	256, 512, 1536, 1544, 2048, 2560, 4096	256, 512, 1536, 1544, 2048, 2560, 4096
Analog Ground Voltage	0 V or VDD/2	0 V	2.4 V (MC14LC5480) VDD/2 (MC145484)	VDD/2	VDD/2	VDD/2
Idle Channel Noise Max Transmit/Receive	15/9 dBmCO -69/-78 dBmOp	15/7 dBmCO -70/-83 dBmOp	17/11 dBmCO -69/-78 dBmOp	20/14 dBmCO -68/-76 dBmOp	17/11 dBmCO -69/-78 dBmOp	20/14 dBmCO -68-76 dBmOp
Output Pwr Drivers -- Load and Vpk	1200 Ohms 6.3 Vpk	300 Ohms 5 Vpk	300 Ohms 3.15 Vpk	TBD	300 Ohms, 3.15 Vpk	TBD
Packages	PDIP, SOIC, PLCC	PDIP, SOIC	SOIC, SSOP, TSSOP, PDIP (MC14LC5480 only)	SOIC, SSOP, TSSOP	SOIC, SSOP, TSSOP	SOIC, TSSOP

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