

Keeping Cars Secure: Solutions for Implementing Security in the Era of the Connected Vehicle

APF-AUT-T0112

Jürgen Frank
Automotive Sr. Systems Engineer



September 2013

Freescale, the Freescale logo, AllWin, C-5, CodeT-EST, CodeWarrior, ColdFire, ColdFire+, C-Wire, the Energy Efficient Solutions logo, iM801, iM801GT, P50, PowerQUICC, Processor Expert, QorIQ, QorIQ+, SafeAssure, the SafeAssure logo, StarCore, Symphony and VortiQa are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. AirStar, BeeBit, BeeStack, ClearNet, Flexio, LayerScope, MagiK, M6C, Platform in a Package, QorIQ Converge, QUICC Engine, ReadyPlay, SMARTMOS, Tower, TurboLink, Vybrid and Vybrid are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © 2013 Freescale Semiconductor, Inc.



- **Introduction – Security, why?**
 - Use-case overview
 - Attack examples
- **In a nutshell: Security Algorithms**
- **Automotive Standards**
- **Freescale Qorivva Security Modules**
 - CSE & CSE2
 - HSM
- **Use-Cases**
 - Secure Boot

Security Use Cases: In Vehicle Security

- **Immobilizer / Component Protection**
 - Detection of replacement or modification of components or ECUs
- **Mileage Protection**
 - Disabling mileage manipulation
- **Secure Boot and Chain of Trust**
 - Verification of authenticity and integrity of vehicle software
- **Secure Communication**
 - Protection of the vehicle network from unauthorized access

Security Use Cases: Connected Vehicle Security

- **Telematics and GPS**
 - Authentication of map data
 - Enforcing permission limitations between open and safety critical domains
- **Android application download**
 - Ensuring permissions of apps are not exceeded
- **DRM for content download/streaming**
 - Authentication that content is licensed
- **Remote ECU firmware update**
 - Enforcing permission limitations between open and safety critical domains

Automotive Security Threats I

Research Study 1: Experimental Security Analysis of a Modern Automobile,

Karl Koscher, Alexei Czeskis, Franziska Roesner, Shwetak Patel, Tadayoshi Kohno, Stephen Checkoway, Damon McCoy, Brian Kantor, Danny Anderson, Hovav Shacham, Stefan Savage

IEEE Symposium on Security and Privacy, Oakland, CA, May 16–19, 2010

Center For Automotive Embedded System Security

[<http://www.autosec.org/pubs/cars-oakland2010.pdf>]



Research Study 2: **Security and Privacy Vulnerabilities of In-Car Wireless Networks:**

A Tire Pressure Monitoring System Case Study

Ishtiaq Rouf, Rob Miller, Hossen Mustafa, Travis Taylor, Sangho Oh, Wenyan Xu, Marco Gruteser, Wade Trappe, and Ivan Seskar

in Proceedings of the 19th **USENIX Security** Symposium, Washington DC, Aug. 11-13, 2010

[<http://www.cse.sc.edu/~wyxu/papers/TPMS2010.pdf>]



Mileage manipulation (in Germany)

- 2 million manipulated cars per year
- Average increases in value per car ~3000€
- Total loss 6 billion euro



Automotive Security Threats II

- Transportation Department Warns Against Counterfeit Air Bags
- October 10, 2012, NHTSA estimates it affects 0.1% of US Fleet, availability of such replacement systems traces back to 2003 (!)



Video created & owned by National Highway Traffic Safety Administration
(NHTSA) [<http://www.nhtsa.gov/>]

- DARPA Funded Researchers Take Control Of two Cars
- Dr. Charlie Miller and Chris Valasek.
- July, 2013, Defcon: Adventures in Automotive Networks and Control Units [http://illmatics.com/car_hacking.pdf]



In a Nutshell: Security Algorithms



FreeScale, the Freescale logo, i.MX, eZ, C-SPYTEST, CodeWarrior, ColdFire, ColdFire+, C-WARE, the Energy Efficient Solution logo, iNtelli, iM801, iM802, iM803, iM804, iM805, iM806, iM807, iM808, iM809, iM810, iM811, iM812, iM813, iM814, iM815, iM816, iM817, iM818, iM819, iM820, iM821, iM822, iM823, iM824, iM825, iM826, iM827, iM828, iM829, iM830, iM831, iM832, iM833, iM834, iM835, iM836, iM837, iM838, iM839, iM840, iM841, iM842, iM843, iM844, iM845, iM846, iM847, iM848, iM849, iM850, iM851, iM852, iM853, iM854, iM855, iM856, iM857, iM858, iM859, iM860, iM861, iM862, iM863, iM864, iM865, iM866, iM867, iM868, iM869, iM870, iM871, iM872, iM873, iM874, iM875, iM876, iM877, iM878, iM879, iM880, iM881, iM882, iM883, iM884, iM885, iM886, iM887, iM888, iM889, iM890, iM891, iM892, iM893, iM894, iM895, iM896, iM897, iM898, iM899, iM900, iM901, iM902, iM903, iM904, iM905, iM906, iM907, iM908, iM909, iM910, iM911, iM912, iM913, iM914, iM915, iM916, iM917, iM918, iM919, iM920, iM921, iM922, iM923, iM924, iM925, iM926, iM927, iM928, iM929, iM930, iM931, iM932, iM933, iM934, iM935, iM936, iM937, iM938, iM939, iM940, iM941, iM942, iM943, iM944, iM945, iM946, iM947, iM948, iM949, iM950, iM951, iM952, iM953, iM954, iM955, iM956, iM957, iM958, iM959, iM960, iM961, iM962, iM963, iM964, iM965, iM966, iM967, iM968, iM969, iM970, iM971, iM972, iM973, iM974, iM975, iM976, iM977, iM978, iM979, iM980, iM981, iM982, iM983, iM984, iM985, iM986, iM987, iM988, iM989, iM990, iM991, iM992, iM993, iM994, iM995, iM996, iM997, iM998, iM999, iM1000, iM1001, iM1002, iM1003, iM1004, iM1005, iM1006, iM1007, iM1008, iM1009, iM1010, iM1011, iM1012, iM1013, iM1014, iM1015, iM1016, iM1017, iM1018, iM1019, iM1020, iM1021, iM1022, iM1023, iM1024, iM1025, iM1026, iM1027, iM1028, iM1029, iM1030, iM1031, iM1032, iM1033, iM1034, iM1035, iM1036, iM1037, iM1038, iM1039, iM1040, iM1041, iM1042, iM1043, iM1044, iM1045, iM1046, iM1047, iM1048, iM1049, iM1050, iM1051, iM1052, iM1053, iM1054, iM1055, iM1056, iM1057, iM1058, iM1059, iM1060, iM1061, iM1062, iM1063, iM1064, iM1065, iM1066, iM1067, iM1068, iM1069, iM1070, iM1071, iM1072, iM1073, iM1074, iM1075, iM1076, iM1077, iM1078, iM1079, iM1080, iM1081, iM1082, iM1083, iM1084, iM1085, iM1086, iM1087, iM1088, iM1089, iM1090, iM1091, iM1092, iM1093, iM1094, iM1095, iM1096, iM1097, iM1098, iM1099, iM1100, iM1101, iM1102, iM1103, iM1104, iM1105, iM1106, iM1107, iM1108, iM1109, iM1110, iM1111, iM1112, iM1113, iM1114, iM1115, iM1116, iM1117, iM1118, iM1119, iM1120, iM1121, iM1122, iM1123, iM1124, iM1125, iM1126, iM1127, iM1128, iM1129, iM1130, iM1131, iM1132, iM1133, iM1134, iM1135, iM1136, iM1137, iM1138, iM1139, iM1140, iM1141, iM1142, iM1143, iM1144, iM1145, iM1146, iM1147, iM1148, iM1149, iM1150, iM1151, iM1152, iM1153, iM1154, iM1155, iM1156, iM1157, iM1158, iM1159, iM1160, iM1161, iM1162, iM1163, iM1164, iM1165, iM1166, iM1167, iM1168, iM1169, iM1170, iM1171, iM1172, iM1173, iM1174, iM1175, iM1176, iM1177, iM1178, iM1179, iM1180, iM1181, iM1182, iM1183, iM1184, iM1185, iM1186, iM1187, iM1188, iM1189, iM1190, iM1191, iM1192, iM1193, iM1194, iM1195, iM1196, iM1197, iM1198, iM1199, iM1200, iM1201, iM1202, iM1203, iM1204, iM1205, iM1206, iM1207, iM1208, iM1209, iM1210, iM1211, iM1212, iM1213, iM1214, iM1215, iM1216, iM1217, iM1218, iM1219, iM1220, iM1221, iM1222, iM1223, iM1224, iM1225, iM1226, iM1227, iM1228, iM1229, iM1230, iM1231, iM1232, iM1233, iM1234, iM1235, iM1236, iM1237, iM1238, iM1239, iM1240, iM1241, iM1242, iM1243, iM1244, iM1245, iM1246, iM1247, iM1248, iM1249, iM1250, iM1251, iM1252, iM1253, iM1254, iM1255, iM1256, iM1257, iM1258, iM1259, iM1260, iM1261, iM1262, iM1263, iM1264, iM1265, iM1266, iM1267, iM1268, iM1269, iM1270, iM1271, iM1272, iM1273, iM1274, iM1275, iM1276, iM1277, iM1278, iM1279, iM1280, iM1281, iM1282, iM1283, iM1284, iM1285, iM1286, iM1287, iM1288, iM1289, iM1290, iM1291, iM1292, iM1293, iM1294, iM1295, iM1296, iM1297, iM1298, iM1299, iM1300, iM1301, iM1302, iM1303, iM1304, iM1305, iM1306, iM1307, iM1308, iM1309, iM1310, iM1311, iM1312, iM1313, iM1314, iM1315, iM1316, iM1317, iM1318, iM1319, iM1320, iM1321, iM1322, iM1323, iM1324, iM1325, iM1326, iM1327, iM1328, iM1329, iM1330, iM1331, iM1332, iM1333, iM1334, iM1335, iM1336, iM1337, iM1338, iM1339, iM1340, iM1341, iM1342, iM1343, iM1344, iM1345, iM1346, iM1347, iM1348, iM1349, iM1350, iM1351, iM1352, iM1353, iM1354, iM1355, iM1356, iM1357, iM1358, iM1359, iM1360, iM1361, iM1362, iM1363, iM1364, iM1365, iM1366, iM1367, iM1368, iM1369, iM1370, iM1371, iM1372, iM1373, iM1374, iM1375, iM1376, iM1377, iM1378, iM1379, iM1380, iM1381, iM1382, iM1383, iM1384, iM1385, iM1386, iM1387, iM1388, iM1389, iM1390, iM1391, iM1392, iM1393, iM1394, iM1395, iM1396, iM1397, iM1398, iM1399, iM1400, iM1401, iM1402, iM1403, iM1404, iM1405, iM1406, iM14

Cryptographic Algorithms

- **Symmetric**
 - DES, AES
 - Cipher Modes
- **Asymmetric**
 - RSA, ECC
- **Secure Hashes**
 - SHA-1 and CMAC

Cryptography – Types of Algorithms

	Symmetric	Asymmetric
Scheme	One key for encoding and decoding The diagram shows a message being locked with a symmetric key by Bob and then unlocked by Alice using the same key.	Key pair (public/non-public) encoding and decoding The diagram shows a message being locked with Alice's public key by Bob and then unlocked by Alice using her private key.
Pro	→ Compact implementation → High performance → Key length (<512-bits)	→ No key exchange problem → Supports verification
Contra	→ Key exchange problem	→ Long calculation time → No message broadcasting
Algorithm	DES, AES	RSA, ECC

Symmetric Ciphers – DES & AES

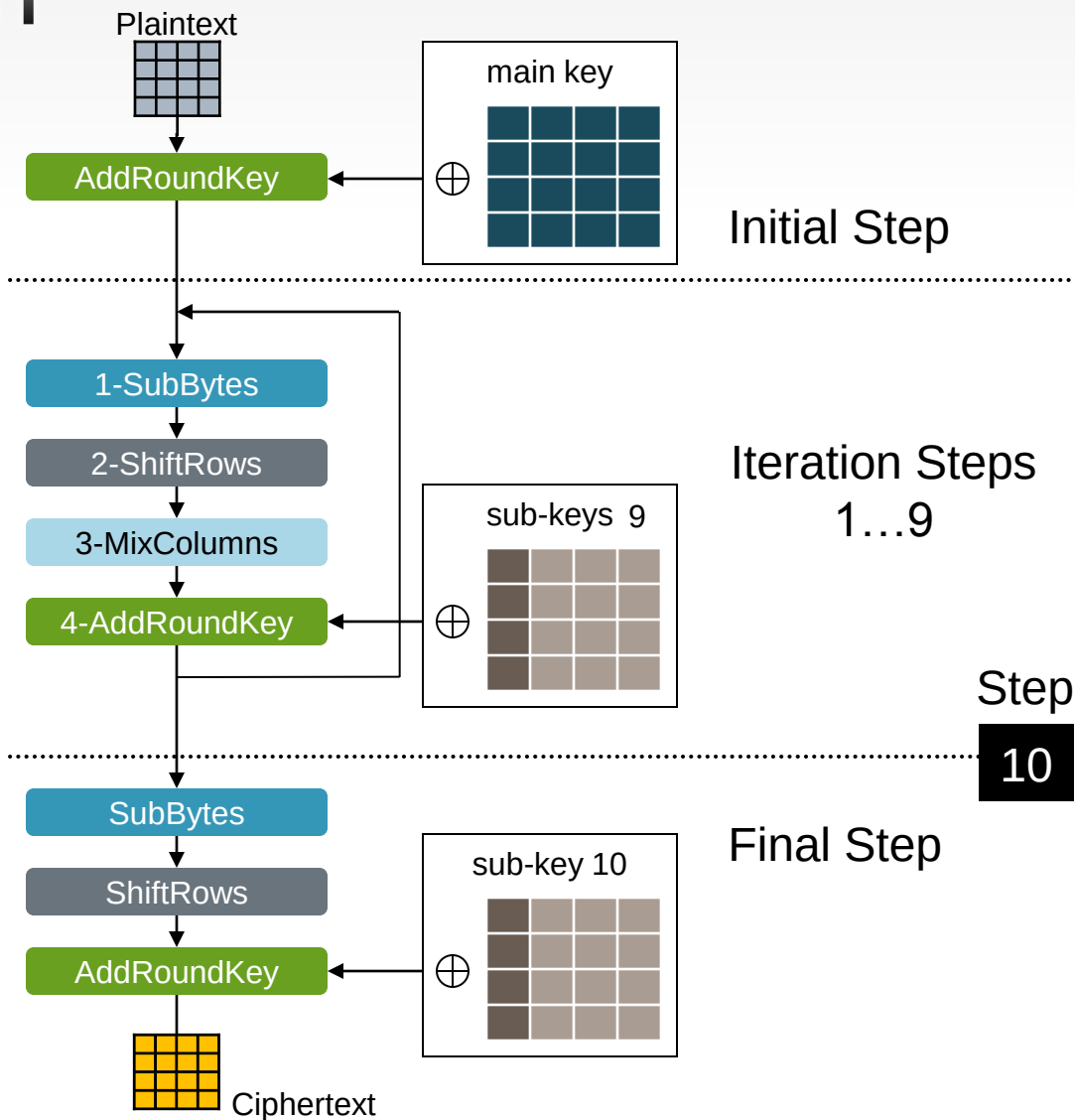
	DES	AES
Developed by	IBM	Vincent Rijmen Joan Daemen
Standardized since	1977	2002
Block size [bits]	64	128
Key size [bits]	64 (reduced to 56 bits)	128, 192 or 256
Rounds	16	10,12 or 14

DES is now considered insecure for many applications!



The diagram illustrates the four components of the AES Round Function, arranged vertically in colored boxes:

- 1-SubBytes (Teal box)
- 2-ShiftRows (Dark Grey box)
- 3-MixColumns (Light Blue box)
- 4-AddRoundKey (Green box)



The AES Algorithm II

1-SubBytes

input:

19	a0	9a	e9
3d	f4	c6	f8
e3	e2	8d	48
be	2b	2a	08

output:

d4	e0	b8	1e
27	bf	b4	41
11	98	5d	52
ae	f1	e5	30

x = high-nibble
y = low-nibble

hex		y															
		0	1	2	3	4	5	6	7	8	9	a	b	c	d	e	f
x	0	63	7c	77	7b	f2	6b	6f	c5	30	1	67	2b	fe	d7	ab	76
	1	ca	82	c9	7d	fa	59	47	f0	ad	d4	a2	af	9c	a4	72	c0
	2	b7	fd	93	26	36	3f	f7	cc	34	a5	e5	f1	71	d8	31	15
	3	04	c7	23	c3	18	96	5	9a	7	12	80	e2	eb	27	b2	75
	4	09	83	2c	1a	1b	6e	5a	a0	52	3b	d6	b3	29	e3	2f	84
	5	53	d1	0	ed	20	fc	b1	5b	6a	cb	be	39	4a	4c	58	cf
	6	d0	ef	aa	fb	43	4d	33	85	45	f9	2	7f	50	3c	9f	a8
	7	51	a3	40	8f	92	9d	38	f5	bc	b6	da	21	10	ff	f3	d2
	8	cd	0c	13	ec	5f	97	44	17	c4	a7	7e	3d	64	5d	19	73
	9	60	81	4f	dc	22	2a	90	88	46	ee	b8	14	de	5e	0b	db
	a	e0	32	3a	0a	49	6	24	5c	c2	d3	ac	62	91	95	e4	79
	b	e7	c8	37	6d	8d	d5	4e	a9	6c	56	f4	ea	65	7a	ae	8
	c	ba	78	25	2e	1c	a6	b4	c6	e8	dd	74	1f	4b	bd	8b	8a
	d	70	3e	b5	66	48	3	f6	0e	61	35	57	b9	86	c1	1d	9e
	e	e1	f8	98	11	69	d9	8e	94	9b	1e	87	e9	ce	55	28	df
	f	8c	a1	89	0d	bf	e6	42	68	41	99	2d	0f	b0	54	bb	16

S-Box

d4	e0	b8	1e
27	bf	b4	41
11	98	5d	52
ae	f1	e5	30

← rotation by 3 byte



d4	e0	b8	1e
bf	b4	41	27
5d	52	11	98
30	ae	f1	e5

$$\begin{pmatrix} 02 & 03 & 01 & 01 \\ 01 & 02 & 03 & 01 \\ 01 & 01 & 02 & 03 \\ 03 & 01 & 01 & 02 \end{pmatrix}$$


d4
bf
5d
30

==

04
66
81
e5



04	e0	48	28
66	cb	f8	06
81	19	d3	26
e5	9a	7a	4c

Every column will be modulo multiplied with a given matrix.

04	e0	48	28
66	cb	f8	06
81	19	d3	26
e5	9a	7a	4c



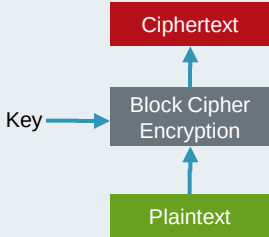
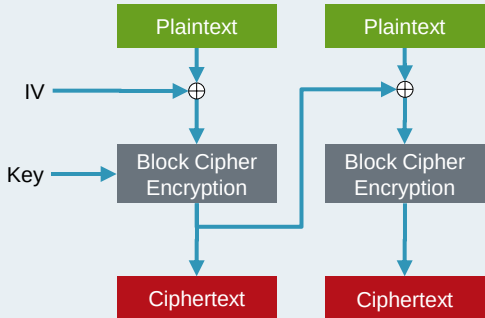
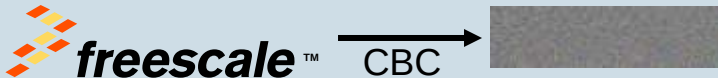
a0	88	23	2a
fa	54	a3	6c
fe	2c	39	76
17	b1	39	05



a4	68	6b	02
9c	9f	5b	6a
7f	35	ea	50
f2	2b	43	49

Every column will be xor-ed with the same column of the actual iteration sub-key.

Cipher Modes – Electronic Codebook (ECB) and Cipher-block Chaining (CBC)

	Electronic codebook (ECB)	Cipher-block chaining (CBC)
Scheme	Each block is encoded/decoded independantly from the others	Previous result is XORed with actual plaintext
Diagram		
Pro	Random access possible	Secure for messages longer than block size
Contra	Insecure for message longer than the block size (statistical analysis)	No random access possible, (before the last block can be decoded all others must be decoded)
Example		

RSA – Public Key Algorithm

- **Facts**

- Developed in 1978 by Ron Rivest, Adi Shamir and Leonard Adleman at MIT
- Key size: ≥ 1024 -bits (CAN-Message offers 8 data bytes)
- Algorithm based on very big numbers $\rightarrow$ optimized libraries (e.g. GMP)
- Expectations: RSA-1024 will be insecure by 2014

- **The RSA algorithm involves three steps**

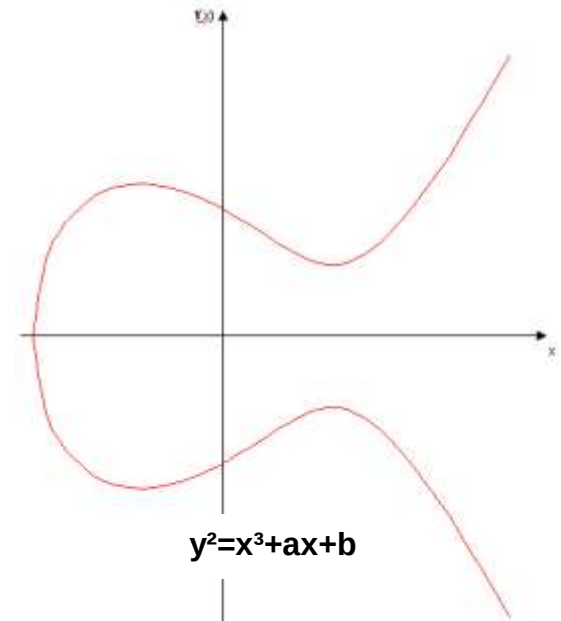
- Key generation
- Encryption
- Decryption

Elliptic Curve Cryptography (ECC)

- 1985: Victor Miller and Neil Koblitz use elliptic curves for cryptography
- It's an alternative asymmetric crypto algorithm
- ECC relies upon the difficulty of the Elliptic Curve Discrete Logarithm Problem (ECDLP)

$$a^x \equiv m \pmod{p}$$

- it's easy to calculate m , when a , x and p is known
- it's hard to find x when a , m and p is known
- ECC offers smaller key size than RSA



Security Level(*) Restricted → Confidential → Secret → Top secret	NIST Recommended Key Sizes [bits]		
	AES	RSA	ECC
Secret	128	3072	256
Top Secret	192	7680	384
	256	15360	512

Cipher Summary

	DES	AES	RSA	ECC
Secure for the next few years	✗	✓	✓ Key size > 2048	✓
Type	symmetric	symmetric	asymmetric	asymmetric
Typical key size [bits]	56	128, 192, 256	1024, 2048, 3072	180, 224, 256,320, 512
Execution time	short	short	long	long
Authentication / verification	✗	✗	✓	✓
Implementation	HW – good SW – lot of bit ops	HW / SW - good	Could combine into one module, req. big number math functions (e.g. GMP)	
Comments	Message broadcasting isn't an issue		Message broadcasting is an issue	



Secure Hashes

SHA and CMAC

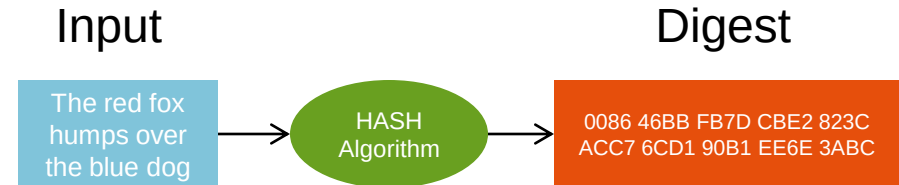


Freemake, the Freemake logo, AllCode, C#, CodeTEST, CodeWarrior, CodePins, CodeLines, C-Sharp, the Brings Efficient Solution! logo, Kinect, moolitEST, PPS, PowerQUICC, ProSource Express, QuEQ, QuickView, SafeShare, the SafeShare logo, StartCore, Symphony and WinPower are trademarks of Freemake Software Corporation, Inc. U.S. Pat & Tm. Off AirNet, Beekit, BeerStack, Coasting, Rides, Lysinease, Magikil, M6K, Platform in a Package, QDot GoVoxing, QDOT, Excelite, Really Play!, SMARTWITS, Vowex, Turbulent, Wyfired and Xtrinsic are trademarks of Freemake Software Corporation, Inc. All other product or service names are the property of their respective owners. © 2013 Freemake Software Corporation, Inc.

Hash Algorithms vs. CMACs

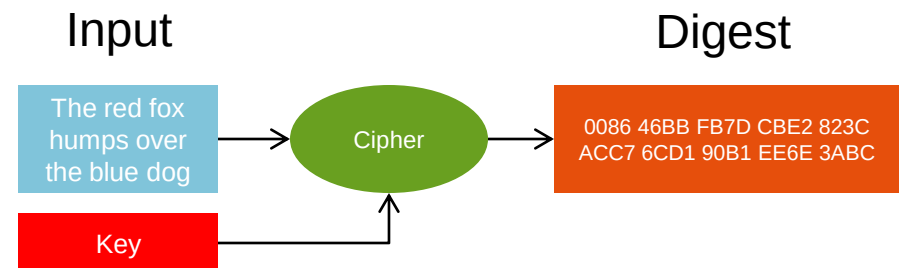
Hashe:

- Integrity check
- Public known algorithm
- Input: data blocks
- Output: HASH value
(e.g 256-512bits)

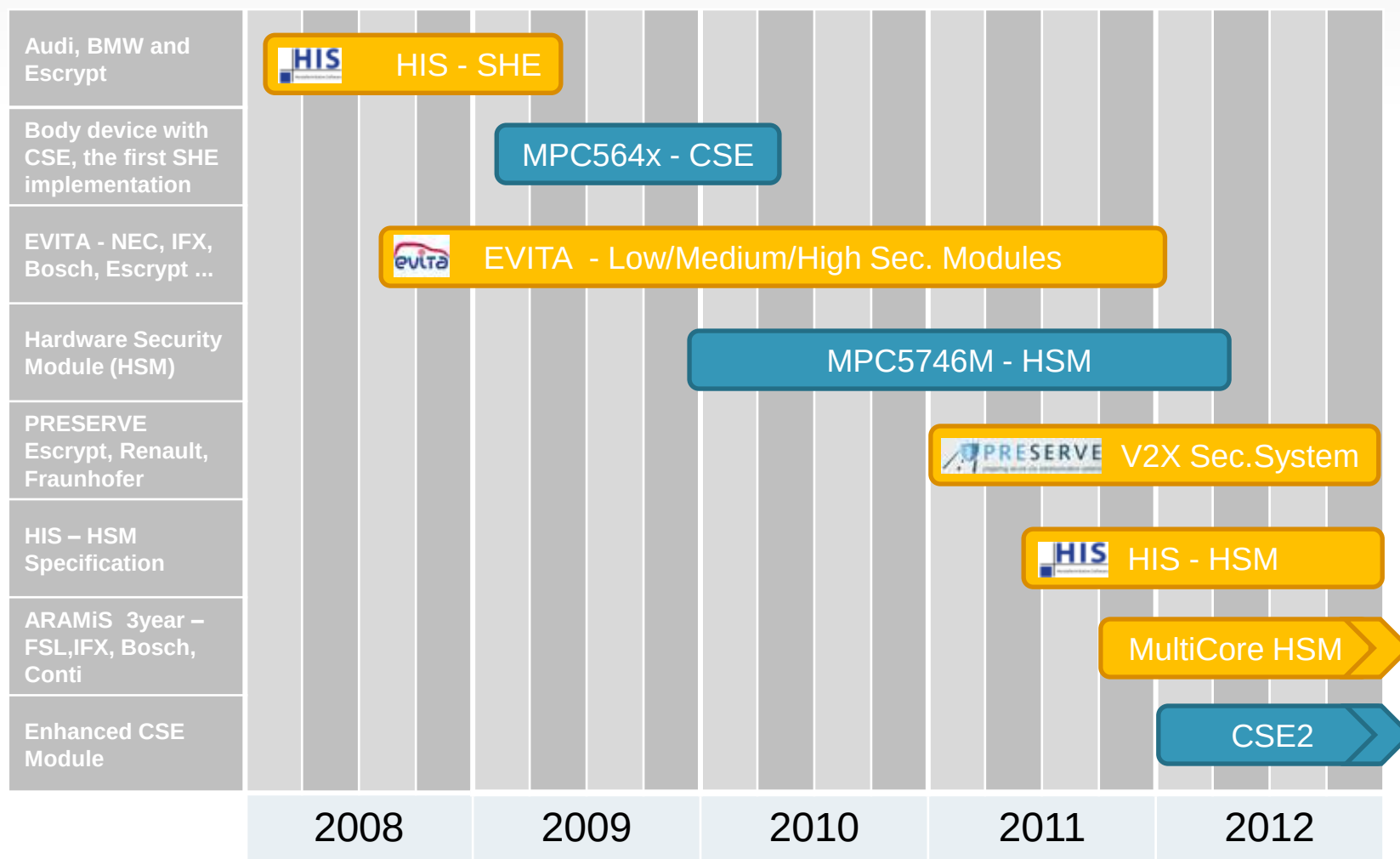


CMAC:

- Integrity & authenticity check
- Public known algorithm
- Input: data blocks & key value
- Output: CMAC value
(e.g 128-256 bits)



Automotive Security Standards and Projects



HIS – SHE Specification

- Created by Audi (main driver), BMW and Escrypt
- Published as a official HIS standard
(HIS => **H**ersteller**i**nitiative **S**oftware, German for 'OEM software initiative')
- Re-view of the Spec. by Freescale in an early phase
- Key features of the SHE specification:
 - A secure storage for crypto keys
 - Crypto algorithm acceleration (AES-128)
 - Secure Boot mechanism to verify custom firmware after reset
 - Offers 19 security specific functions
 - Up to 10 general and 5 special purpose crypto keys

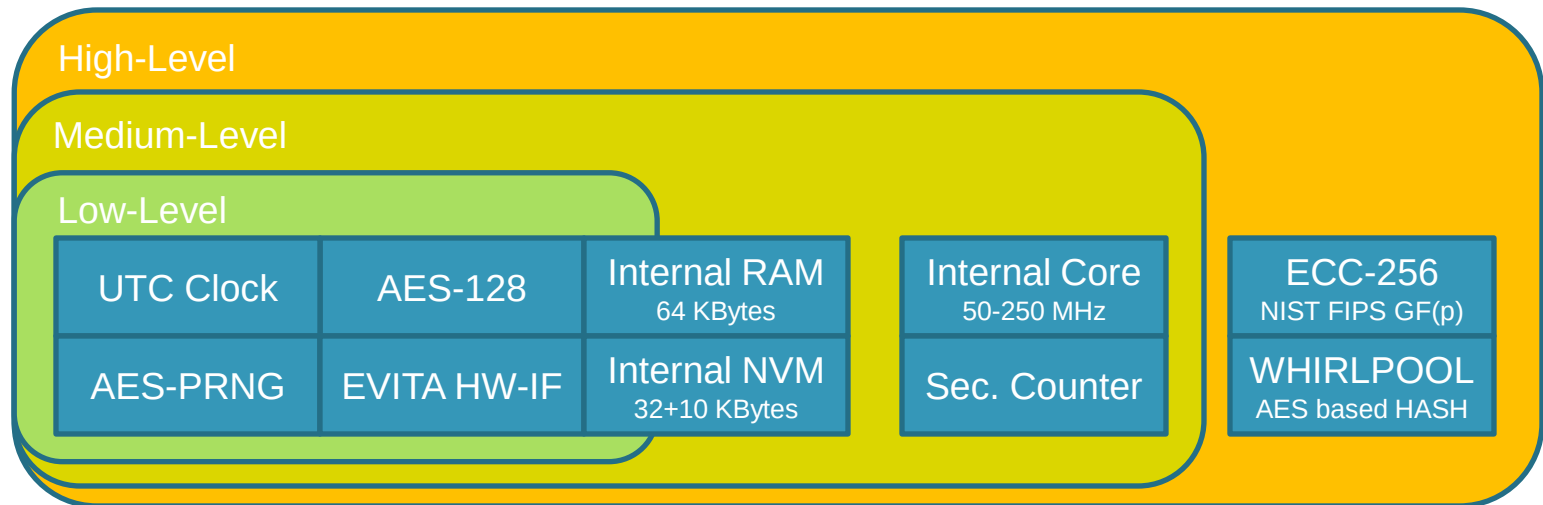
<http://www.evita-project.org>

EVITA a project co-funded by the European Union.

The objective of EVITA is to design, verify, and prototype an architecture for automotive on-board networks where security-relevant components are protected against tampering and sensitive data are protected against compromise.



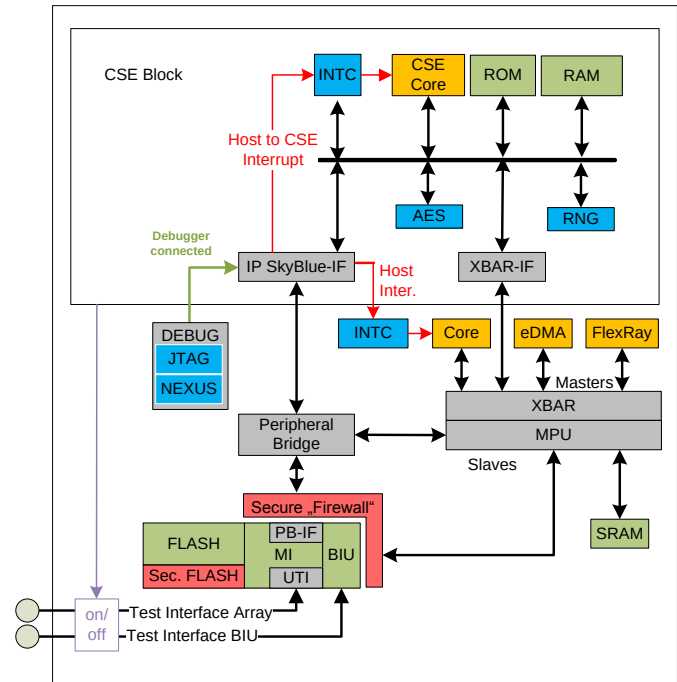
EVITA Security Modules



[illegible]

Cryptographic Services Engine (CSE) Qorivva MPC564xB/C

- **CSE module implements the official HIS SHE-Specification**
- **32-bit secure core working at 120 MHz**
- **AES-128**
 - Supported crypto modes: ECB & CBC
 - Throughput 100 Mbit/sec
 - Latency 2 μ s per one encoding/decoding ops
- **CSE module interfaces:**
 - Crossbar master interface
 - Configuration interface
- **Secure flash blocks assigned to the CSE module. Accesses from other masters are impossible.**
- **PRNG seed generation via TRNG**
- **CSE Core not programmable by customer**



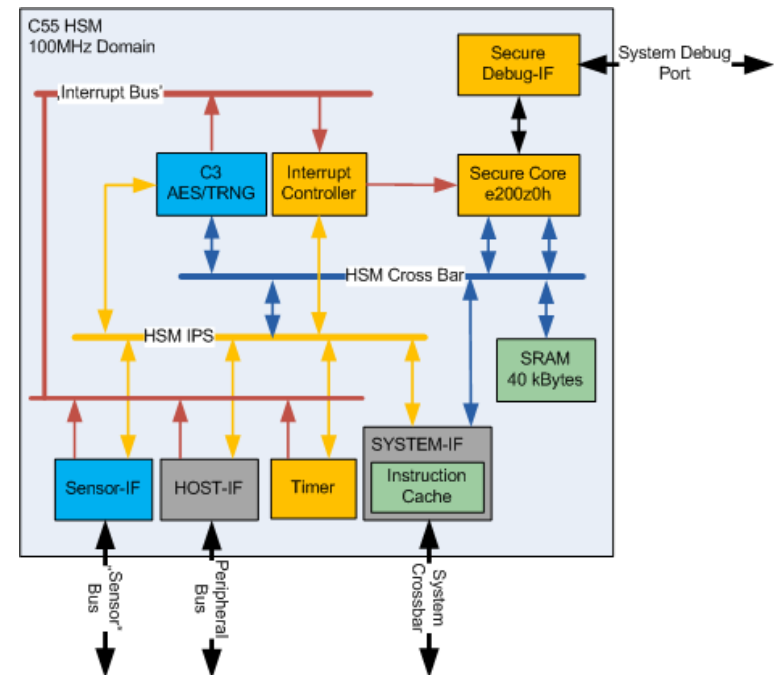
Hardware Security Module (HSM)

v1: MPC5746M / MPC5777M & v2: MPC5748G

**HSM is free programmable by the customer,
additional security algorithm could implemented in software**

Features:

- **e200z0h core (v1: 100MHz / v2: 80 MHz)**
- **4Kbytes Instruction cache**
- **Secure Debugger Interface**
- **Cryptographic Modules with AES-128, Random Number Generator, DMA**
- **Sensor Interface – monitor for voltage, temperature and clock (v1 only)**
- **Memory**
 - SRAM (v1: 40 Kbytes / v2: 32 Kbytes)
 - Flash
 - code: 2 x 64 Kbytes + 1 x 16KBytes
 - data : 2 x 16 Kbytes



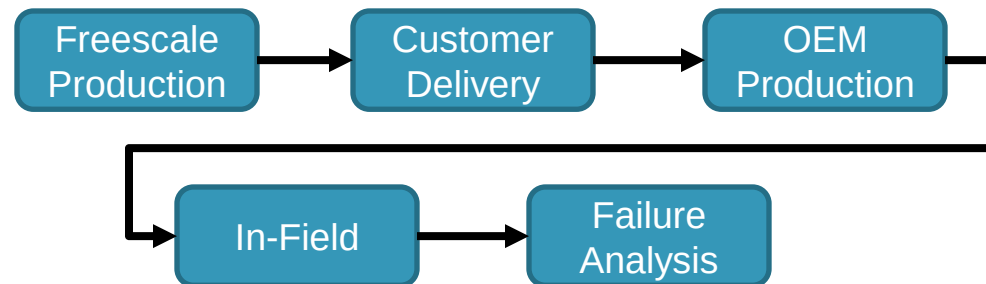
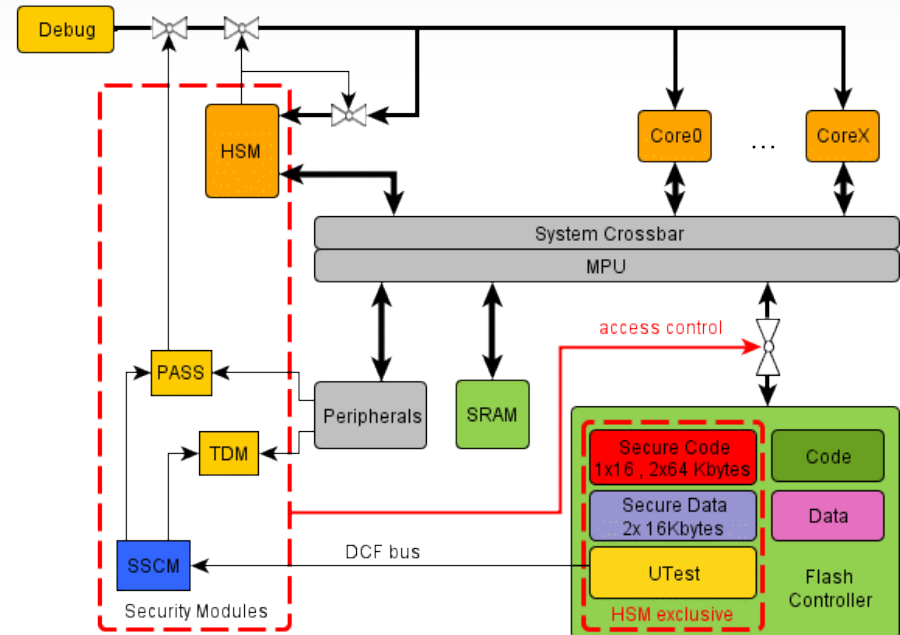
C3 – AES-128 Features

- Cryptographic keys
 - 2 x read-only keys (programmed by FSL)
 - 8 x GPR-keys
 - 1 x PRNG-key
- Cipher Modes: ECB, CBC, OFB, CFB, CTR, XTS, CMAC, GCM and OFB for PRNG
- Multiple contexts; context swapping performed by hardware
- Programming interfaces:
 - Register based mode
 - SmartDMA mode

Qorivva HSM Security Architecture

Features:

- Device life cycle scheme
- Unique ID for each device
- Debugger restrictions
- Flash Protection
 - OTP
 - read / write & erase
 - diary to log erasing-steps



SSCM: System Status Configuration Module
PASS: Password And Device Security Module
TDM: Tamper Detection Module
HSM: Hardware Security Module
MPU: Memory Protection Unit
DCF: Device Configuration Format

Freescal Devices with Security

Freescale Security Solution for Automotive products			
	Device	Platform	Module
MCU (internal flash)	MPC564xB/C	PowerPC e200	CSE
	MPC5746M		HSMv1
	MPC5777M		HSMv1
	MPC5748G		HSMv2
MPU (flash-less)	Vybrid Controller Solutions	ARM Cortex-Ax/Mx & ARM9/11	Trust Zone + Sahara / CAAM
	i.Mx 2x / 3x / 5x / 6x / 7x		

no automotive standards

CSE, HSM and the Security Standards

Security Standards	EVITA- Low	HIS-SHE	EVITA-Medium (HIS-Medium)	EVITA-High
Main features	UID Crypto engine	NVM is mandatory Fix function set	Programmable by customer	Public Key HASH
CSE / CSE2	supported by MPC564xB/C			
HSM (v1/v2)	supported by MPC5746M & Calypso			



FreeScale, the Freescale logo, AllWin, e3, CookTEST, CookWarp, CookPins, CookPins+, e2Ware, the Energy Efficient Solution logo, iMx6, iMx6TEST, PPG, PowerQICC, Processor Expert, QoQo, QoQoCore, SafeSense, the SafeSense logo, SafeCore, Symphony and Vortex are trademarks of Freescale Semiconductor, Inc. ® U.S. Pat. & Tm. Off. Art. Bee, BeeLink, BeeStack, Coasting, RISC, iAgnosic, MagiQ, M6M, Platform in a Package, QoQo GoVortex, QoQo Desktop, Really IP, SMARTWIS, Toss, Turbula, Vybrid and Vybrid are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © 2013 Freescale Semiconductor, Inc.

Secure Boot

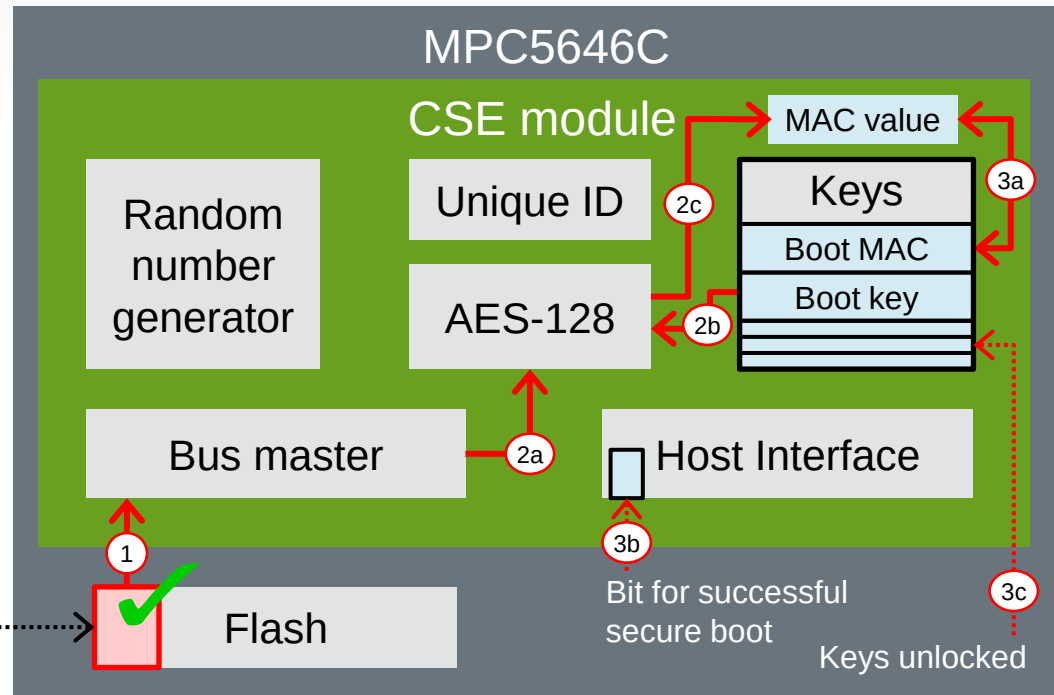
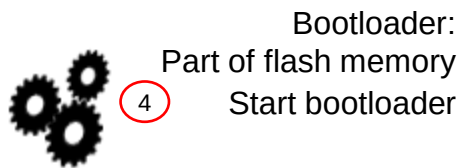
Check Boot Loader for Integrity and Authenticity

Step 1: After power on: CSE module reads bootloader via its bus master interface.

Step 2: CSE module uses the boot key to calculates the **MAC value** of the bootloader.

Step 3: CSE module compares calculated MAC with stored boot MAC. If identical: successful secure boot → set respective bit in host interface and unlock keys

Step 4: MCU always starts bootloader.



- **MAC** protects against modification of bootloader and depends on the (secret) boot key → integrity and authenticity of bootloader.
- Only if calculated MAC value matches stored boot MAC value: successful secure boot → set respective bit in host interface and unlock keys for further usage (see next demos)

Chain of Trust

Check parts of Flash Memory for Integrity and Authenticity

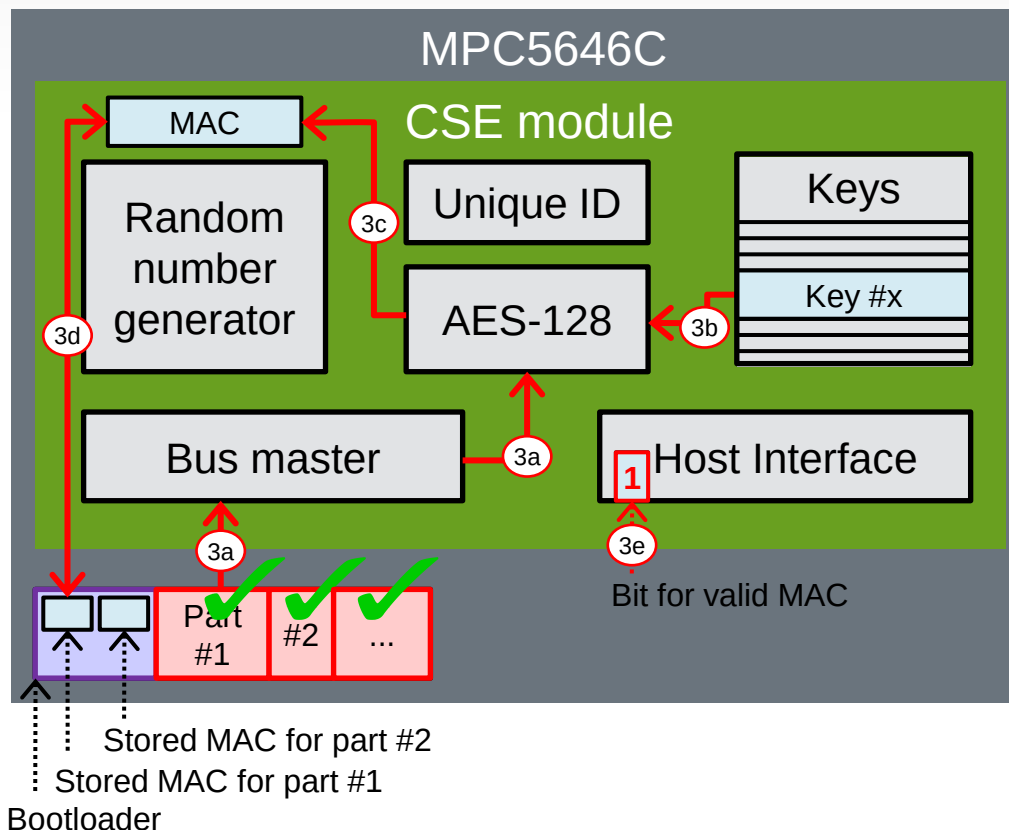
Step 1: Successful secure boot to verify bootloader and to unlock keys. Then the bootloader configures the MCU for full speed, best memory timing, etc.

Step 2: Bootloader asks CSE module to verify MAC for part #1 of flash memory using key #x

Step 3: CSE module reads part of flash, uses key #x to calculate MAC, and compares calculated MAC with MAC for part #1 as stored in bootloader. If identical, CSE module sets corresponding bit in host interface.

Step 4: Bootloader checks bit.
If set: Part #1 of flash ok → execute part #1.

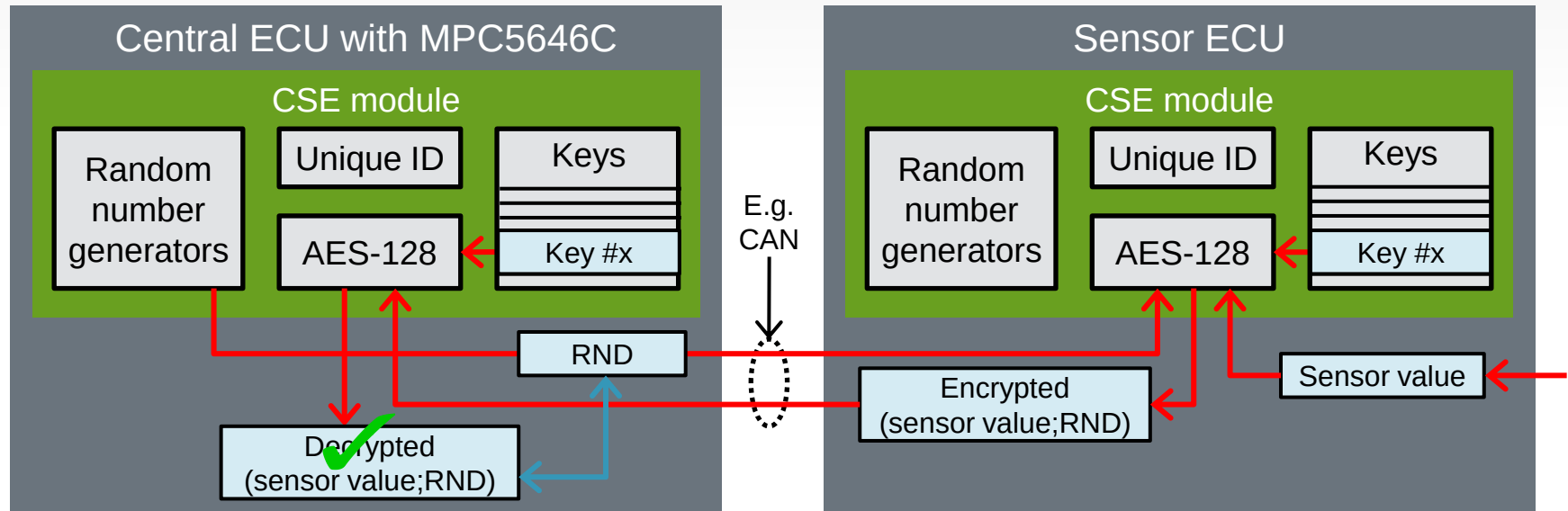
Step 5 etc: Similar to bootloader vs. part #1 of flash: Part #n of flash verifies part #n+1 → chain of trust.



- MACs stored in bootloader provide integrity and authenticity of the related parts in flash memory.
 - Bootloader protected by secure boot (see previous demo).
 - Part-by-part checking of flash to execute critical parts of flash (e.g., MCU configuration/IRQ table) as soon as possible.
- juergen.frank@freescale.com

juergen.frank@freescale.com

Secure Communication



Step 1: Central ECU obtains random number and sends it to sensors ECU (e.g., after power-on of car)

Step 2: Sensor ECU reads sensor value and asks CSE module to encrypt it and the received random number (using key #x)

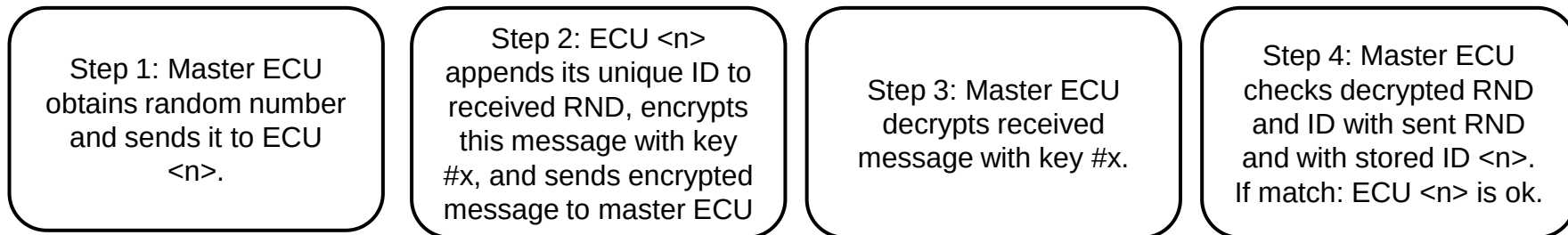
Step 3: Sensor ECU sends encrypted message to central ECU.

Step 4: Central ECU asks CSE module to decrypt received message (using key #x).

Step 5: Central ECU checks
sent random number vs.
received/decrypted random
number.

- Random number: protects against replay attacks.
- Encryption: protects against eavesdropping.
- Random number and encryption: ensures data integrity and authenticity.

juergen.frank@freescale.com



- Replacement or modification of ECU <n> will change its unique ID and/or keys. Both will be detected with this proposal for component protection.

Summary





Summary

- Automotive Security: Protection and Enablement
- Automotive Security: A global concern, a requirement and a new trend.
- Two types: In-vehicle Security and Connected Vehicle Security
- Freescale offers a variety of solutions today

Freescale Security Solution for Automotive products			
	Device	Platform	Module
MCU (internal flash)	MPC564xB/C	PowerPC e200	CSE
	MPC5746M		HSMv1
	MPC5777M		HSMv1
	MPC5748G		HSMv2
MPU (flash-less)	Vybrid Controller Solutions	ARM Cortex-Ax/Mx & ARM9/11	Trust Zone + Sahara / CAAM
	i.Mx 2x / 3x / 5x / 6x / 7x		

- **Jürgen Frank**

- Juergen.Frank@freescale.com

