

# MPC8569E Instruction RAM Microcode Package Release 8569Rev2\_IRAM\_1.0.0

## General

This release note reflects differences between the *QUICC Engine™ Block Reference Manual with Protocol Interworking*, QEIWRM Rev. 3, and the features which are available for this device using the provided microcode Instruction RAM (IRAM) packages. The following release note reveals any exceptions to the features which are specified in this release of the specification. The notes also describe any addition to the specification or any missing functionality in comparison to the specification.

The user should follow tightly the instructions specified in the QE\_Ucode\_Loader file provided in the package in relation to the header files containing the code. These instructions assure proper operation and activation of the right features in the code.

Refer to the *QUICC Engine Microcode Errata* for all known issues related to this and other microcode packages.

This package includes the following core blocks: ATM (AAL0, AAL1, AAL2, AAL5), Ethernet (10/100/1000), Plain PPP, ML/MC PPP, PPP MUX, channelized HDLC, transparent, or SS7 protocols, CES, IMA, POS, IP fragmentation, Virtual Port, Burst Tolerance, IP/UDP Header Compression, IP Security, Longest Prefix Match, L2 switch, SPI, HDLC, USB, ESS7, UART, BISYNC, QMC, Serial ATM, and all interworking features. Features of these core blocks that are not supported in this package are described in [Table 3](#).

## Availability

The package is currently available for the following devices.

**Table 1. Package Availability by Device**

Device	Loader file name (.h)
<a href="#">MPC8569 rev 2.0</a>	iram_mpc8569_r2.0.h

## Package content

The tables below designate the content of this package. The baseline is the *QUICC Engine™ Block Reference Manual with Protocol Interworking*, QEIWRM Rev. 3. The tables below show additional features and features which are not supported. For the specification of additional features, which are not described in the *QUICC Engine™ Block Reference Manual with Protocol Interworking*, QEIWRM Rev. 3, please contact Freescale support. Contact information may be found at [www.freescale.com](http://www.freescale.com).

**Table 2. New Features (Which are Not Described in QEIWRM Rev. 3)**

Feature	Comments
None	

**Table 3. Removed Features (Described in QEIWRM Rev. 3 but Not Supported)**

Feature	Comments	QEIWRM Rev. 3
IP Reassembly 2 Fragments	This feature is not supported and will be replaced in future releases with IP Reassembly FULL.	
Hash LookUp Set Description	The hash lookup set description is changed (see in Table 3).	
ATM MSP	ATM MSP	

## Revision History

Table 4. Revision History for Release 8569Rev2\_IRAM\_1.0.0

Release Date: Jan. 31, 2010 Revision Register Number: 0xD2010000	
<b>New Features</b>	N/A
<b>Removed Features</b>	N/A
<b>Bug Fixes</b>	N/A

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Freescale Semiconductor Japan Ltd.  
 Headquarters  
 ARCO Tower 15F  
 1-8-1, Shimo-Meguro, Meguro-ku  
 Tokyo 153-0064  
 Japan  
 0120 191014 or  
 +81 3 5437 9125  
[support.japan@freescale.com](mailto:support.japan@freescale.com)

### Asia/Pacific:

Freescale Semiconductor China Ltd.  
 Exchange Building 23F  
 No. 118 Jianguo Road  
 Chaoyang District  
 Beijing 100022  
 China  
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Freescale Semiconductor  
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