



Product Type	Integrated Communication Processor
Freescale Part #	MPC875, MPC885
Name	PowerQUICC 1 "Duet"
Package	256 BGA
Crypto Hardware	SEC 1.2

Algorithms	Max Key Size (bits)
DES (ECB, CBC)	56
3DES (ECB, CBC)	168 (3-keys)
AES (ECB, CBC, CTR)	256
MD-5+ HMAC	(up to 512 bit keys)
SHA-1+ HMAC	(up to 512 bit keys)
SHA-256+ HMAC	(up to 512 bit keys)

Target Applications:
Broadband Gateways, SOHO routers, wireless access points

Export Control Info:
Harmonized Tariff (US): 8542.31.0000
ENC Status: Restricted. US EAR part 740.17(b)(2)
ECCN: 5A002
CCAT: G026024

Overview:
The MPC875/MPC885 are member's of the PowerQUICC 1 family of integrated communications processor from Freescale. The PowerQUICC 1 is considered one of the most highly integrated and widely adopted communications processor in the world, and is capable of supporting most LAN and WAN networking standards. The MPC875 and MPC885 include an on-chip encryption acceleration unit which is derived from the MPC185, a Freescale Encryption Co-Processor already granted ENC status (CCAT: G026024). This on-chip encryption accelerator (also known as the SEC 1.2) is expected to achieve ~200Mbps 3DES throughput.