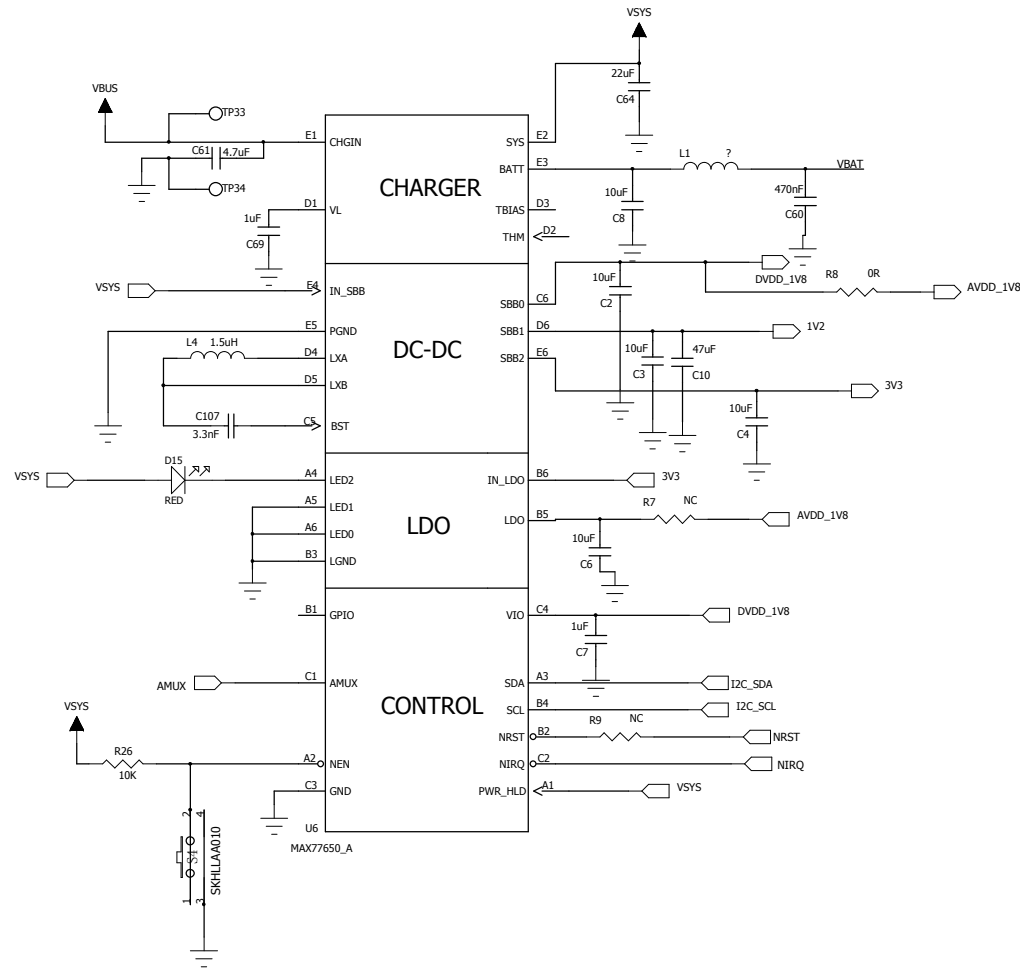
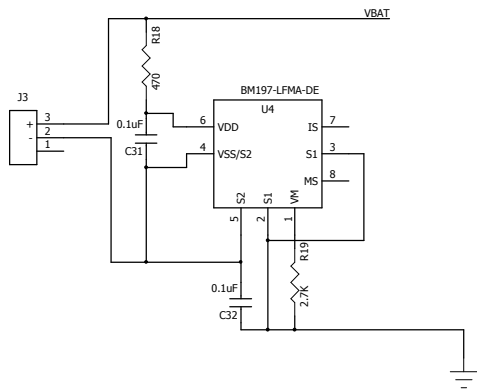


V07	change MCU form 32 pins to 48 pins KLseries
V071	change power IC from two LDOs and one charge IC to one PMIC MAX77650
V072	Remove MIC on board Add a 4 dip switch
V073	LDO_1.8V not be used but as a back up
V08	Seperate AVDD_1V8 AND DVDD_1V8 AT SBB0 OF MX77650 ADD A PINHEADER FOR LP_UARTTX,LP_UARTRX,GND
V081	The AMUX pin of the MAX77650 (U6) connect to the pin PTE22 of the KL27 Connect the NIRQ pin of the MAX77650 (U6) to PTD0 of the KL27 (U9). The MIC_MUTE pin connect to the pin PTC8 of the KL27,and The SPK_MUTE pin connect to pin the PTC9 Add C10(47μF) next to C3 Support audio over USB an I2S interface(by 0R): PTC0 (I2S0_TXD0) -> I2S_S_OUT (DATIN pin DA7212)、PTC5 (I2S0_RXD0) -> I2S_S_IN (DATOUT pin DA7212)、 PTB18 -> I2S_BCLK、PTB19 -> I2S_WS Pins A1 and A2 of tJ2 move to PTD1 and PTD2.
V1.0	Remove R20 and C9



POR_RESET TP11
I2S_CLK TP10
I2S_S_IN TP9
I2S_S_OUT TP8
I2S_WS TP7

SPI_TP

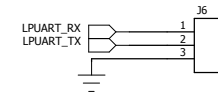
SPI_MISO TP5
SPI_MOSI TP1
SPI_CLK TP2
SPI_CSN TP3
SPI_INT TP4

SPI_TP

I2C_SDA TP19
I2C_SCL TP20
I2C_TP
DVDD_1V8 TP18
NRST TP14

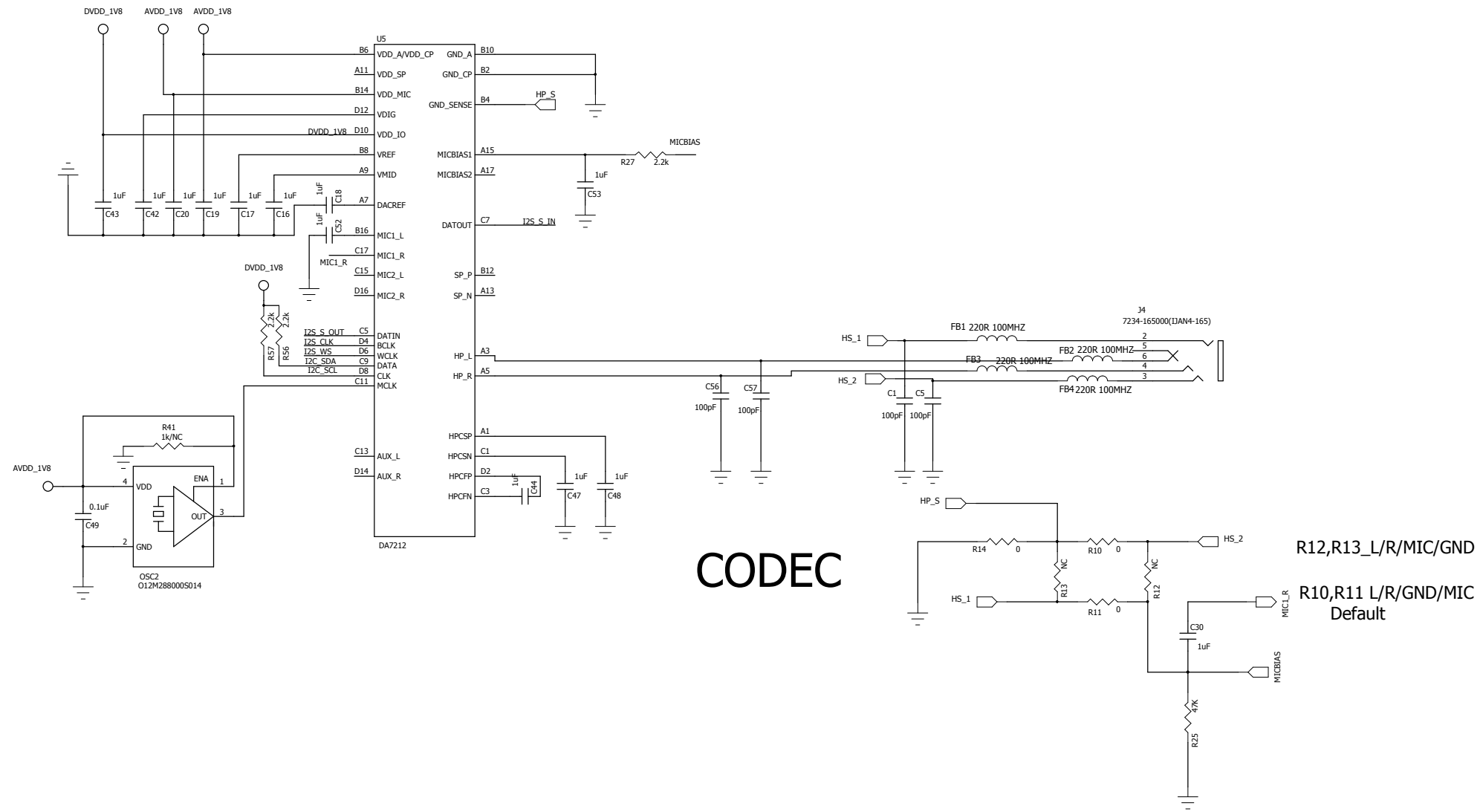
UART_TP

MARK1 MARK4
MARK2 MARK5
MARK3 MARK6



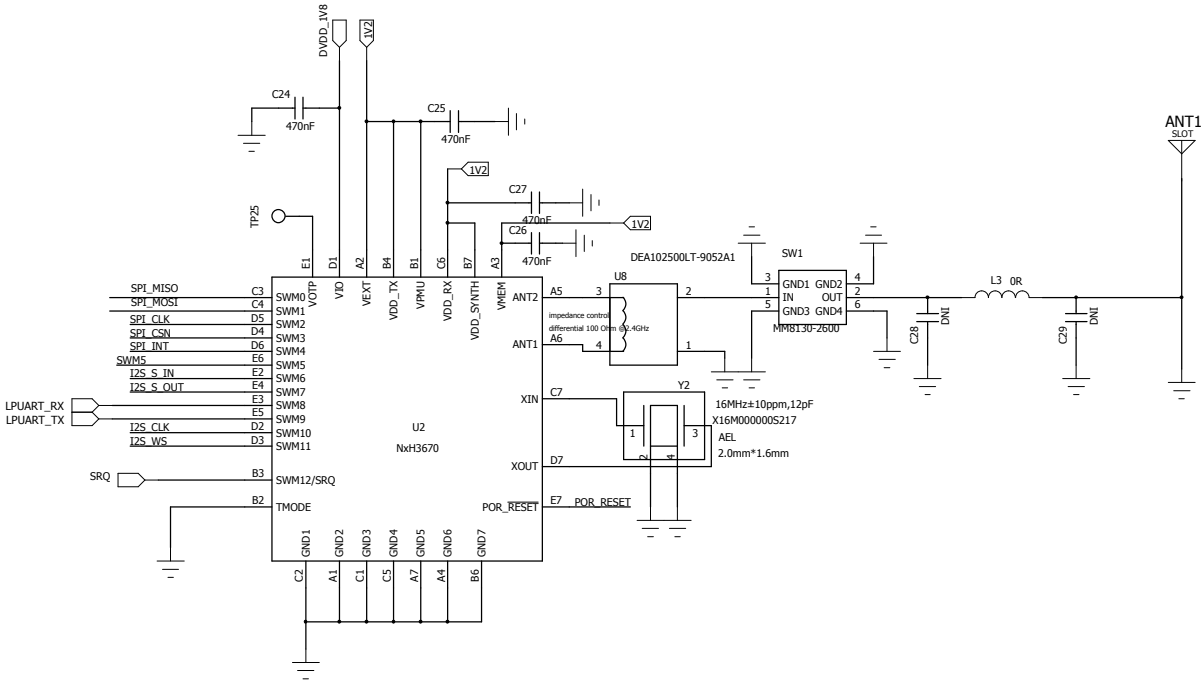
HORN Shenzhen Horn Audio.,LTD

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TP21 ○ SPI_MISO
TP6 ○ SPI_MOSI
TP22 ○ SPI_CLK
TP23 ○ SPI_CSN
TP24 ○ SPI_INT
TP26 ○ SWM5
TP27 ○ I2S_S_IN
TP28 ○ I2S_S_OUT
TP29 ○ LPUART_RX
TP30 ○ LPUART_TX
TP31 ○ I2S_CLK
TP32 ○ I2S_WS

NXH3670_TPS



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