

QUICC Multi-Channel Controller (QMC) RAM Microcode Package Release 0.0.0

General

This release note reflects the new features for QUICC Multi-Channel Controller (QMC) microcode available in the described RAM packages. The packages are available for the QUICC Engine devices MPC8360 and MPC8323. This release note reveals any exceptions to the features which are specified in the reference manual. The notes also describe any additions or any missing functionality in comparison to the reference manual.

Refer to the *QUICC Engine Microcode Errata* for all known issues related to this and other microcode packages.

For the MPC8360, refer to the “QUICC Multi-Channel Controller (QMC)” chapter in the current version of the *QUICC Engine™ Block Reference Manual with Protocol Interworking (QEIWRM, Rev. 3)*.

For the MPC8323, refer to the “QUICC Multi-Channel Controller (QMC)” chapter in current version of the *MPC8323E PowerQUICC™ II Pro Integrated Communications Processor Reference Manual (MPC8323ERM, Rev. 2)*.

Availability

The table below shows the currently available packages by device.

Table 1. Package Availability by Device

Device	Loader File Name (.h) / C function file name (.c)
MPC8360 rev 2.1	QMC_SCEn_mpc8360_r2.1.h QMC_SCEn_mpc8360_r2.1.c QMC_SCDIs_mpc8360_r2.1.h QMC_SCDIs_mpc8360_r2.1.c
MPC8323 rev 1.1	QMC_SCEn_mpc8323_r1.1.h QMC_SCEn_mpc8323_r1.1.c QMC_SCDIs_mpc8323_r1.1.h QMC_SCDIs_mpc8323_r1.1.c

Package Content

1. The QMC RAM package includes optimized QMC microcode. This package was built to increase QMC performance and reduce QUICC Engine RISC utilization. The QMC RAM package includes 2 QMC patches:
 - **QMC_SCEn** - with Subchanneling on each time slot support (Mask field supported in each QMC TSA table entry).
 - **QMC_SCDIs** - without Subchanneling on each time slot support. Only 8 bits per time slot. The mask field in QMC TSA table entry is ignored.
2. The QMC RAM package provides .h files for use in the NetComm SW standard microcode loader and .c files for use in general C function format.

Revision History

Table 2. Revision History for Release 0.0.0

	<p>Release Date: Feb 12, 2009</p> <p>Revision Register Numbers:</p> <p>0xB60F1000 - MPC8360 Full byte support</p> <p>0xB60F2000 - MPC8360 8-bit mode only</p> <p>0xB23F1000 - MPC8323 Full byte support</p> <p>0xB23F2000 - MPC8323 8-bit mode only</p>
New Features	Performance improvement
Removed Features	None
Bug Fixes	None

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