

Multi-Channel Controller on UCC (UMCC) RAM Package Release 0.0.0

General

This release note reflects the Multi-Channel Controller on UCC (UMCC) microcode available in the described RAM packages. The packages are available for the QUICC Engine MPC8323E device.

Refer to the *QUICC Engine Microcode Errata* for all known issues related to this and other microcode packages.

The user should follow tightly the instructions specified in the QE_Ucode_Loader file provided in the package in relation to the header files containing the code. These instructions assure proper operation and activation of the right features in the code.

Availability

The package is currently available for the following devices.

Table 1. Package Availability by Device

Device	Loader file name (.h)/(c)
MPC8323 rev 1.1	UMCC_mpc8323_r1.1.h UMCC_mpc8323_r1.1.c

Package Content

1. The UMCC package includes the functions of Multi-Channel Controller on UCC, see “Multi-Channel Controller on UCC(UMCC),” the UMCC spec.
2. The UMCC uses the new initialization command with the OPCODE - 0xE for UCC slow protocol, see Section 23.3.2, “UMCC Commands and Section 23.3.6, “UMCC Initialization,” in UMCC spec.
3. The UMCC RAM package provides .h files for use in the NetComm SW standard microcode loader and .c files for use in general C function format.

For the specification of UMCC, please contact Freescale support. Contact information may be found at www.freescale.com.

Revision History

Table 2. Revision History for Release 0.0.0

	Release Date: Aug 24, 2009 Revision Register Number: 0xB23F4000 for MPC8323Rev1.1
New Features	UMCC controller
Removed Features	None
Bug Fixes	None

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