

Advanced Control Library

User Reference Manual

56800E
Digital Signal Controller

56800E_ACLIB
Rev. 2
5/2011

freescale.com

The following revision history table summarizes changes contained in this document.

Table 0-1. Revision History

Date	Revision Label	Description
	0	Initial release
	1	Reformatted and updated revision
	2	FSLESL 2.0

Chapter 1 License Agreement

FREESCALE SEMICONDUCTOR SOFTWARE LICENSE AGREEMENT.

This is a legal agreement between you (either as an individual or as an authorized representative of your employer) and Freescale Semiconductor, Inc. ("Freescale"). It concerns your rights to use this file and any accompanying written materials (the "Software"). In consideration for Freescale allowing you to access the Software, you are agreeing to be bound by the terms of this Agreement. If you do not agree to all of the terms of this Agreement, do not download the Software. If you change your mind later, stop using the Software and delete all copies of the Software in your possession or control. Any copies of the Software that you have already distributed, where permitted, and do not destroy will continue to be governed by this Agreement. Your prior use will also continue to be governed by this Agreement.

OBJECT PROVIDED, OBJECT REDISTRIBUTION LICENSE GRANT.

Freescale grants to you, free of charge, the non-exclusive, non-transferable right (1) to reproduce the Software, (2) to distribute the Software, and (3) to sublicense to others the right to use the distributed Software. The Software is provided to you only in object (machine-readable) form. You may exercise the rights above only with respect to such object form. You may not translate, reverse engineer, decompile, or disassemble the Software except to the extent applicable law specifically prohibits such restriction. In addition, you must prohibit your sublicensees from doing the same. If you violate any of the terms or restrictions of this Agreement, Freescale may immediately terminate this Agreement, and require that you stop using and delete all copies of the Software in your possession or control.

COPYRIGHT. The Software is licensed to you, not sold. Freescale owns the Software, and United States copyright laws and international treaty provisions protect the Software. Therefore, you must treat the Software like any other copyrighted material (e.g. a book or musical recording). You may not use or copy the Software for any other purpose than what is described in this Agreement. Except as expressly provided herein, Freescale does not grant to you any express or implied rights under any Freescale or third-party patents, copyrights, trademarks, or trade secrets. Additionally, you must reproduce and apply any copyright or other proprietary rights notices included on or embedded in the Software to any copies or derivative works made thereof, in whole or in part, if any.

SUPPORT. Freescale is NOT obligated to provide any support, upgrades or new releases of the Software. If you wish, you may contact Freescale and report problems and provide suggestions regarding the Software. Freescale has no obligation whatsoever to respond in any way to such a problem report or

suggestion. Freescale may make changes to the Software at any time, without any obligation to notify or provide updated versions of the Software to you.

NO WARRANTY. TO THE MAXIMUM EXTENT PERMITTED BY LAW, FREESCALE EXPRESSLY DISCLAIMS ANY WARRANTY FOR THE SOFTWARE. THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. YOU ASSUME THE ENTIRE RISK ARISING OUT OF THE USE OR PERFORMANCE OF THE SOFTWARE, OR ANY SYSTEMS YOU DESIGN USING THE SOFTWARE (IF ANY). NOTHING IN THIS AGREEMENT MAY BE CONSTRUED AS A WARRANTY OR REPRESENTATION BY FREESCALE THAT THE SOFTWARE OR ANY DERIVATIVE WORK DEVELOPED WITH OR INCORPORATING THE SOFTWARE WILL BE FREE FROM INFRINGEMENT OF THE INTELLECTUAL PROPERTY RIGHTS OF THIRD PARTIES.

INDEMNITY. You agree to fully defend and indemnify Freescale from any and all claims, liabilities, and costs (including reasonable attorney's fees) related to (1) your use (including your sublicensee's use, if permitted) of the Software or (2) your violation of the terms and conditions of this Agreement.

LIMITATION OF LIABILITY. IN NO EVENT WILL FREESCALE BE LIABLE, WHETHER IN CONTRACT, TORT, OR OTHERWISE, FOR ANY INCIDENTAL, SPECIAL, INDIRECT, CONSEQUENTIAL OR PUNITIVE DAMAGES, INCLUDING, BUT NOT LIMITED TO, DAMAGES FOR ANY LOSS OF USE, LOSS OF TIME, INCONVENIENCE, COMMERCIAL LOSS, OR LOST PROFITS, SAVINGS, OR REVENUES TO THE FULL EXTENT SUCH MAY BE DISCLAIMED BY LAW.

COMPLIANCE WITH LAWS; EXPORT RESTRICTIONS. You must use the Software in accordance with all applicable U.S. laws, regulations and statutes. You agree that neither you nor your licensees (if any) intend to or will, directly or indirectly, export or transmit the Software to any country in violation of U.S. export restrictions.

GOVERNMENT USE. Use of the Software and any corresponding documentation, if any, is provided with **RESTRICTED RIGHTS**. Use, duplication or disclosure by the Government is subject to restrictions as set forth in subparagraph (c)(1)(ii) of The Rights in Technical Data and Computer Software clause at DFARS 252.227-7013 or subparagraphs (c)(1) and (2) of the Commercial Computer Software--Restricted Rights at 48 CFR 52.227-19, as applicable. Manufacturer is Freescale Semiconductor, Inc., 6501 William Cannon Drive West, Austin, TX, 78735.

HIGH RISK ACTIVITIES. You acknowledge that the Software is not fault tolerant and is not designed, manufactured or intended by Freescale for

incorporation into products intended for use or resale in on-line control equipment in hazardous, dangerous to life or potentially life-threatening environments requiring fail-safe performance, such as in the operation of nuclear facilities, aircraft navigation or communication systems, air traffic control, direct life support machines or weapons systems, in which the failure of products could lead directly to death, personal injury or severe physical or environmental damage ("High Risk Activities"). You specifically represent and warrant that you will not use the Software or any derivative work of the Software for High Risk Activities.

CHOICE OF LAW; VENUE; LIMITATIONS. You agree that the statutes and laws of the United States and the State of Texas, USA, without regard to conflicts of laws principles, will apply to all matters relating to this Agreement or the Software, and you agree that any litigation will be subject to the exclusive jurisdiction of the state or federal courts in Texas, USA. You agree that regardless of any statute or law to the contrary, any claim or cause of action arising out of or related to this Agreement or the Software must be filed within one (1) year after such claim or cause of action arose or be forever barred.

PRODUCT LABELING. You are not authorized to use any Freescale trademarks, brand names, or logos.

ENTIRE AGREEMENT. This Agreement constitutes the entire agreement between you and Freescale regarding the subject matter of this Agreement, and supersedes all prior communications, negotiations, understandings, agreements or representations, either written or oral, if any. This Agreement may only be amended in written form, executed by you and Freescale.

SEVERABILITY. If any provision of this Agreement is held for any reason to be invalid or unenforceable, then the remaining provisions of this Agreement will be unimpaired and, unless a modification or replacement of the invalid or unenforceable provision is further held to deprive you or Freescale of a material benefit, in which case the Agreement will immediately terminate, the invalid or unenforceable provision will be replaced with a provision that is valid and enforceable and that comes closest to the intention underlying the invalid or unenforceable provision.

NO WAIVER. The waiver by Freescale of any breach of any provision of this Agreement will not operate or be construed as a waiver of any other or a subsequent breach of the same or a different provision.

Chapter 2 INTRODUCTION

2.1 Overview

This Reference Manual describes *Advanced Control Library* for Freescale 56F800E family of Digital Signal Controllers. This library contains optimized functions for 56F800E family of controllers. The library is supplied in a binary form, which is unique by its simplicity to integrate with user application. For correct functionality of Motor Control Library, General Functions Library (GFLIB) must be installed and included in the application project.

2.2 Supported Compilers

Advanced Control Library (ACLIB) is written in assembly language with C-callable interface. The library was built and tested using following compiler:

1. CodeWarrior™ Development Studio for Freescale™ DSC56800/E Digital Signal Controllers, version 8.3

The library is delivered in library module *56F800E_ACLIB.lib* and is intended for use in small data memory model projects. The interfaces to the algorithms included in this library have been combined into a single public interface include file, *aclib.h*. This was done to simplify the number of files required for inclusion by application programs. Refer to the specific algorithm sections of this document for details on the software Application Programming Interface (API), defined and functionality provided for the individual algorithms.

2.3 Installation

If the user wants to fully use this library, the CodeWarrior tools should be installed prior to the *Advanced Control Library*. In case that *Advanced Control Library* tool is installed while CodeWarrior is not present, users can only browse the installed software package, but will not be able to build, download, and run the code. The installation itself consists of copying the required files to the destination hard drive, checking the presence of CodeWarrior, and creating the shortcut under the Start->Programs menu.

Each *Advanced Control Library* release is installed in its own new folder named *56800E_ACLIB_rX.X*, where X.X denotes the actual release number. This way of library installation allows users to maintain older releases and projects and gives them a free choice to select the active library release.

To start the installation process, follow the following steps:

1. Execute `56800E_FSLESL_RXX.exe`
2. Follow the *Advanced Control Library* software installation instructions on your screen.

2.4 Library integration

The *Advanced Control Library* is added into a new CodeWarrior project by taking the following steps:

1. Create a new empty project.
2. Create *ACLIB* group in your new open project. Note that this step is not mandatory, it is mentioned here just for the purpose of maintaining file consistency in the CodeWarrior project window. In the CodeWarrior menu, choose Project > Create Group..., type *ACLIB* into the dialog window that pops up, and click <OK>.
3. Refer the *56800E_ACLIB.lib* file in the project window. This can be achieved by dragging the library file from the proper library subfolder and dropping it into the *ACLIB* group in the CodeWarrior project window. This step will automatically add the *ACLIB* path into the project access paths, such as the user can take advantage of the library functions to achieve flawless project compilation and linking.
4. It is similar with the reference file *aclib.h*. This file can be dragged from the proper library subfolder and dropped into the *ACLIB* group in the CodeWarrior project window.
5. The following program line must be added into the user-application source code in order to use the library functions.

```
#include "aclib.h"
```

6. Since *Advanced Control Library* is not stand-alone, *General Functions Library (GFLIB)* and *Motor Control Library (MCLIB)* must be installed and included in the application project prior to *ACLIB*.
7. Create *GFLIB* group in your new open project. Note that this step is not mandatory, it is mentioned here just for the purpose of maintaining file consistency in the CodeWarrior project window. In the CodeWarrior menu, choose Project > Create Group..., type *GFLIB* into the dialog window that pops up, and click <OK>.
8. Refer the *56800E_GFLIB.lib* file in the project window. This can be done by dragging the library file from the proper library subfolder and dropping it into the *GFLIB* group in the CodeWarrior project window. This step will automatically add the *GFLIB* path into the project access paths, such as the user can take advantage of the library functions to achieve flawless project compilation and linking.

9. It is similar with the reference file *gflib.h* in the project window. This can be achieved by dragging the file from the proper library subfolder and dropping it into the *GFLIB* group in the CodeWarrior project window.
10. Create *MCLIB* group in your new open project. Note that this step is not mandatory, it is mentioned here just for the purpose of maintaining file consistency in the CodeWarrior project window. In the CodeWarrior menu, choose Project > Create Group..., type *MCLIB* into the dialog window that pops up, and click <OK>.
11. Refer the *56800E_ACLIB.lib* file in the project window. This can be done by dragging the library file from the proper library subfolder and dropping it into the *MCLIB* group in the CodeWarrior project window. This step will automatically add the *MCLIB* path into the project access paths, such as the user can take the advantage of the library functions to achieve flawless project compilation and linking.
12. It is similar with the reference file *mclib.h* in the project window. This can be achieved by dragging the file from proper library subfolder and dropping it into the *MCLIB* group in the CodeWarrior project window.
13. The following program lines must be added into the user application source code in order to use the library functions.

```
#include "gflib.h"  
#include "mclib.h"
```

2.5 API definition

The description of each function described in this Advanced Control Library user reference manual consists of a number of subsections:

Synopsis

This subsection gives the header files that should be included within a source file that references the function or macro. It also shows an appropriate declaration for the function or for a function that can be substituted by a macro. This declaration is not included in your program; only the header file(s) should be included.

Prototype

This subsection shows the original function prototype declaration with all its arguments.

Arguments

This optional subsection describes input arguments to a function or macro.

Description

This subsection is a description of the function or macro. It explains algorithms being used by functions or macros.

Return

This optional subsection describes the return value (if any) of the function or macro.

Range Issues

This optional subsection specifies the ranges of input variables.

Special Issues

This optional subsection specifies special assumptions that are mandatory for correct function calculation; for example saturation, rounding, and so on.

Implementation

This optional subsection specifies, whether a call of the function generates a library function call or a macro expansion.

This subsection also consists of one or more examples of the use of the function. The examples are often fragments of code (not completed programs) for illustration purposes.

See Also

This optional subsection provides a list of related functions or macros.

Performance

This section specifies the actual requirements of the function or macro in terms of required code memory, data memory, and number of clock cycles to execute. If the clock cycles have two numbers for instance 21/22, then the number 21 is measured on the MCF56F80xx core and the number 22 is measured on the MCF56F83xx core.

2.6 Data Types

The 16-bit DSC core supports four types of two's-complement data formats:

- Signed integer
- Unsigned integer
- Signed fractional
- Unsigned fractional

The Signed and unsigned integer data types are useful for general-purpose computation; they are familiar with the microprocessor and microcontroller programmers. Fractional data types allow powerful numeric and digital-signal-processing algorithms to be implemented.

2.6.1 Signed Integer (SI)

This format is used for processing data as integers. In this format, the N-bit operand is represented using the N.0 format (N integer bits). The signed integer numbers lie in the following range:

$$-2^{[N-1]} \leq SI \leq [2^{[N-1]} - 1] \quad \text{Eqn. 2-1}$$

This data format is available for bytes, words, and longs. The most negative, signed word that can be represented is $-32,768$ (\$8000), and the most negative, signed long word is $-2,147,483,648$ (\$80000000).

The most positive, signed word is $32,767$ (\$7FFF), and the most positive signed long word is $2,147,483,647$ (\$7FFFFFFF).

2.6.2 Unsigned Integer (UI)

The unsigned integer numbers are positive only, and they have nearly twice the magnitude of a signed number of the same size. The unsigned integer numbers lie in the following range:

$$0 \leq UI \leq [2^{[N-1]} - 1] \quad \text{Eqn. 2-2}$$

The binary word is interpreted as having a binary point immediately to the right of the integer's least significant bit. This data format is available for bytes, words, and long words. The most positive, 16-bit, unsigned integer is $65,535$ (\$FFFF), and the most positive, 32-bit, unsigned integer is $4,294,967,295$ (\$FFFFFFFF). The smallest unsigned integer number is zero (\$0000), regardless of size.

2.6.3 Signed Fractional (SF)

In this format, the N-bit operand is represented using the 1.[N-1] format (one sign bit, N-1 fractional bits). The signed fractional numbers lie in the following range:

$$-1,0 \leq SF \leq 1,0 - 2^{-[N-1]} \quad \text{Eqn. 2-3}$$

This data format is available for words and long words. For both word and long-word signed fractions, the most negative number that can be represented is -1.0 ; its internal representation is \$8000 (word) or \$80000000 (long word). The most positive word is \$7FFF ($1.0 - 2^{-15}$); its most positive long word is \$7FFFFFFF ($1.0 - 2^{-31}$).

2.6.4 Unsigned Fractional (UF)

The unsigned fractional numbers can be positive only, and they have nearly twice the magnitude of a signed number with the same number of bits. The unsigned fractional numbers lie in the following range:

$$0,0 \leq UF \leq 2,0 - 2^{-[N-1]} \quad \text{Eqn. 2-4}$$

The binary word is interpreted as having a binary point after the MSB. This data format is available for words and longs. The most positive, 16-bit, unsigned

number is \$FFFF, or $\{1.0 + (1.0 - 2^{-[N-1]})\} = 1.99997$. The smallest unsigned fractional number is zero (\$0000).

2.7 User Common Types

Table 2-1. User-Defined Typedefs in *MCF51_types.h*

Mnemonics	Size — bits	Description
Word8	8	To represent 8-bit signed variable/value.
UWord8	8	To represent 16-bit unsigned variable/value.
Word16	16	To represent 16-bit signed variable/value.
UWord16	16	To represent 16-bit unsigned variable/value.
Word32	32	To represent 32-bit signed variable/value.
UWord32	32	To represent 16-bit unsigned variable/value.
Int8	8	To represent 8-bit signed variable/value.
UInt8	8	To represent 16-bit unsigned variable/value.
Int16	16	To represent 16-bit signed variable/value.
UInt16	16	To represent 16-bit unsigned variable/value.
Int32	32	To represent 32-bit signed variable/value.
UInt32	32	To represent 16-bit unsigned variable/value.
Frac16	16	To represent 16-bit signed variable/value.
Frac32	32	To represent 32-bit signed variable/value.
NULL	constant	Represents NULL pointer.
bool	16	Boolean variable.
false	constant	Represents false value.
true	constant	Represents true value.
FRAC16()	macro	Transforms float value from <-1, 1) range into fractional representation <-32768, 32767>.
FRAC32()	macro	Transforms float value from <-1, 1) range into fractional representation <-2147483648, 2147483648>.

Table 2-2. User-Defined Typedefs in *mclib_types.h*

Name	Structure Members	Description
MCLIB_3_COOR_SYST_T	Frac16 f16A Frac16 f16B Frac16 f16C	three phase system
MCLIB_2_COOR_SYST_T	Frac16 f16A Frac16 f16B	two phase system
MCLIB_2_COOR_SYST_ALPHA_BETA_T	Frac16 f16Alpha Frac16 f16Beta	two phase system — alpha/beta
MCLIB_2_COOR_SYST_D_Q_T	Frac16 f16D Frac16 f16Q	two phase system — generic DQ
MCLIB_ANGLE_T	Frac16 f16Sin Frac16 f16Cos	two phase system — sine and cosine components

2.8 Special Issues

All functions in Advanced Control Library are implemented without storing any of the volatile register used by the respective routine. Only non-volatile registers (for list of volatile/non-volatile registers refer to the compiler manual) are saved by pushing the registers on the stack. Therefore if the particular registers initialized before the library function call are to be used after the function call, it is necessary to save them manually.

3.1 API Summary

Table 3-1. API functions summary

Name	Arguments	Output	Description
ACLIB_PMSMBemfObsrvAB	<i>MCLIB_2_COOR_SYST_ALPHA_BETA_T</i> <i>*pudtlalbet</i> <i>MCLIB_2_COOR_SYST_ALPHA_BETA_T</i> <i>*pudtUalbet</i> <i>Frac16 f16Speed</i> <i>ACLIB_BEMF_OBSRV_AB_T * const</i> <i>pudtCtrl</i>	void	This function calculates the algorithms of finding permanent-magnet axis.
ACLIB_PMSMBemfObsrv12AB	<i>MCLIB_2_COOR_SYST_ALPHA_BETA_T</i> <i>*pudtlalbet</i> <i>MCLIB_2_COOR_SYST_ALPHA_BETA_T</i> <i>*pudtUalbet</i> <i>Frac16 f16Speed</i> <i>ACLIB_BEMF_OBSRV_AB_T * const</i> <i>pudtCtrl</i>	void	This function calculates the algorithms of finding permanent-magnet axis. This version uses the quicker 12-bit precision sine calculation therefore it is quicker but with reduced precision in comparison to ACLIB_PMSMBemfObsrvAB .
ACLIB_AngleTrackObsrv	<i>MCLIB_ANGLE_T *pudtSinCos</i> <i>ACLIB_ANGLE_TRACK_OBSRV_T *</i> <i>const pudtCtrl</i>	void	This function calculates the algorithm of velocity and position-tracking observer.
ACLIB_AngleTrackObsrv12	<i>MCLIB_ANGLE_T *pudtSinCos</i> <i>ACLIB_ANGLE_TRACK_OBSRV_T *</i> <i>const pudtCtrl</i>	void	This function calculates the algorithm of velocity and position-tracking observer. This version uses the quicker 12-bit precision sine calculation therefore it is quicker but with reduced precision in comparison to ACLIB_AngleTrackObsrv .
ACLIB_TrackObsrv	<i>Frac16 f16ThetaErr</i> <i>ACLIB_TRACK_OBSRV_T * const pudtCtrl</i>	void	This function calculates the tracking observer for determination angular speed and position of input error functional signal.
ACLIB_PMSMBemfObsrvDQ	<i>MCLIB_2_COOR_SYST_D_Q_T *pudtlldq</i> <i>MCLIB_2_COOR_SYST_D_Q_T</i> <i>*pudtUdq,Frac16 f16Speed</i> <i>ACLIB_BEMF_OBSRV_DQ_T * const</i> <i>pudtCtrl</i>	void	The function calculates the algorithm of back electro-motive force observer in rotating reference frame.
ACLIB_Integrator	<i>Frac16 f16X</i> <i>ACLIB_INTEGRATOR_T *pudtIntg</i>	void	The function calculates the algorithm of numerical integrator of its input.



3.2 ACLIB_PMSMBemfObsrvAB

The function calculates the algorithm of back electro-motive force observer in stationary reference frame.

3.2.1 Synopsis

```
#include "aclib.h"
void ACLIB_PMSMBemfObsrvAB(
MCLIB_2_COOR_SYST_ALPHA_BETA_T *pudtCurrentAlphaBeta,
MCLIB_2_COOR_SYST_ALPHA_BETA_T *pudtVoltageAlphaBeta,
Frac16 f16Speed,
ACLIB_BEMF_OBSRV_AB_T *pudtCtrl)
```

3.2.2 Prototype

```
asm void ACLIB_PMSMBemfObsrvABFasm(MCLIB_2_COOR_SYST_ALPHA_BETA_T
*pudtCurrentAlphaBeta, MCLIB_2_COOR_SYST_ALPHA_BETA_T
*pudtVoltageAlphaBeta, Frac16 f16Speed, ACLIB_BEMF_OBSRV_AB_T *pudtCtrl)
```

3.2.3 Arguments

Table 3-2. Function Arguments

Name	In/Out	Format	Valid Range	Description
*pudtCurrentAlphaBeta	in	MCLIB_2_COOR_SYST_ALPHA_BETA_T	N/A	Input signal of alpha/beta current components.
*pudtVoltageAlphaBeta	in	MCLIB_2_COOR_SYST_ALPHA_BETA_T	N/A	Input signal of alpha/beta voltage components.
f16Speed	in	SF16	\$8000... \$7FFF	Fraction value of electrical speed.
*pudtCtrl	in/out	ACLIB_BEMF_OBSRV_AB_T	N/A	Pointer to an observer structure, which contains coefficients.

Table 3-3. User Types

Typedef	Name	Format	Valid Range	Description
ACLIB_BEMF_OBSRV_AB_T	udtEObsrv.f32Alpha	SF32	0x80000000... 0x7FFFFFFF	Estimated bakc-EMF voltage in beta axis.
	udtEObsrv.f32Beta	SF32	0x80000000... 0x7FFFFFFF	Estimated bakc-EMF voltage in beta axis.
	udtIObsrv.f32Alpha	SF32	0x80000000... 0x7FFFFFFF	Estimated current in alpha axis.
	udtIObsrv.f32Beta	SF32	0x80000000... 0x7FFFFFFF	Estimated current in beta axis.
	udtCtrl.f32IAlpha_1	SF32	0x80000000... 0x7FFFFFFF	State variable in alpha part of the observer; integral part at step k-1.
	udtCtrl.f32IBeta_1	SF32	0x80000000... 0x7FFFFFFF	State variable in beta part of the observer; integral part at step k-1.
	udtCtrl.f16PropScaled	SF16	\$8000... \$7FFF	Observer proportional gain.
	udtCtrl.i16PropShift	SI16	-F..F	Observer proportional gain shift.
	udtCtrl.f16IntegScaled	SF16	\$8000... \$7FFF	Observer integral gain.
	udtCtrl.i16IntegShift	SI16	-F..F	Observer integral gain shift.
	mcUnityVctr.f16Sin	MCLIB_ANGLE_T	\$8000... \$7FFF	Sine component of estimated unity vector.
	mcUnityVctr.f16Cos	MCLIB_ANGLE_T	\$8000... \$7FFF	Cosine component of estimated unity vector.
	f16IScaled	SF16	\$8000... \$7FFF	Scaling coefficient for current I_{FRAC} .
	f16UScaled	SF16	\$8000... \$7FFF	Scaling coefficient for voltage U_{FRAC} .
	f16WIScaled	SF16	\$8000... \$7FFF	Scaling coefficient for angular speed W_{FRAC} .
f16EScaled	SF16	\$8000... \$7FFF	Scaling coefficient for back-EMF E_{FRAC} .	

3.2.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.2.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_MCLIB library
- 56800E_GFLIB library
- ACLIB_AngleTrackObsrvAsm.h
- aclib.h

3.2.6 Description

This back-emf observer is realized within stationary α, β reference frame.

$$\begin{bmatrix} u_\alpha \\ u_\beta \end{bmatrix} = R_s \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} + \begin{bmatrix} sL_D & \Delta L \omega_r \\ -\Delta L_D \omega_r & sL_D \end{bmatrix} \cdot \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} + (\Delta L \cdot (\omega_e i_D - i_Q') + k_e \omega_r) \cdot \begin{bmatrix} -\sin(\theta_r) \\ \cos(\theta_r) \end{bmatrix} \quad \text{Eqn. 3-1}$$

where

- R_s stator resistance
- L_d, L_q - D-axis and Q-axis inductance
- k_e back-EMF constant
- ω_e rotor angular speed
- u_α, u_β components of stator voltage vector
- i_α, i_β components of stator current vector
- s operator of derivative
- i_q' first derivative of i_q current
- $\Delta L = (L_D - L_Q)$ motor saliency

This extended back-EMF model includes both position information from the conventionally defined back-EMF and the stator inductance as well. This allows to extract the rotor position and velocity information by estimating the extended back EMF only.

Both alpha and beta-axis consists of the stator current observer based on RL motor circuit which requires motor parameters.

The current observer is fed by the sum of the actual applied motor voltage, cross-coupled rotational term, which corresponds to the motor saliency ($L_d - L_q$) and compensator corrective output. The observer provides back-EMF signals as disturbance because back-EMF is not included in observer model.

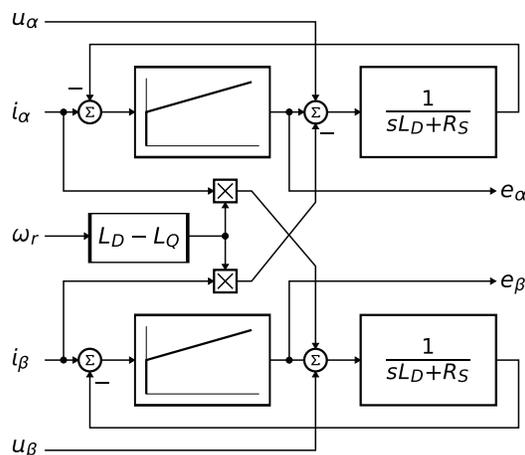


Figure 3-1. Block diagram of back-emf observer

It is obvious that the accuracy of the back EMF estimates is determined by the correctness of used motor parameters (R, L) by fidelity of the reference stator voltage and by quality of compensator such as bandwidth, phase lag and so on.

Appropriate dynamic behavior of the back emf observer is achieved by placement of the poles of the stator current observer characteristic polynomial. This general method is based on matching the coefficients of the characteristic polynomial with the coefficients of the general second-order system.

$$\hat{E}_{\alpha\beta}(s) = -E_{\alpha\beta}(s) \cdot \left[\frac{F_c(s)}{sL_D + R_S + F_c(s)} \right] \quad \text{Eqn. 3-2}$$

Back emf observer is Luenberger type observer with motor model which is realized in fixed point arithmetic transformed using backward Euler transformation.

$$\mathbf{i}_{FRAC}(k) = U_{FRAC} \cdot \mathbf{u}_{FRAC}(k) + E_{FRAC} \cdot \mathbf{e}_{FRAC}(k) - W I_{FRAC} \cdot \omega_{eFRAC}(k) \mathbf{i}_{FRAC}(k) + I_{FRAC} \cdot \mathbf{i}_{FRAC}(k-1) \quad \text{Eqn. 3-3}$$

where

- $\mathbf{i}_{FRAC}(k) = [i_{\omega}, i_{\beta}]$ is fractional representation of stator current vector
- $\mathbf{u}_{FRAC}(k) = [u_{\omega}, u_{\beta}]$ is fractional representation of stator voltage vector
- $\mathbf{e}_{FRAC}(k) = [e_{\omega}, e_{\beta}]$ is fractional representation of stator back-emf voltage vector
- $\mathbf{i}'_{FRAC}(k) = [i_{\beta}, -i_{\alpha}]$ is fractional representation of complementary stator current vector
- $\omega_{FRAC}(k)$ is fractional representation of angular speed

Scaling coefficients relating to maximal values are expressed as

$$U_{FRAC} = \frac{\Delta T_S}{L_d + \Delta T_S R_S} \cdot \frac{U_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-4}$$

$$E_{FRAC} = \frac{\Delta T_S}{L_d + \Delta T_S R_S} \cdot \frac{E_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-5}$$

$$WI_{FRAC} = \frac{\Delta L \cdot \Delta T_S}{L_d + \Delta T_S R_S} \cdot \Omega_{MAX} \quad \text{Eqn. 3-6}$$

$$I_{FRAC} = \frac{L_d}{L_d + \Delta T_S R_S} \quad \text{Eqn. 3-7}$$

where

- ΔT_S sampling time in [sec]
- I_{MAX} maximal peak current in [A]
- E_{MAX} maximal peak back-emf voltage in [V]
- U_{MAX} maximal peak stator voltage in [V]
- Ω_{MAX} maximal angular speed in [rad/sec]

If a Luenberger type stator current observer is properly designed in the stationary reference frame, the back-EMF can be estimated as a disturbance, produced by the observer controller. This is only valid however if the back-EMF term is not included in the observer model. The observer is actually a closed loop current observer so it acts as a state filter for the back-EMF term.

The estimate of extended EMF term can be derived from [Equation 3-2](#) as follows:

$$\frac{\hat{E}_{\alpha\beta}(s)}{E_{\alpha\beta}(s)} = \frac{sK_P + K_I}{s^2 L_D + sR_S + sK_P + K_I} \quad \text{Eqn. 3-8}$$

The observer controller can be designed by comparing the closed loop characteristic polynomial with that of a standard second order system as:

$$s^2 + \frac{K_P + R_S}{L_D} \cdot s + \frac{K_I}{L_D} = s^2 + 2\xi\omega_0 s + \omega_0^2 \quad \text{Eqn. 3-9}$$

where

- ω_0 is the natural frequency of the closed loop system (loop bandwidth)
- ξ is the loop attenuation.

3.2.7 Returns

The function returns a unity vector representing the estimated value of sine and cosine values of back emf.

3.2.8 Implementation

Example 3-1. Implementation Code

```

#include "gflib.h"
#include "mclib.h"
#include "aclib.h"

MCLIB_2_COOR_SYST_ALPHA_BETA_T    mcI, mcU;
ACLIB_BEMF_OBSRV_AB_T             acBemfObsrv;

void Isr(void);

void main (void)
{
acBemfObsrv.udtEObsrv.f32Alpha      = FRAC32(0.0);
acBemfObsrv.udtEObsrv.f32Beta      = FRAC32(0.0);
acBemfObsrv.udtIObsrv.f32Alpha     = FRAC32(0.0);
acBemfObsrv.udtIObsrv.f32Beta      = FRAC32(0.0);
acBemfObsrv.udtCtrl.f32IAlpha_1    = FRAC32(0.0);
acBemfObsrv.udtCtrl.f32IBeta_1     = FRAC32(0.0);
acBemfObsrv.udtCtrl.f16PropScaled   = BEMFOBSRV_AB_PROP_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16PropShift    = BEMFOBSRV_AB_PROP_GAIN_SHIFT;
acBemfObsrv.udtCtrl.f16IntegScaled  = BEMFOBSRV_AB_INTEG_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16IntegShift   = BEMFOBSRV_AB_INTEG_GAIN_SHIFT;
acBemfObsrv.f16IScaled              = BEMFOBSRV_AB_I_SCALED;
acBemfObsrv.f16UScaled              = BEMFOBSRV_AB_U_SCALED;
acBemfObsrv.f16EScaled              = BEMFOBSRV_AB_E_SCALED;
acBemfObsrv.f16WIScaled             = BEMFOBSRV_AB_WI_SCALED;
}

/* Periodical function or interrupt */
void ISR(void)
{
ACLIB_PMSMBemfObsrvAB (&mcI, &mcU, f16Speed, &acBemfObsrv);
}

```

3.2.9 Performance

Table 3-4. Performance of ACLIB_PMSMBemfObsrvAB function

Code Size (words)	168 + 65 (GFLIB_SqrtPoly)	
Data Size (words)	0 + 34 (GFLIB_SqrtPoly)	
Execution Clock	Min	257/238 cycles
	Max	325/301 cycles

3.3 ACLIB_PMSMBemfObsrv12AB

The function calculates the algorithm of back electro-motive force observer in stationary reference frame. This version uses the quicker 12-bit precision sine calculation therefore it is quicker but with reduced precision in comparison to [ACLIB_PMSMBemfObsrvAB](#).

3.3.1 Synopsis

```
#include "aclib.h"
void ACLIB_PMSMBemfObsrv12AB(
MCLIB_2_COOR_SYST_ALPHA_BETA_T *pudtCurrentAlphaBeta,
MCLIB_2_COOR_SYST_ALPHA_BETA_T *pudtVoltageAlphaBeta,
Frac16 f16Speed,
ACLIB_BEMF_OBSRV_AB_T *pudtCtrl)
```

3.3.2 Prototype

```
asm void ACLIB_PMSMBemfObsrv12ABFasm(MCLIB_2_COOR_SYST_ALPHA_BETA_T
*pudtCurrentAlphaBeta, MCLIB_2_COOR_SYST_ALPHA_BETA_T
*pudtVoltageAlphaBeta, Frac16 f16Speed, ACLIB_BEMF_OBSRV_AB_T *pudtCtrl)
```

3.3.3 Arguments

Table 3-5. Function Arguments

Name	In/Out	Format	Valid Range	Description
*pudtCurrentAlphaBeta	in	MCLIB_2_COOR_SYST_ALPHA_BETA_T	N/A	Input signal of alpha/beta current components.
*pudtVoltageAlphaBeta	in	MCLIB_2_COOR_SYST_ALPHA_BETA_T	N/A	Input signal of alpha/beta voltage components.
f16Speed	in	SF16	\$8000... \$7FFF	Fraction value of electrical speed.
*pudtCtrl	in/out	ACLIB_BEMF_OBSRV_AB_T	N/A	Pointer to an observer structure, which contains coefficients.

Table 3-6. User Types

Typedef	Name	Format	Valid Range	Description
ACLIB_BEMF_OBSRV_AB_T	udtEObsrv.f32Alpha	SF32	0x80000000... 0x7FFFFFFF	Estimated back-EMF voltage in beta axis.
	udtEObsrv.f32Beta	SF32	0x80000000... 0x7FFFFFFF	Estimated back-EMF voltage in beta axis.
	udtIObsrv.f32Alpha	SF32	0x80000000... 0x7FFFFFFF	Estimated current in alpha axis.
	udtIObsrv.f32Beta	SF32	0x80000000... 0x7FFFFFFF	Estimated current in beta axis.
	udtCtrl.f32IAlpha_1	SF32	0x80000000... 0x7FFFFFFF	State variable in alpha part of the observer; integral part at step k-1.
	udtCtrl.f32IBeta_1	SF32	0x80000000... 0x7FFFFFFF	State variable in beta part of the observer; integral part at step k-1.
	udtCtrl.f16PropScaled	SF16	\$8000... \$7FFF	Observer proportional gain.
	udtCtrl.i16PropShift	SI16	-F..F	Observer proportional gain shift.
	udtCtrl.f16IntegScaled	SF16	\$8000... \$7FFF	Observer integral gain.
	udtCtrl.i16IntegShift	SI16	-F..F	Observer integral gain shift.
	mcUnityVctr.f16Sin	MCLIB_ANGLE_T	\$8000... \$7FFF	Sine component of estimated unity vector.
	mcUnityVctr.f16Cos	MCLIB_ANGLE_T	\$8000... \$7FFF	Cosine component of estimated unity vector.
	f16IScaled	SF16	\$8000... \$7FFF	Scaling coefficient for current I_{FRAC} .
	f16UScaled	SF16	\$8000... \$7FFF	Scaling coefficient for voltage U_{FRAC} .
	f16WIScaled	SF16	\$8000... \$7FFF	Scaling coefficient for angular speed W_{FRAC} .
f16EScaled	SF16	\$8000... \$7FFF	Scaling coefficient for back-EMF E_{FRAC} .	

3.3.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.3.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_MCLIB library
- 56800E_GFLIB library
- ACLIB_AngleTrackObsrvAsm.h
- aclib.h

3.3.6 Description

This back-emf observer is realized within stationary α, β reference frame.

$$\begin{bmatrix} u_\alpha \\ u_\beta \end{bmatrix} = R_s \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} + \begin{bmatrix} sL_D & \Delta L \omega_r \\ -\Delta L \omega_r & sL_D \end{bmatrix} \cdot \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} + (\Delta L \cdot (\omega_e i_D - i_Q') + k_e \omega_r) \cdot \begin{bmatrix} -\sin(\theta_r) \\ \cos(\theta_r) \end{bmatrix} \quad \text{Eqn. 3-10}$$

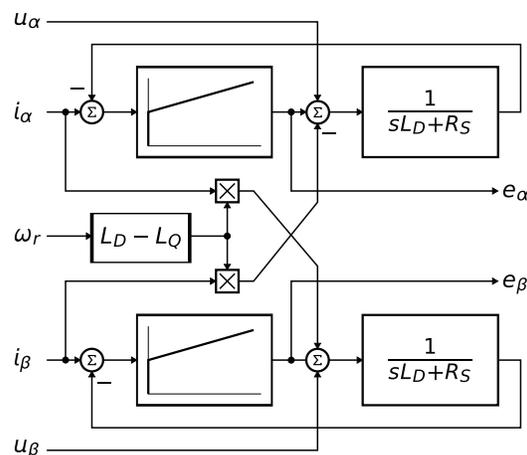
where

- R_s stator resistance
- L_d, L_q - D-axis and Q-axis inductance
- k_e back-EMF constant
- ω_e rotor angular speed
- u_α, u_β components of stator voltage vector
- i_α, i_β components of stator current vector
- s operator of derivative
- i_q' first derivative of i_q current
- $\Delta L = (L_D - L_Q)$ motor saliency

This extended back-EMF model includes both position information from the conventionally defined back-EMF and the stator inductance as well. This allows to extract the rotor position and velocity information by estimating the extended back EMF only.

Both alpha and beta-axis consists of the stator current observer based on RL motor circuit which requires motor parameters.

The current observer is fed by the sum of the actual applied motor voltage, cross-coupled rotational term, which corresponds to the motor saliency ($L_d - L_q$) and compensator corrective output. The observer provides back-EMF signals as disturbance because back-EMF is not included in observer model.


Figure 3-2. Block diagram of back-emf observer

It is obvious that the accuracy of the back EMF estimates is determined by the correctness of used motor parameters (R, L) by fidelity of the reference stator voltage and by quality of compensator such as bandwidth, phase lag and so on.

Appropriate dynamic behavior of the back emf observer is achieved by placement of the poles of the stator current observer characteristic polynomial. This general method is based on matching the coefficients of the characteristic polynomial with the coefficients of the general second-order system.

$$\hat{E}_{\alpha\beta}(s) = -E_{\alpha\beta}(s) \cdot \left[\frac{F_c(s)}{sL_D + R_S + F_c(s)} \right] \quad \text{Eqn. 3-11}$$

Back emf observer is Luenberger type observer with motor model which is realized in fixed point arithmetic transformed using backward Euler transformation.

$$\mathbf{i}_{FRAC}(k) = U_{FRAC} \cdot \mathbf{u}_{FRAC}(k) + E_{FRAC} \cdot \mathbf{e}_{FRAC}(k) - WI_{FRAC} \cdot \boldsymbol{\omega}_{eFRAC}(k) \mathbf{i}_{FRAC}(k) + I_{FRAC} \cdot \mathbf{i}_{FRAC}(k-1) \quad \text{Eqn. 3-12}$$

where

- $\mathbf{i}_{FRAC}(k) = [i_\omega, i_\beta]$ is fractional representation of stator current vector
- $\mathbf{u}_{FRAC}(k) = [u_\omega, u_\beta]$ is fractional representation of stator voltage vector
- $\mathbf{e}_{FRAC}(k) = [e_\omega, e_\beta]$ is fractional representation of stator back-emf voltage vector
- $\mathbf{i}'_{FRAC}(k) = [i_\beta, -i_\alpha]$ is fractional representation of complementary stator current vector
- $\boldsymbol{\omega}_{FRAC}(k)$ is fractional representation of angular speed

Scaling coefficients relating to maximal values are expressed as

$$U_{FRAC} = \frac{\Delta T_S}{L_d + \Delta T_S R_S} \cdot \frac{U_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-13}$$

$$E_{FRAC} = \frac{\Delta T_S}{L_d + \Delta T_S R_S} \cdot \frac{E_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-14}$$

$$WI_{FRAC} = \frac{\Delta L \cdot \Delta T_S}{L_d + \Delta T_S R_S} \cdot \Omega_{MAX} \quad \text{Eqn. 3-15}$$

$$I_{FRAC} = \frac{L_d}{L_d + \Delta T_S R_S} \quad \text{Eqn. 3-16}$$

where

- ΔT_S sampling time in [sec]
- I_{MAX} maximal peak current in [A]
- E_{MAX} maximal peak back-emf voltage in [V]
- U_{MAX} maximal peak stator voltage in [V]
- Ω_{MAX} maximal angular speed in [rad/sec]

If a Luenberger type stator current observer is properly designed in the stationary reference frame, the back-EMF can be estimated as a disturbance, produced by the observer controller. This is only valid however if the back-EMF term is not included in the observer model. The observer is actually a closed loop current observer so it acts as a state filter for the back-EMF term.

The estimate of extended EMF term can be derived from [Equation 3-11](#) as follows:

$$\frac{\hat{E}_{\alpha\beta}(s)}{E_{\alpha\beta}(s)} = \frac{sK_P + K_I}{s^2 L_D + sR_S + sK_P + K_I} \quad \text{Eqn. 3-17}$$

The observer controller can be designed by comparing the closed loop characteristic polynomial with that of a standard second order system as:

$$s^2 + \frac{K_P + R_S}{L_D} \cdot s + \frac{K_I}{L_D} = s^2 + 2\xi\omega_0 s + \omega_0^2 \quad \text{Eqn. 3-18}$$

where

- ω_0 is the natural frequency of the closed loop system (loop bandwidth)
- ξ is the loop attenuation.

3.3.7 Returns

The function returns a unity vector representing the estimated value of sine and cosine values of back emf.

3.3.8 Implementation

Example 3-2. Implementation Code

```

#include "gflib.h"
#include "mclib.h"
#include "aclib.h"

MCLIB_2_COOR_SYST_ALPHA_BETA_T    mcI, mcU;
ACLIB_BEMF_OBSRV_AB_T             acBemfObsrv;

void Isr(void);

void main (void)
{
acBemfObsrv.udtEObsrv.f32Alpha      = FRAC32(0.0);
acBemfObsrv.udtEObsrv.f32Beta      = FRAC32(0.0);
acBemfObsrv.udtIObsrv.f32Alpha     = FRAC32(0.0);
acBemfObsrv.udtIObsrv.f32Beta      = FRAC32(0.0);
acBemfObsrv.udtCtrl.f32IAlpha_1    = FRAC32(0.0);
acBemfObsrv.udtCtrl.f32IBeta_1     = FRAC32(0.0);
acBemfObsrv.udtCtrl.f16PropScaled   = BEMFOBSRV_AB_PROP_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16PropShift    = BEMFOBSRV_AB_PROP_GAIN_SHIFT;
acBemfObsrv.udtCtrl.f16IntegScaled  = BEMFOBSRV_AB_INTEG_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16IntegShift   = BEMFOBSRV_AB_INTEG_GAIN_SHIFT;
acBemfObsrv.f16IScaled              = BEMFOBSRV_AB_I_SCALED;
acBemfObsrv.f16UScaled              = BEMFOBSRV_AB_U_SCALED;
acBemfObsrv.f16EScaled              = BEMFOBSRV_AB_E_SCALED;
acBemfObsrv.f16WIScaled             = BEMFOBSRV_AB_WI_SCALED;
}

/* Periodical function or interrupt */
void ISR(void)
{
ACLIB_PMSMBemfObsrv12AB(&mcI, &mcU, f16Speed, &acBemfObsrv);
}

```

3.3.9 Performance

Table 3-7. Performance of ACLIB_PMSMBemfObsrv12AB function

Code Size (words)	167 + 28 (GFLIB_SqrtIter)	
Data Size (words)	0	
Execution Clock	Min	301/282 cycles
	Max	301/282 cycles

3.4 ACLIB_AngleTrackObsrv

The function calculates angle tracking observer for determination angular speed and position of input functional signal.

3.4.1 Synopsis

```
#include"aclib.h"
Frac16 ACLIB_AngleTrackObsrv(MCLIB_ANGLE_T *pudtSinCos,
ACLIB_ANGLE_TRACK_OBSRV_T * pudtCtrl)
```

3.4.2 Prototype

```
asm Frac16 ACLIB_AngleTrackObsrvFAsm(MCLIB_ANGLE_T *pudtSinCos,
ACLIB_ANGLE_TRACK_OBSRV_T * const pudtCtrl)
```

3.4.3 Arguments

Table 3-8. Function Arguments

Name	In/Out	Format	Valid Range	Description
*pudtSinCos	in	MCLIB_ANGLE_T	N/A	input signal of sine, cosine components to be filtered
*pudtCtrl	in/out	ACLIB_ANGLE_TRACK_OBSRV_T	N/A	pointer to an angle tracking observer structure ACLIB_ANGLE_TRACK_OBSRV_T, which contains algorithm coefficients

Table 3-9. User type definitions

Typedef	Name	In/Out	Format	Valid Range	Description
MCLIB_ANGLE_T	f16Sin	In	SF16	\$8000... \$7FFF	sine component to be estimated
	f16Cos	In	SF16	\$8000... \$7FFF	cosine component to be estimated
ACLIB_ANGLE_TRACK_OBSRV_T	f32Speed	in/out	SF32	0x80000000... 0x7FFFFFFF	Estimated speed as output of the first numerical integrator
	f32A2	in/out	SF32	0x80000000... 0x7FFFFFFF	Output of the second numerical integrator
	f16Theta	in/out	SF16	\$8000... \$7FFF	Estimated position
	f16SinEstim	in	SF16	\$8000... \$7FFF	Sine signal to be estimated
	f16CosEstim	in	SF16	\$8000... \$7FFF	Cosine signal to be estimated
	f16K1Scaled	in	SF16	\$8000... \$7FFF	K1 coefficient scaled to fractional range
	i16K1Shift	in	SI16	-F...F	Scaling shift
	f16K2Scaled	in	SF16	\$8000... \$7FFF	K2 coefficient scaled to fractional range
	i16K2Shift	in	SI16	-F...F	Scaling shift
	f16A2Scaled	in	SF16	\$8000... \$7FFF	Scaling coefficient due to numerical integration
	i16A2Shift	in	SI16	-F...F	Scaling shift

3.4.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.4.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_MCLIB library
- 56800E_GFLIB library
- ACLIB_AngleTrackObsrvAsm.h
- aclib.h

3.4.6 Description

This function calculates the angle tracking observer algorithm. It is recommended to call this function at every sampling period. It requires two input arguments as sine and cosine samples. The practical implementation of the angle tracking observer algorithm is described below.

The angle tracking observer compares values of the input signals $\sin(\theta)$, $\cos(\theta)$ with their corresponding estimations $\sin(\hat{\theta})$, $\cos(\hat{\theta})$. As in any common closed-loop systems, the intent is to minimize observer error towards zero value. The observer error is given here by subtraction of the estimated resolver rotor angle $\hat{\theta}$ from the actual rotor angle θ (see **Figure 3-3**).

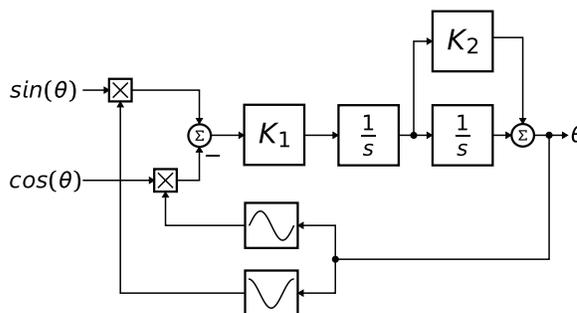


Figure 3-3. Block scheme of the angle tracking observer

Note that mathematical expression of observer error is known as the formula of the difference of two angles:

$$\sin(\theta - \hat{\theta}) = \sin(\theta) \cdot \cos(\hat{\theta}) - \cos(\theta) \cdot \sin(\hat{\theta}) \quad \text{Eqn. 3-19}$$

In the case of minimal deviations out of the estimated rotor angle compared to the actual rotor angle, the observer error may be expressed in the following form

$$\sin(\theta - \hat{\theta}) \approx \theta - \hat{\theta} \quad \text{Eqn. 3-20}$$

The primary benefit of the angle tracking observer utilization, in comparison with the trigonometric method, is its smoothing capability. This filtering is achieved by the integrator and proportional and integral controller, which are connected in series and closed by a unit feedback loop. This block diagram nicely tracks actual rotor angle and speed and continuously updates their estimations. The angle tracking observer transfer function is expressed as follows

$$\frac{\hat{\theta}(s)}{\theta(s)} = \frac{K_1(1 + K_2s)}{s^2 + K_1K_2s + K_1} \quad \text{Eqn. 3-21}$$

The characteristic polynomial of the angle tracking observer corresponds to the denominator of transfer function

$$s^2 + K_1K_2s + K_1 \quad \text{Eqn. 3-22}$$

Appropriate dynamic behavior of the angle tracking observer is achieved by placement of the poles of the characteristic polynomial. This general method is based on matching the coefficients of the characteristic polynomial with the coefficients of the general second-order system.

The analog integrators in Figure 3-1, marked as $1/s$ are replaced by an equivalent of the discrete-time integrator using the backward Euler integration method. The discrete-time block diagram of the angle tracking observer is shown in **Figure 3-4**.

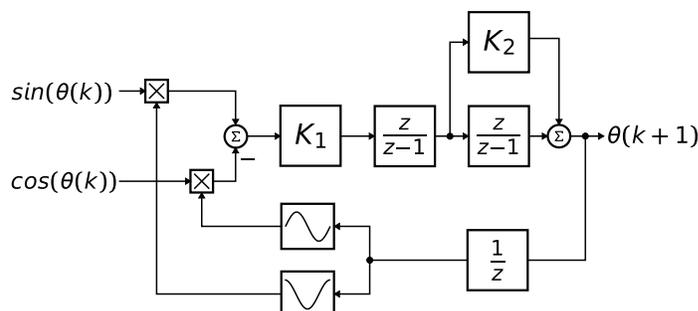


Figure 3-4. Block scheme of discrete-time tracking observer

The essential equations for implementation of the angle tracking observer, according to block scheme in **Figure 3-4**, are as follows:

$$e(k) = \sin(k) \cdot \cos(\hat{\theta}(k)) - \cos(k) \cdot \sin(\hat{\theta}(k)) \quad \text{Eqn. 3-23}$$

$$\omega(k) = \omega(k-1) + K_1 \cdot \Delta T_S \cdot e(k) \quad \text{Eqn. 3-24}$$

$$a_2(k) = a_2(k-1) + \Delta T_S \cdot \omega(k) \quad \text{Eqn. 3-25}$$

$$\theta(k) = K_2 \cdot \omega(k) + a_2(k) \quad \text{Eqn. 3-26}$$

In equations [Equation 3-23](#) to [Equation 3-26](#), there are coefficients and quantities that might be greater than one (for example, the actual rotor speed $\omega(k)$) or that are too small to be precisely represented within 16-bit fractional value. Due to this fact a special transformation of equations [Equation 3-23](#) to [Equation 3-26](#) have to be carried out in order to be successfully implemented using fractional arithmetic.

$$K_{1FRAC} = \Delta T_S \cdot \frac{K_1}{\Omega_{MAX}} \quad \text{Eqn. 3-27}$$

$$K_{2FRAC} = K_2 \cdot \frac{\Omega_{MAX}}{\Theta_{MAX}} \quad \text{Eqn. 3-28}$$

$$A_{2FRAC} = \Delta T_S \cdot \frac{\Omega_{MAX}}{\Theta_{MAX}} \quad \text{Eqn. 3-29}$$

where the variables of the angle tracking observer are

- $e(k)$ is observer error in step k ,
- ΔT_s is the sampling period [s],
- $\omega(k)$ is the actual rotor speed [rad/s] in step k ,
- $\theta(k)$ is the actual rotor angle [rad] in step k ,
- $a_2(k)$ is the actual rotor angle [rad] without scaled addition of speed in step k .

The scaled coefficients which are suitable for implementation on the DSP core are as follows:

$$f16K1Scaled = K_{1FRAC} \cdot 2^{-i16K1Shift} \quad \text{Eqn. 3-30}$$

$$f16K2Scaled = K_{2FRAC} \cdot 2^{-i16K2Shift} \quad \text{Eqn. 3-31}$$

$$f16A2Scaled = A_{2FRAC} \cdot 2^{-i16A2Shift} \quad \text{Eqn. 3-32}$$

3.4.7 Return

The function returns an estimation of the actual rotor angle as 16 bit fractional value.

3.4.8 Range Issues

The function works with the 16-bit signed fractional values in the range $<-1,1)$.

3.4.9 Special Issues

The [ACLIB_AngleTrackObsrv](#) function requires the saturation mode to be turned on.

3.4.10 Implementation

Example 3-3. implementation Code

```
#include "gflib.h"
#include "mclib.h"
#include "aclib.h"

MCLIB_ANGLE_T          mcAngle;
ACLIB_ANGLE_TRACK_OBSRV_T acAngleTrackObsrv;
Frac16                 f16PositionOut;

void main (void)
{
    acAngleTrckObsrv.f32Speed          = FRAC32(0);
```

Advanced Control Library, Rev. 2

```

acAngleTrckObsrv.f32A2           = FRAC32(0);
acAngleTrckObsrv.f16Theta        = FRAC16(0);
acAngleTrckObsrv.f16SinEstim     = FRAC16(0);
acAngleTrckObsrv.f16CosEstim     = FRAC16(0);
acAngleTrckObsrv.f16K1Scaled     = ANGLETRACKOBSRV_K1_SCALED;
acAngleTrckObsrv.i16K1Shift      = ANGLETRACKOBSRV_K1_SHIFT;
acAngleTrckObsrv.f16K2Scaled     = ANGLETRACKOBSRV_K2_SCALED;
acAngleTrckObsrv.i16K2Shift      = ANGLETRACKOBSRV_K2_SHIFT;
acAngleTrckObsrv.f16A2Scaled     = ANGLETRACKOBSRV_A2_SCALED;
acAngleTrckObsrv.i16A2Shift      = ANGLETRACKOBSRV_A2_SHIFT;

}

/* Periodical function or interrupt */
void ISR(void)
{
f16PositionOut = ACLIB_AngleTrackObsrv(&mcAngle, &acAngleTrackObsrv);
}

```

3.4.11 Performance

Table 3-10. Performance of [ACLIB_AngleTrackObsrv](#) function

Code Size (words)	78 + 38 (GFLIB_SinTlr)	
Data Size (words)	0 + 10 (GFLIB_SinTlr)	
Execution Clock	Min	196/183 cycles
	Max	196/183 cycles

3.5 ACLIB_AngleTrackObsrv12

The function calculates angle tracking observer for determination angular speed and position of input functional signal. This version uses the quicker 12-bit precision sine calculation therefore it is quicker but with reduced precision in comparison to [ACLIB_AngleTrackObsrv](#).

3.5.1 Synopsis

```
#include"aclib.h"
Frac16 ACLIB_AngleTrackObsrv12(MCLIB_ANGLE_T *pudtSinCos,
ACLIB_ANGLE_TRACK_OBSRV_T * pudtCtrl)
```

3.5.2 Prototype

```
asm Frac16 ACLIB_AngleTrackObsrv12Fasm(MCLIB_ANGLE_T *pudtSinCos,
ACLIB_ANGLE_TRACK_OBSRV_T * const pudtCtrl)
```

3.5.3 Arguments

Table 3-11. Function Arguments

Name	In/Out	Format	Valid Range	Description
*pudtSinCos	in	MCLIB_ANGLE_T	N/A	input signal of sine, cosine components to be filtered
*pudtCtrl	in/out	ACLIB_ANGLE_TRACK_OBSRV_T	N/A	pointer to an angle tracking observer structure ACLIB_ANGLE_TRACK_OBSRV_T, which contains algorithm coefficients

Table 3-12. User type definitions

Typedef	Name	In/Out	Format	Valid Range	Description
MCLIB_ANGLE_T	f16Sin	In	SF16	\$8000... \$7FFF	sine component to be estimated
	f16Cos	In	SF16	\$8000... \$7FFF	cosine component to be estimated
ACLIB_ANGLE_TRACK_OBSRV_T	f32Speed	in/out	SF32	0x80000000... 0x7FFFFFFF	Estimated speed as output of the first numerical integrator
	f32A2	in/out	SF32	0x80000000... 0x7FFFFFFF	Output of the second numerical integrator
	f16Theta	in/out	SF16	\$8000... \$7FFF	Estimated position
	f16SinEstim	in	SF16	\$8000... \$7FFF	Sine signal to be estimated
	f16CosEstim	in	SF16	\$8000... \$7FFF	Cosine signal to be estimated
	f16K1Scaled	in	SF16	\$8000... \$7FFF	K1 coefficient scaled to fractional range
	i16K1Shift	in	SI16	-F...F	Scaling shift
	f16K2Scaled	in	SF16	\$8000... \$7FFF	K2 coefficient scaled to fractional range
	i16K2Shift	in	SI16	-F...F	Scaling shift
	f16A2Scaled	in	SF16	\$8000... \$7FFF	Scaling coefficient due to numerical integration
	i16A2Shift	in	SI16	-F...F	Scaling shift

3.5.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.5.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_MCLIB library
- 56800E_GFLIB library
- ACLIB_AngleTrackObsrvAsm.h
- aclib.h

3.5.6 Description

This function calculates the angle tracking observer algorithm. It is recommended to call this function at every sampling period. It requires two input arguments as sine and cosine samples. The practical implementation of the angle tracking observer algorithm is described below.

The angle tracking observer compares values of the input signals $\sin(\theta)$, $\cos(\theta)$ with their corresponding estimations $\sin(\hat{\theta})$, $\cos(\hat{\theta})$. As in any common closed-loop systems, the intent is to minimize observer error towards zero value. The observer error is given here by subtraction of the estimated resolver rotor angle $\hat{\theta}$ from the actual rotor angle θ (see **Figure 3-5**).

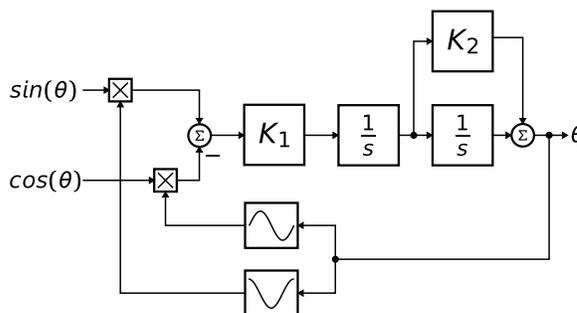


Figure 3-5. Block scheme of the angle tracking observer

Note that mathematical expression of observer error is known as the formula of the difference of two angles:

$$\sin(\theta - \hat{\theta}) = \sin(\theta) \cdot \cos(\hat{\theta}) - \cos(\theta) \cdot \sin(\hat{\theta}) \tag{Eqn. 3-33}$$

In the case of minimal deviations out of the estimated rotor angle compared to the actual rotor angle, the observer error may be expressed in the following form

$$\sin(\theta - \hat{\theta}) \approx \theta - \hat{\theta} \tag{Eqn. 3-34}$$

The primary benefit of the angle tracking observer utilization, in comparison with the trigonometric method, is its smoothing capability. This filtering is achieved by the integrator and proportional and integral controller, which are connected in series and closed by a unit feedback loop. This block diagram nicely tracks actual rotor angle and speed and continuously updates their estimations. The angle tracking observer transfer function is expressed as follows

$$\frac{\hat{\theta}(s)}{\theta(s)} = \frac{K_1(1 + K_2s)}{s^2 + K_1K_2s + K_1} \tag{Eqn. 3-35}$$

The characteristic polynomial of the angle tracking observer corresponds to the denominator of transfer function

$$s^2 + K_1K_2s + K_1 \tag{Eqn. 3-36}$$

Appropriate dynamic behavior of the angle tracking observer is achieved by placement of the poles of the characteristic polynomial. This general method is based on matching the coefficients of the characteristic polynomial with the coefficients of the general second-order system.

The analog integrators in Figure 3-1, marked as $1/s$ are replaced by an equivalent of the discrete-time integrator using the backward Euler integration method. The discrete-time block diagram of the angle tracking observer is shown in **Figure 3-6**.

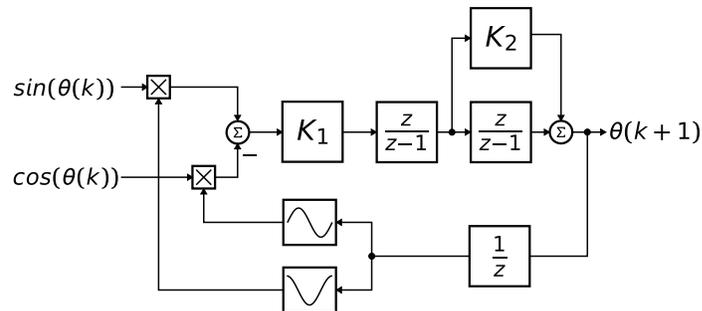


Figure 3-6. Block scheme of discrete-time tracking observer

The essential equations for implementation of the angle tracking observer, according to block scheme in **Figure 3-6**, are as follows:

$$e(k) = \sin(k) \cdot \cos(\hat{\theta}(k)) - \cos(k) \cdot \sin(\hat{\theta}(k)) \tag{Eqn. 3-37}$$

$$\omega(k) = \omega(k-1) + K_1 \cdot \Delta T_S \cdot e(k) \tag{Eqn. 3-38}$$

$$a_2(k) = a_2(k-1) + \Delta T_S \cdot \omega(k) \tag{Eqn. 3-39}$$

$$\theta(k) = K_2 \cdot \omega(k) + a_2(k) \tag{Eqn. 3-40}$$

In equations Equation 3-37 to Equation 3-40, there are coefficients and quantities that might be greater than one (for example, the actual rotor speed $\omega(k)$) or that are too small to be precisely represented within 16-bit fractional value. Due to this fact a special transformation of equations Equation 3-37 to Equation 3-40 have to be carried out in order to be successfully implemented using fractional arithmetic.

$$K_{1FRAC} = \Delta T_S \cdot \frac{K_1}{\Omega_{MAX}} \tag{Eqn. 3-41}$$

$$K_{2FRAC} = K_2 \cdot \frac{\Omega_{MAX}}{\Theta_{MAX}} \tag{Eqn. 3-42}$$

$$A_{2FRAC} = \Delta T_S \cdot \frac{\Omega_{MAX}}{\Theta_{MAX}} \tag{Eqn. 3-43}$$

where the variables of the angle tracking observer are

- $e(k)$ is observer error in step k ,
- ΔT_s is the sampling period [s],
- $\omega(k)$ is the actual rotor speed [rad/s] in step k ,
- $\theta(k)$ is the actual rotor angle [rad] in step k ,
- $a_2(k)$ is the actual rotor angle [rad] without scaled addition of speed in step k .

The scaled coefficients which are suitable for implementation on the DSP core are as follows:

$$f16K1Scaled = K_{1FRAC} \cdot 2^{-i16K1Shift} \quad \text{Eqn. 3-44}$$

$$f16K2Scaled = K_{2FRAC} \cdot 2^{-i16K2Shift} \quad \text{Eqn. 3-45}$$

$$f16A2Scaled = A_{2FRAC} \cdot 2^{-i16A2Shift} \quad \text{Eqn. 3-46}$$

3.5.7 Return

The function returns an estimation of the actual rotor angle as 16 bit fractional value.

3.5.8 Range Issues

The function works with the 16-bit signed fractional values in the range $<-1,1)$.

3.5.9 Special Issues

The [ACLIB_AngleTrackObsrv12](#) function requires the saturation mode to be turned on.

3.5.10 Implementation

Example 3-4. implementation Code

```
#include "gflib.h"
#include "mclib.h"
#include "aclib.h"

MCLIB_ANGLE_T          mcAngle;
ACLIB_ANGLE_TRACK_OBSRV_T acAngleTrackObsrv;
Frac16                 f16PositionOut;

void main (void)
{
    acAngleTrckObsrv.f32Speed          = FRAC32(0);
```

Advanced Control Library, Rev. 2

```

acAngleTrckObsrv.f32A2           = FRAC32(0);
acAngleTrckObsrv.f16Theta       = FRAC16(0);
acAngleTrckObsrv.f16SinEstim    = FRAC16(0);
acAngleTrckObsrv.f16CosEstim    = FRAC16(0);
acAngleTrckObsrv.f16K1Scaled    = ANGLETRACKOBSRV_K1_SCALED;
acAngleTrckObsrv.i16K1Shift     = ANGLETRACKOBSRV_K1_SHIFT;
acAngleTrckObsrv.f16K2Scaled    = ANGLETRACKOBSRV_K2_SCALED;
acAngleTrckObsrv.i16K2Shift     = ANGLETRACKOBSRV_K2_SHIFT;
acAngleTrckObsrv.f16A2Scaled    = ANGLETRACKOBSRV_A2_SCALED;
acAngleTrckObsrv.i16A2Shift     = ANGLETRACKOBSRV_A2_SHIFT;

}

/* Periodical function or interrupt */
void ISR(void)
{
f16PositionOut = ACLIB_AngleTrackObsrv12(&mcAngle, &acAngleTrackObsrv);
}

```

3.5.11 Performance

Table 3-13. Performance of [ACLIB_AngleTrackObsrv12](#) function

Code Size (words)	78 + 25 (GFLIB_Sin12Tlr)	
Data Size (words)	0 + 5 (GFLIB_Sin12Tlr)	
Execution Clock	Min	168/156 cycles
	Max	168/156 cycles

3.6 ACLIB_PMSMBemfObsrvDQ

The function calculates the algorithm of back electro-motive force observer in rotating reference frame.

3.6.1 Synopsis

```
#include "aclib.h"
void ACLIB_PMSMBemfObsrvDQ(MCLIB_2_COOR_SYST_D_Q_T *pudtCurrentDQ,
MCLIB_2_COOR_SYST_D_Q_T *pudtVoltageDQ, Frac16 f16Speed,
ACLIB_BEMF_OBSRV_DQ_T *pudtCtrl)
```

3.6.2 Prototype

```
asm void ACLIB_PMSMBemfObsrvDQFAsm(MCLIB_2_COOR_SYST_D_Q_T
*pudtCurrentDQ, MCLIB_2_COOR_SYST_D_Q_T *pudtVoltageDQ, Frac16 f16Speed,
ACLIB_BEMF_OBSRV_DQ_T *pudtCtrl)
```

3.6.3 Arguments

Table 3-14. Function Arguments

Name	In/Out	Format	Valid Range	Description
*pudtCurrentDQ	in	MCLIB_2_COOR_SYST_D_Q_T	N/A	pointer to structure which contain input signal of d/q current components
*pudtVoltageDQ	in	MCLIB_2_COOR_SYST_D_Q_T	N/A	pointer to structure which contain input signal of d/q voltage components
f16Frac	in/out	SF16	N/A	Fraction value of electrical speed.
*pudtCtrl	in/out	ACLIB_BEMF_OBSRV_DQ_T	N/A	Pointer to an observer structure, which contains coefficients.

Table 3-15. User Types

Typedef	Name	Format	Valid Range	Description
ACLIB_BEMF_OBSRV_AB_T	udtEObsrv.f32D	SF32	0x80000000... 0x7FFFFFFF	Estimated back-EMF voltage in d-axis
	udtEObsrv.f32Q	SF32	0x80000000... 0x7FFFFFFF	Estimated back-EMF voltage in q-axis
	udtIObsrv.f32D	SF32	0x80000000... 0x7FFFFFFF	Estimated current in d-axis
	udtIObsrv.f32Q	SF32	0x80000000... 0x7FFFFFFF	Estimated current in q-axis
	udtCtrl.f32ID_1	SF32	0x80000000... 0x7FFFFFFF	State variable in alpha part of the observer; integral part at step k-1;
	udtCtrl.f32IQ_1	SF32	0x80000000... 0x7FFFFFFF	State variable in beta part of the observer; integral part at step k-1;
	udtCtrl.f16PropScaled	SF16	\$8000... \$7FFF	Observer proportional gain
	udtCtrl.i16PropShift	SI16	-F...F	Observer proportional gain shift
	udtCtrl.f16IntegScaled	SF16	\$8000... \$7FFF	Observer integral gain
	udtCtrl.i16IntegShift	SI16	-F...F	Observer integral gain shift
	f16Error	SF16	\$8000... \$7FFF	Estimated phase error between real d/q frame system and estimated d/q reference system
	f16IScaled	SF16	\$8000... \$7FFF	Scaling coefficient for current I_{FRAC}
	f16UScaled	SF16	\$8000... \$7FFF	Scaling coefficient for voltage U_{FRAC}
	f16WIScaled	SF16	\$8000... \$7FFF	Scaling coefficient for angular speed WI_{FRAC}
f16EScaled	SF16	\$8000... \$7FFF	Scaling coefficient for back-emf E_{FRAC}	

3.6.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.6.5 Dependencies

List of all dependent files:

- 56800E_types.h

Advanced Control Library, Rev. 2

- 56800E_MCLIB library
- 56800E_GFLIB library
- ACLIB_PMSMBemfObsrvDQAsm.h
- aclub.h

3.6.6 Description

The estimation method for the rotor position and angular speed is based on the motor mathematical model of interior PMSM motor with an extended electro-motive force function which is realized in estimated quasi synchronous reference frame $\gamma\delta$ as depicted on [Figure 3-7](#).

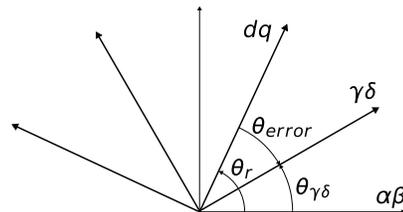


Figure 3-7. Estimated $\gamma\delta$ and real rotor dq synchronous reference frames

The back-EMF observer detects the generated motor voltages induced by the permanent magnets. A tracking observer uses the back-EMF signals to calculate the position and speed of the rotor. The transformed model is then derived as follows:

$$\begin{bmatrix} u_\gamma \\ u_\delta \end{bmatrix} = \begin{bmatrix} R_s + sL_D & -\omega_r L_Q \\ \omega_r L_Q & R_s + sL_D \end{bmatrix} \begin{bmatrix} i_\gamma \\ i_\delta \end{bmatrix} + (\Delta L \cdot (\omega_e i_D - i_Q') + k_e \omega_e) \cdot \begin{bmatrix} -\sin(\theta_{error}) \\ \cos(\theta_{error}) \end{bmatrix} \quad \text{Eqn. 3-47}$$

where

- R_s stator resistance
- L_D, L_Q - D-axis and Q-axis inductance
- k_e back-EMF constant
- ω_e angular electrical speed
- u_D, u_Q stator voltages
- i_D, i_Q stator currents
- s operator of derivative
- i_q' - first derivative of i_q current

Block diagram of the observer in the estimated reference frame is shown on [Figure 3-8](#). The observer compensator is substituted by a standard PI controller. As can be noted from [Figure 3-8](#), observer model and hence also PI controller gains in both axis are identical to each other.

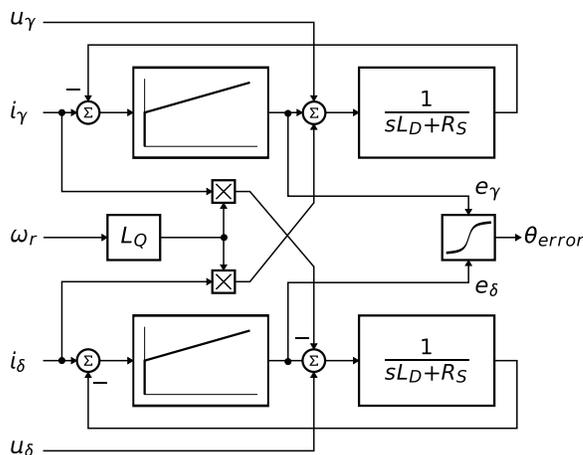


Figure 3-8. Block diagram of proposed Luenberger type stator current observer acting as state filter for back-EMF.

The position estimation can now be performed by extracting the θ_{error} term from the model and adjusting the position of the estimated reference frame such as to achieve $\theta_{error} = 0$. Because the θ_{error} term is only included in the saliency-based EMF component of both u_γ, u_δ axis voltage equations, the Luenberger based disturbance observer is designed to observe these voltage components u_γ, u_δ . The position displacement information θ_{error} is then obtained from estimated back-EMFs as follows:

$$\theta_{error} = \text{atan}\left(\frac{-u_\gamma}{u_\delta}\right) \quad \text{Eqn. 3-48}$$

The estimated position $\hat{\theta}_r$ can be obtained by driving the position of the estimated reference frame such as to achieve zero displacement $\theta_{error} = 0$. The phase locked loop mechanism can be adopted, where the loop compensator ensures correct tracking of the actual rotor flux position by keeping the error signal θ_{error} to be zeroed, $\theta_{error} = 0$.

A perfect match between the actual and estimated motor model parameters is assumed, and then back-EMF transfer function is simplified as follows

$$\hat{E}_{\alpha\beta}(s) = -E_{\alpha\beta}(s) \cdot \left[\frac{F_c(s)}{sL_D + R_S + F_c(s)} \right] \quad \text{Eqn. 3-49}$$

Appropriate dynamic behavior of the back emf observer is achieved by placement of the poles of the stator current observer characteristic polynomial. This general method is based on matching the coefficients of the characteristic polynomial with the coefficients of the general second-order system.

Back emf observer is Luenberger type observer with motor model which is realized in fixed point arithmetic transformed using backward Euler transformation.

$$\mathbf{i}_{FRAC}(k) = U_{FRAC} \cdot \mathbf{u}_{FRAC}(k) + E_{FRAC} \cdot \mathbf{e}_{FRAC}(k) + WI_{FRAC} \cdot \omega_{eFRAC}(k) \cdot \mathbf{i}'_{FRAC}(k) + I_{FRAC} \cdot \mathbf{i}_{FRAC}(k-1) \quad \text{Eqn. 3-50}$$

where

- $\mathbf{i}_{FRAC}(k) = [i_\gamma, i_\delta]$ is fractional representation of stator current vector
- $\mathbf{u}_{FRAC}(k) = [u_\gamma, u_\delta]$ is fractional representation of stator voltage vector
- $\mathbf{e}_{FRAC}(k) = [e_\gamma, e_\delta]$ is fractional representation of stator back-emf voltage vector
- $\mathbf{i}'_{FRAC}(k) = [i_\delta, -i_\gamma]$ is fractional representation of complementary stator current vector
- $\omega_{FRAC}(k)$ is fractional representation of angular speed

Scaling coefficients relating to maximal values are expressed as

$$U_{FRAC} = \frac{\Delta T_S}{L_D + \Delta T_S R_S} \cdot \frac{U_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-51}$$

$$E_{FRAC} = \frac{\Delta T_S}{L_D + \Delta T_S R_S} \cdot \frac{E_{MAX}}{I_{MAX}} \quad \text{Eqn. 3-52}$$

$$WI_{FRAC} = \frac{L_Q \cdot \Delta T_S}{L_D + \Delta T_S R_S} \cdot \Omega_{MAX} \quad \text{Eqn. 3-53}$$

$$I_{FRAC} = \frac{L_D}{L_D + \Delta T_S R_S} \quad \text{Eqn. 3-54}$$

where

- ΔT_S sampling time in [sec]
- I_{MAX} maximal peak current in [A]
- E_{MAX} maximal peak back-emf voltage in [V]
- U_{MAX} maximal peak stator voltage in [V]
- Ω_{MAX} maximal angular speed in [rad/sec]

If a Luenberger type stator current observer is properly designed in the stationary reference frame, the back-EMF can be estimated as a disturbance, produced by the observer controller. This is only valid however if the back-EMF term is not included in the observer model. The observer is actually a closed loop current observer so it acts as a state filter for the back-EMF term.

The estimate of extended EMF term can be derived from [Equation 3-49](#) as follows:

$$\frac{\hat{E}_{\gamma\delta}(s)}{E_{\gamma\delta}(s)} = \frac{sK_p + K_I}{s^2 L_D + sR_S + sK_p + K_I} \quad \text{Eqn. 3-55}$$

The observer controller can be designed by comparing the closed loop characteristic polynomial with that of a standard second order system as:

$$s^2 + \frac{K_P + R_S}{L_D} \cdot s + \frac{K_I}{L_D} = s^2 + 2\xi\omega_0s + \omega_0^2 \quad \text{Eqn. 3-56}$$

where

- ω_0 is the natural frequency of the closed loop system (loop bandwidth)
- ξ is the loop attenuation.

3.6.7 Returns

The function returns a phase error between real rotating reference frame and estimated one.

3.6.8 Range Issues

The function works with the 16-bit signed fractional values in the range <-1,1).

3.6.9 Special Issues

The [ACLIB_PMSMBemfObsrvDQ](#) function requires the saturation mode to be turned on.

3.6.10 Implementation

Example 3-5. Implementation Code

```
#include "gflib.h"
#include "mclib.h"
#include "aclib.h"

MCLIB_2_COOR_SYST_D_Q_T    mcIdq, mcUdq;
ACLIB_BEMF_OBSRV_DQ_T     acBemfObsrv;
Frac16                    f16Speed;

void main (void)
{

acBemfObsrv.udtIObsrv.f32D = FRAC32(0.0);
acBemfObsrv.udtIObsrv.f32Q = FRAC32(0.0);
acBemfObsrv.udtEObsrv.f32D = FRAC32(0.0);
acBemfObsrv.udtEObsrv.f32Q = FRAC32(0.0);
acBemfObsrv.udtCtrl.f32ID_1= FRAC32(0.0);
acBemfObsrv.udtCtrl.f32IQ_1= FRAC32(0.0);
acBemfObsrv.udtCtrl.f16PropScaled= BEMFOBSRV_DQ_PROP_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16PropShift= BEMFOBSRV_DQ_PROP_GAIN_SHIFT;
acBemfObsrv.udtCtrl.f16IntegScaled= BEMFOBSRV_DQ_INTEG_GAIN_SCALED;
acBemfObsrv.udtCtrl.i16IntegShift  = BEMFOBSRV_DQ_INTEG_GAIN_SHIFT;
acBemfObsrv.f16IScaled            = BEMFOBSRV_DQ_I_SCALED;
```

```

acBemfObsrv.f16UScaled          = BEMFOBSRV_DQ_U_SCALED;
acBemfObsrv.f16EScaled          = BEMFOBSRV_DQ_E_SCALED;
acBemfObsrv.f16WIScaled         = BEMFOBSRV_DQ_WI_SCALED;

}

/* Periodical function or interrupt */
void ISR(void)
{
ACLIB_PMSMBemfObsrvDQ(&mcIdq, &mcUdq, f16Speed, &acBemfObsrv);
}

```

3.6.11 Performance

Table 3-16. Performance of [ACLIB_PMSMBemfObsrvDQ](#) function

Code Size (words)	145 + 100 (GFLIB_AtanYX)	
Data Size (words)	0 + 33 (GFLIB_AtanYX)	
Execution Clock	Min	225/204 cycles
	Max	301/277 cycles

3.7 ACLIB_TrackObsrv

The function calculates tracking observer for determination angular speed and position of input error functional signal.

3.7.1 Synopsis

```
#include"aclib.h"
Frac16 ACLIB_TrackObsrv(Frac16 f16Error, ACLIB_TRACK_OBSRV_T *pudtCtrl)
```

3.7.2 Prototype

```
asm Frac16 ACLIB_TrackObsrvFasm(Frac16 f16Error, ACLIB_TRACK_OBSRV_T
*pudtCtrl)
```

3.7.3 Arguments

Table 3-17. Function Arguments

Name	In/Out	Format	Valid Range	Description
f16Error	in	SF16	\$8000... \$7FFF	input signal representing phase error of system to be estimated
*pudtCtrl	in/out	ACLIB_TRACK_OBSRV_T	N/A	pointer to a racking observer structure ACLIB_TRACK_OBSRV_T, which contains algorithm coefficients

Table 3-18. User type definitions

Typedef	Name	In/Out	Format	Valid Range	Description
ACLIB_TRACK_OBSRV_T	f32Theta	in/out	SF32	0x80000000... 0x7FFFFFFF	Estimated position as output of the second numerical integrator
	f32Speed	in/out	SF32	0x80000000... 0x7FFFFFFF	Estimated speed as output of the first numerical integrator
ACLIB_TRACK_OBSRV_T	f32I_1	in/out	SF32	0x80000000... 0x7FFFFFFF	State variable in controller part of the observer; integral part at step k-1
	f16PropScale	in	SF16	\$8000... \$7FFF	Obsrever proportional gain
	i16PropShift	in	SI16	-F...F	Obsrever proportional gain shift
	f16IntegScale	in	SF16	\$8000... \$7FFF	Obsrever integral gain
	i16IntegShift	in	SI16	-F...F	Obsrever integral gain shift
	f16ThScaled	in	SF16	\$8000... \$7FFF	Scaling coefficient for output integrator of position
	i16ThShift	in	SI16	-F...F	Scaling coefficient shift for output integrator of position

3.7.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for 56800E platforms.

3.7.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_GFLIB library
- 56800E_MCLIB library
- ACLIB_TrackObsrvAsm.h
- aclib.h

3.7.6 Description

This function calculates the tracking observer algorithm where phase locked loop mechanism is adopted. It is recommended to call this function at every sampling period. It requires a single input argument as phase error. Such phase tracking observer, with standard PI controller used as the loop compensator, is depicted on [Figure 3-9](#).



Figure 3-9. Block diagram of proposed PLL scheme for position estimation

Depicted tracking observer structure has the transfer function as

$$\frac{\hat{\theta}(s)}{\theta(s)} = \frac{sK_p + K_i}{s^2 + sK_p + K_i} \quad \text{Eqn. 3-57}$$

where the controller gains K_p and K_i are calculated by comparing the characteristic polynomial of the resulting transfer function to a standard second order system polynomial.

The essential equations for implementation of the tracking observer, according to block scheme in [Figure 3-9](#), are as follows:

$$\begin{aligned} \omega(k) &= K_p \cdot e(k) + \Delta T_S \cdot K_i \cdot e(k) + I(k-1) \\ I(k) &= \Delta T_S \cdot K_i \cdot e(k) + I(k-1) \end{aligned} \quad \text{Eqn. 3-58}$$

$$\theta(k) = \theta(k-1) + \Delta T_S \cdot \omega(k) \quad \text{Eqn. 3-59}$$

In equations [Equation 3-58](#) and [Equation 3-59](#), there are coefficients and quantities that might be greater than one (for example, the actual rotor speed $\omega(k)$) or that are too small to be precisely represented within 16-bit fractional value. Due to this fact a special transformation have to be carried out in order to be successfully implemented using fractional arithmetic.

$$K_{pFRAC} = \frac{K_p}{\Omega_{MAX}} \quad \text{Eqn. 3-60}$$

$$K_{iFRAC} = \Delta T_S \cdot \frac{K_i}{\Omega_{MAX}} \quad \text{Eqn. 3-61}$$

$$T_{hFRAC} = \Delta T_S \cdot \frac{\Omega_{MAX}}{\Theta_{MAX}} \quad \text{Eqn. 3-62}$$

where the variables of the angle tracking observer are

- $e(k)$ is observer error in step k ,
- ΔT_S is the sampling period [s],
- $\omega(k)$ is the actual rotor speed [rad/s] in step k ,
- $\theta(k)$ is the actual rotor angle [rad] in step k .

The scaled coefficients which are suitable for implementation on the DSP core are as follows:

$$f16KPScaled = K_{pFRAC} \cdot 2^{-i16KPShift} \quad \text{Eqn. 3-63}$$

$$f16KIScaled = K_{iFRAC} \cdot 2^{-i16KIShift} \quad \text{Eqn. 3-64}$$

$$f16ThScaled = T_{hFRAC} \cdot 2^{-i16ThShift} \quad \text{Eqn. 3-65}$$

3.7.7 Returns

The function returns an estimation of the actual rotor angle as 16 bit fractional value.

3.7.8 Range Issues

The function works with the 16-bit signed fractional values in the range <-1,1).

3.7.9 Special Issues

The [ACLIB_TrackObsrv](#) function requires the saturation mode to be turned on. Upon completion of the function the saturation mode is set off.

3.7.10 Implementation

Example 3-6. Implementation Code

```
#include "aclib.h"

ACLIB_TRACK_OBSRV_T acTo;
Frac16          f16ThetaError;
Frac16          f16PositionEstim;

void main (void)
{
    acTo.f32Theta      = FRAC32(0.0);
    acTo.f32Speed      = FRAC32(0.0);
    acTo.f32I_1        = FRAC32(0.0);
    acTo.f16PropScale= TRACKOBSRV_PROP_GAIN_SCALED;
    acTo.i16PropShift= TRACKOBSRV_PROP_GAIN_SHIFT;
    acTo.f16IntegScale= TRACKOBSRV_INTEG_GAIN_SCALED;
    acTo.i16IntegShift= TRACKOBSRV_INTEG_GAIN_SHIFT;
    acTo.f16ThScaled= TRACKOBSRV_TH_SCALED;
    acTo.i16ThShift   = TRACKOBSRV_TH_SHIFT;
}

/* Periodical function or interrupt */
void ISR(void)
{
    f16PositionEstim = ACLIB_TrackObsrv(f16ThetaError, &acTo);
}

```

3.7.11 See Also

3.7.12 Performance

Table 3-19. Performance of [ACLIB_TrackObsrv](#) function

Code Size (words)	50	
Data Size (words)	0	
Execution Clock	Min	73/66 cycles
	Max	73/66 cycles

3.8 ACLIB_Integrator

The function calculates the algorithm of numerical integrator of its input.

3.8.1 Synopsis

```
#include"aclib.h"
Frac16 ACLIB_Integrator(Frac16 f16Xinp, ACLIB_INTEGRATOR_T *pudtIntg)
```

3.8.2 Prototype

```
asm Frac16 ACLIB_IntegratorFAsm(Frac16 f16Xinp, ACLIB_INTEGRATOR_T
*pudtIntg)
```

3.8.3 Arguments

Table 3-20. Function Arguments

Name	In/Out	Format	Valid Range	Description
f16Xinp	in	SF16	\$8000... \$7FFF	input variable to be integrated
*pudtIntg	in/out	ACLIB_INTEGRATOR_T	N/A	pointer to structure which contain parameters of numerical integrator

Table 3-21. User type definitions

Typedef	Name	In/Out	Format	Valid Range	Description
ACLIB_INTEGRATOR_T	f32Integ_1	in	SF32	0x80000000... 0x7FFFFFFF	state variable of integration in step k-1
	f16IntegScaled	in	SF16	\$8000... \$7FFF	scaling coefficient of integrator gain
	i16IntegShift	in	SI16	-F...F	integrator gain shift

3.8.4 Availability

This library module is available in the C-callable interface assembly formats.

This library module is targeted for DSC 56800E platforms.

3.8.5 Dependencies

List of all dependent files:

- 56800E_types.h
- 56800E_MCLIB library

- 56800E_GFLIB library
- ACLIB_IntegratorAsm.h
- aclib.h

3.8.6 Description

Numerical integration is the approximate computation of an integral using numerical techniques. The integrator is approximated by the backward Euler method, also known as backward rectangular or right - hand approximation as follows.

$$I(k) = \Delta T_S \cdot in(k) + I(k-1) \quad \text{Eqn. 3-66}$$

where the variables of the angle tracking observer are

- $in(k)$ is integrator input in step k ,
- ΔT_S is the sampling period [s],
- $I(k)$ is the integrator value in step k .

The integrator coefficient might be greater than one or that is too small to be precisely represented within 16-bit fractional value. Due to this fact a special transformation have to be carried out in order to be successfully implemented using fractional arithmetic.

$$I_{FRAC} = \Delta T_S \cdot \frac{IN_{MAX}}{OUT_{MAX}} \quad \text{Eqn. 3-67}$$

The scaled coefficient which is suitable for implementation on the DSP core is follows:

$$f16IScaled = I_{FRAC} \cdot 2^{-i16IShift} \quad \text{Eqn. 3-68}$$

3.8.7 Returns

3.8.8 The integrated value. Range Issues

The function works with the 16-bit signed fractional values in the range <-1,1).

3.8.9 Special Issues

The [ACLIB_Integrator](#) function requires the saturation mode to be turned on if the output variable is required to be limited otherwise output variable is naturally wrapped around.

3.8.10 Returns

The function returns an integrated value of its input variable.

3.8.11 Implementation

Example 3-7. Implementation Code

```

#include "aclib.h"

ACLIB_INTEGRATOR_T      acIntegrator;
Frac16                  f16X;
Frac16                  f16Intg;

void main (void)
{
    acIntegrator.f32I_1 = FRAC32(0.0);
    acIntegrator.f16IntegScaled = INTEG_GAIN_SCALED;
    acIntegrator.i16IntegShift = INTEG_GAIN_SHIFT;
}

/* Periodical function or interrupt */
void ISR(void)
{
    f16Intg = ACLIB_Integrator(f16X, &acIntegrator);
}
    
```

3.8.12 Performance

Table 3-22. Performance of [ACLIB_Integrator](#) function

Code Size (words)	18	
Data Size (words)	0	
Execution Clock	Min	35/33 cycles
	Max	35/33 cycles



How to Reach Us:

Home Page:

www.freescale.com

E-mail:

support@freescale.com

USA/Europe or Locations Not Listed:

Freescale Semiconductor
Technical Information Center, CH370
1300 N. Alma School Road
Chandler, Arizona 85224
+1-800-521-6274 or +1-480-768-2130
support@freescale.com

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
support@freescale.com

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd.
Technical Information Center
2 Dai King Street
Tai Po Industrial Estate
Tai Po, N.T., Hong Kong
+800 2666 8080
support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center
P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or 303-675-2140
Fax: 303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

RoHS-compliant and/or Pb-free versions of Freescale products have the functionality and electrical characteristics of their non-RoHS-compliant and/or non-Pb-free counterparts. For further information, see <http://www.freescale.com> or contact your Freescale sales representative.

For information on Freescale's Environmental Products program, go to <http://www.freescale.com/epp>.

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2006. All rights reserved.

