Occupancy Sensor Node Reference Design Board
User's Guide

1. Introduction

This manual describes the NXP reference design board Occupancy Sensor Node. The Occupancy Sensor Node circuit board provides a diverse reference design with all necessary I/O connections to use as a self-contained board or for connection to an external application.

The Occupancy Sensor Node board is built around the NXP MKW24D512. The MKW24D512 wireless MCU is a 2.4 GHz Industrial, Scientific, and Medical (ISM) single-chip device intended for the IEEE® Std. 802.15.4, including Thread®, ZigBee Pro, ZigBee RF4CE, and IPv6/6loWPAN protocols.

The Occupancy Sensor Node board contains the MKW24D512 transceiver that works in conjunction with a software stack to implement an IEEE Std. 802.15.4 platform solution.

1.1. Audience

This manual is intended for system designers and system engineers.
2. Board overview and description

The Occupancy Sensor Node board is based on the NXP MKW24D512 transceiver (MKW24); it incorporates a complete low-power IEEE Std. 802.15.4, 2.4 GHz radio frequency transceiver. The Occupancy Sensor Node platform contains the MKW24D512 device with 32 MHz reference oscillator crystal, RF circuitry including antenna, a 6-axis sensor with integrated linear accelerometer and magnetometer, an EEPROM, an occupancy sensor and supporting circuitry in a reduced form factor board. The board is a standalone and supports applications development with the NXP IEEE Std. 802.15.4 protocol stack.

2.1. Board features

The Occupancy Sensor Node reference design board contains the MKW24D512 device and is used to demonstrate and evaluate the available features of the MKW24D512 in a reduced form factor board with a specific application taking advantage of the MKW24D512 low-power capabilities, USB module and NXP Combo sensor. It is powered by a Lithium-ion Rechargeable Cell Battery or via USB.

Figure 1 shows the Occupancy Sensor Node reference design board.

The Occupancy Sensor Node reference design board includes the following features:
- The NXP low-power Kinetis MKW24D512 transceiver
- Full IEEE Std. 802.15.4 compliant wireless node; ZigBee and Thread capable
Reference design area with small footprint, low-cost RF node
  - RF circuitry includes a Balun to convert the differential I/O pin of the MKW24D512 transceiver to single-ended for on-board signal routing
  - Low off-chip component count
  - Programmable output power from -35 dBm to +8 dBm
  - Receiver sensitivity: -102 dBm, typical (@1% PER for 20 byte payload packet)

- Integrated PCB meander horizontal antenna
- 32 MHz reference oscillator
- 32 kHz clock oscillator
- 2.4 GHz frequency operation (ISM Band)
- Cortex 10-pin (0.05 inch) JTAG/SWD debug port for target MCU
- 1 RGB LED indicator
- 2 interrupt push button switches
- 1 FXOS8700CQ combo sensor
- 1 Coin Cell battery holder
- 1 Occupancy Sensor
- 1 EEPROM
- 1 Battery Charger
- 1 micro-USB connector
- 1 On/Off switch

Figure 2 shows the main board features of the NXP Occupancy Sensor Node board.
3. Occupancy Sensor Node reference design board

The Occupancy Sensor Node board is a reference design based on the NXP MKW24D512 transceiver. The core device is accompanied by a 32 MHz reference oscillator crystal, RF circuitry including a PCB antenna (and supporting circuitry), accelerometer/magnetometer sensor, EEPROM, Occupancy Sensor and RGB LED in a small form factor board. Figure 3 shows a simple block diagram.

![Figure 3. Occupancy Sensor Node block diagram](image)
3.1. **PCB Features**

The Occupancy Sensor Node board provides the following features:

- 2-layer metal, 0.062 inch thick FR4 board
- LGA footprint and power supply bypass
- Integrated PCB meander horizontal antenna
- 32 MHz reference oscillator
- 32 kHz clock oscillator
- 2.4 GHz frequency operation (ISM Band)
- Cortex 10-pin (0.05 inch) JTAG/SWD debug port for target MCU
- 1 RGB LED indicator
- 2 interrupt push button switches
- 1 FXOS87000CQ combo sensor
- 1 Coin Cell battery holder
- 1 Occupancy Sensor
- 1 EEPROM
- 1 Battery Charger
- 1 micro-USB connector
- 1 on/off switch

3.2. **Functional description**

The Occupancy Sensor Node board is built around the NXP MKW24D512 transceiver in a 63-pin (56-pin usable) LGA package. The MKW24D512 device features an IEEE Std. 802.15.4 radio frequency transceiver and a Kinetis family low-power, mixed-signal ARM® Cortex®-M4 MCU in a single package. This two-layer board is intended as a reference design platform and/or as a building block for application development. The principal purpose of the reference design board is to demonstrate some of the available features of the MKW24D512 transceiver in a reduced form factor board with a specific application taking advantage of the MKW24D512 transceiver’s USB and low-power capabilities.
3.2.1. RF performance and considerations

The Occupancy Sensor Node reference design board includes a 1 mW nominal output PA with internal voltage controlled oscillator (VCO), integrated transmit/receive switch, on-board power supply regulation, and full spread-spectrum encoding and decoding. Key specifications for the MKW24D512 transceiver are:

- Programmable output power from -35 dBm to +8 dBm MCU output pins
- Typical sensitivity is -102 dBm (@1% PER for 20 byte payload packet)
- Frequency range is 2360 MHz to 2480 MHz
- Differential bidirectional RF I/O port with integrated transmit/receive switch
- Meander horizontal printed metal antenna for a small footprint, low-cost design
- The board features a low component count RF matching network with off-chip 1:1 Balun

The layout has provision for out-of-band signal suppression (components L1 and C2) if required. Figure 4 shows the typical topology for the RF circuitry.
3.2.2. **Clocks**

The Occupancy Sensor Node has two clocks:

- **32 MHz Reference Oscillator**: Figure 5 shows the external 32 MHz external crystal Y1. This mounted crystal must meet the specifications outlined in the AN3251 application note. The IEEE Std. 802.15.4 requires that the frequency be accurate to less than ±40 ppm.
  - Capacitors C20 and C21 provide the bulk of the crystal load capacitance. At 25 °C it is desired to have the frequency accurate to ±10 ppm or less to allow for temperature variation.
  - To measure the 32 MHz oscillator frequency, signal CLKOUT (PTA18/CLK_OUT) can optionally be programmed to provide a buffered output clock signal.

- **Optional 32.768 kHz Crystal Oscillator**: Provision is also made for a secondary 32.768 kHz crystal Y2 (see Figure 6). This oscillator can be used for a low power accurate time base.
  - The module comes provided with this Y2 crystal and its load capacitors C23 and C24.
  - Load capacitors C23 and C24 provide the entire crystal load capacitance; there is no onboard trim capacitance.
  - The 32 kHz oscillator components are supplied.

![32MHz crystal](image1)

*Figure 5. Occupancy Sensor Node 32 MHz reference oscillator circuit*

![32kHz crystal](image2)

*Figure 6. Occupancy Sensor Node 32.768 kHz optional oscillator circuit*
3.2.3. **Power management**

The Occupancy Sensor Node power management circuit is shown in Figure 7.

![Power Management Diagram](image)

**Figure 7.** Occupancy Sensor Node power management circuit

3.2.4. **Battery charger**

The Occupancy Sensor Node battery charger circuit is shown in Figure 8.
3.2.5. **USB connector**

The Occupancy Sensor Node USB connector circuit is shown in Figure 9.

3.2.6. **External flash memory**

The Occupancy Sensor Node External Flash Memory circuit is shown in Figure 10.
Figure 10. Occupancy Sensor Node External flash memory circuit
3.2.7. **Peripheral functions – Combo sensor (I2C interface)**

Component U2 is a Freescale sensor, FXOS8700C, a 6-axis sensor with integrated linear accelerometer and magnetometer, very low power consumption, I2C selectable. Figure 11 shows the sensor circuit.

- Sensor power supply is P3V3_BRD
- Discrete pull-up resistors for the I2C port are provided
- Two interrupt signals

![FXOS8700CQ COMBO SENSOR](image)

**Figure 11.** Occupancy Sensor Node FXOS8700CQ combo sensor
3.3. **Schematic, board layout, and bill of material**

3.3.1. **Schematic**

![Schematic Image](image-url)

**Figure 12.** Occupancy Sensor Node schematic Rev X3
Figure 13. Occupancy Sensor Node board component location (top view)

Figure 14. Occupancy Sensor Node board test points
Figure 15. Occupancy Sensor Node board layout (top view)

Figure 16. Occupancy Sensor Node board layout (bottom view)
### 3.3.2. Bill of Materials

Table 1. Bill of materials (common parts for all frequency bands)

<table>
<thead>
<tr>
<th>Item</th>
<th>Qty</th>
<th>Reference</th>
<th>Value</th>
<th>Description</th>
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## Occupancy Sensor Node reference design board

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4. PCB manufacturing specifications

This section provides the specifications used to manufacture the Occupancy Sensor Node development printed circuit board (PCB) described in this guide.

The Occupancy Sensor Node development platform PCBs must comply with the following:

- The PCB must comply with Perfag1D/3C (www.perfag.dk/en/)
- The PCB manufacturer’s logo is required
- The PCB production week and year code is required
  - The manufacturer’s logo and week/year code must be stamped on the back of the PCB solder mask
  - The PCB manufacturer cannot insert text on the PCB either in copper or in silkscreen without written permission from NXP Semiconductors.
- The required Underwriter’s Laboratory (UL) Flammability Rating
  - The level is 94V-0 (http://ulstandards.ul.com/standard/?id=94)
  - The UL information must be stamped on the back of the PCB solder mask

NOTE

- A complete set of design files is available for the Occupancy Sensor Node transceiver at the NXP website (KW2xD) under “Software and Tools.” These reference designs should be used as a starting point for a custom application.
- The Freescale IEEE 802.15.4 / ZigBee Package and Hardware Layout Considerations Reference Manual, (ZHDCRM) is also available at the same web site to provide additional design guidance.

4.1. Single PCB construction

This section describes individual PCB construction details.

- The Occupancy Sensor Node PCBs are two-layer, multi-layer designs
- The PCBs contain no blind, buried, or micro vias
- PCB data:
  - Occupancy Sensor Node board’s size: approximately 63.9 x 33.2 mm (2.52 x 1.31 inches)
  - Occupancy Sensor Node board’s final thickness (Cu/Cu): 1.57 mm (0.62 inches) ±10% (excluding solder mask)

Table 2 defines some of the layers of the completed PCB. The artwork identification refers to the name of the layer in commonly used terms.
Table 2. **Occupancy Sensor Node layer by layer overview**

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<thead>
<tr>
<th>Layer</th>
<th>Artwork Identification</th>
<th>File Name</th>
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<td>Silkscreen Top</td>
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<tr>
<td>2</td>
<td>Top Layer Metal</td>
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<td>3</td>
<td>Bottom Layer Metal</td>
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<tr>
<td>4</td>
<td>Silkscreen Bottom</td>
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**CAUTION:**

The Occupancy Sensor Node reference design board contains high frequency 2.4 GHz RF circuitry. As a result, RF component placement, line geometries and layout, and spacing to the ground plane are critical parameters. As a result, BOARD STACKUP GEOMETRY IS CRITICAL.

Dielectric and copper thicknesses and spacing must not be changed; follow the stackup (see Figure 17) information provided with the reference design.

![0.062” +/-10% FINAL THICKNESS](image)

**Figure 17.** Occupancy Sensor Node PCB stackup cross-section (two layer)

- Solder mask is required
- Silk screen is required

4.2. **Panelization**

The panel size can be negotiated depending on production volume.
4.3. **Materials**

The PCB composite materials must meet the following requirements:
- Laminate: the base material (laminate) must be FR4. If the laminate material is changed, the RF electrical characteristics may change and degrade RF performance.
- Copper foil:
  - Top and Bottom copper layers must be 1 oz. copper
- Plating: All pad plating must be Hot Air Leveling (HAL).

4.4. **Solder mask**

The solder mask must meet the following requirements:
- Solder mask type: Liquid Film Electra EMP110 or equivalent
- Solder mask thickness: 10–30 μm.

4.5. **Silk screen**

The silk screen must meet the following requirements:
- Silk screen color: White
- Silk screen must be applied after application of solder mask if solder mask is required
- The silk screen ink must not extend into any plated-thru-holes
- The silk screen must be clipped back to the line of resistance.

4.6. **Electrical PCB testing**

- All PCBs must be 100% tested for opens and shorts
- Impedance measurement: An impedance measurement report is not mandatory.

4.7. **Packaging**

Packaging for the PCBs must meet the following requirements:
- Finished PCBs must remain in panel
- Finished PCBs must be packed in plastic bags that do not contain silicones or sulphur materials. These materials can degrade solderability.
4.8. **Hole specification/tool table**

See the `ncdrill-1-2.tap` file included with the Gerber files and the `FAB-KW2X-OSN-RD_X3.pdf` file.

4.9. **File description**

Downloadable files include: Design, Gerber, and PDF files. Gerber files are RS-274x format. Not all files included with the Gerber files are for PCB manufacturing.

PDF files included are:
- `FAB-KW2X-OSN-RD_X3.pdf` — board fabrication drawing
- `GRB-KW2X-OSN-RD_X3.pdf` — metal layers, solder mask, solder paste and silk screen
- `SPF-KW2X-OSN-RD_X3.pdf` — schematic diagram

Design files are in Allegro format with OrCAD schematic capture.
## 5. Revision history

<table>
<thead>
<tr>
<th>Revision number</th>
<th>Date</th>
<th>Substantive changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>02/2017</td>
<td>Initial release</td>
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