## UM12014 KITFS2400FRDMEVM user manual Rev. 1 — 1 February 2024

User manual

## **1** Introduction



This document is the user manual for the KITFS2400FRDMEVM evaluation board. This document is intended for the engineers involved in the evaluation, design, implementation, and validation of the FS2400 fail-safe system basis chip (SBC) with switch mode power supply (SMPS) and low dropout (LDO).

The scope of this document is to provide the user with information to evaluate the FS2400 fail-safe system basis chip with SMPS and LDO. This document covers connecting the hardware, installing the software and tools, configuring the environment, and using the kit.



### KITFS2400FRDMEVM user manual

The KITFS2400FRDMEVM enables development on the FS2400 device. The kit can be connected to the NXP GUI software that allows the user to interact with registers, try one time programming (OTP) configurations, and burn the part.

The device OTP can be burned twice. This board supports the FS2400 device.

## Important notice

## IMPORTANT NOTICE

For engineering development or evaluation purposes only

NXP provides the product under the following conditions:

This evaluation kit is for use of ENGINEERING DEVELOPMENT OR EVALUATION PURPOSES ONLY. It is provided as a sample IC pre-soldered to a printed-circuit board to make it easier to access inputs, outputs and supply terminals. This evaluation board may be used with any development system or other source of I/O signals by connecting it to the host MCU computer board via off-theshelf cables. This evaluation board is not a Reference Design and is not intended to represent a final design recommendation for any particular application. Final device in an application heavily depends on proper printed-circuit board layout and heat sinking design as well as attention to supply filtering, transient suppression, and I/O signal quality.

The product provided may not be complete in terms of required design, marketing, and or manufacturing related protective considerations, including product safety measures typically found in the end device incorporating the product. Due to the open construction of the product, it is the responsibility of the user to take all appropriate precautions for electric discharge. In order to minimize risks associated with the customers' applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards. For any safety concerns, contact NXP sales and technical support services.

User manual

## 2 Finding kit resources and information on the NXP web site

NXP Semiconductors provides online resources for this evaluation board and its supported device(s) on <u>http://</u><u>www.nxp.com</u>.

The information page for KITFS2400FRDMEVM evaluation board is at <u>http://</u><u>www.nxp.com/KITFS2400FRDMEVM</u>. The information page provides overview information, documentation, software and tools, parametric, ordering information, and a Getting Started tab. The Getting Started tab provides quick-reference information applicable to using the KITFS2400FRDMEVM evaluation board, including the downloadable assets referenced in this document.

## 2.1 Collaborate in the NXP community

The NXP community is for sharing ideas and tips, asking and answering technical questions, and receiving input on just about any embedded design topic.

The NXP community is at <u>http://community.nxp.com</u>.

## 3 Getting ready

Working with the KITFS2400FRDMEVM requires the kit contents, additional hardware, and a Windows PC workstation with installed software.

## 3.1 Kit contents

- Assembled and tested evaluation board, with preprogrammed S32K144 microcontroller embedded, placed in an anti-static bag
- USB-STD A to USB-B-mini cable
- Four connectors, terminal block plug, two positions, 3.81 mm pitch between pins
- One connector, terminal block plug, three positions, 3.81 mm pitch between pins
- Jumpers mounted on the board

## 3.2 Additional hardware

In addition to the kit contents, the following hardware is necessary or beneficial when working with this kit.

• A power supply with a range up to 40 V and a current limit set initially to 1 A

## 3.3 Windows PC workstation

This evaluation board requires a Windows PC workstation. Meeting the following minimum specifications is required:

- USB-enabled computer with Windows 7 or Windows 10
- FTDI USB serial port driver (for FT230X basic UART device)

### 3.4 Software

Installing the latest version of the NXP GUI software is necessary to work with this evaluation board. The NXP GUI for automotive PMICs can be downloaded at <a href="https://www.nxp.com">https://www.nxp.com</a>

## 4 Getting to know the hardware

## 4.1 Kit overview

The KITFS2400FRDMEVM kit provides an integrated platform for evaluating designs based on NXP's FS2400 SBC.

The kit hardware consists of the KITFS2400FRDMEVM evaluation board with an embedded S32K144 microcontroller, and the USB cable required to connect the board to the PC.

The KITFS2400FRDMEVM evaluation board is populated with a superset device. This device can be OTP programmed up to two times. It is then possible to test as many configurations as needed in Emulation mode. Connectors, jumpers, and switches on the board can be used to configure an evaluation environment that meets specific design requirements. The board also contains LEDs and test points that provide a means of monitoring performance in real time.

The S32K144 is soldered on the bottom side of the KITFS2400FRDMEVM board. The role of the S32K144 is to interface the FS2400 device with the GUI installed on the connected computer. The S32K144 draws power either from the USB cable connected to the PC or from the battery supply (when not connected to the GUI).

Note: Because of the socket, this kit is not optimized for performance measurement or current higher than 1 A.

### 4.1.1 KITFS2400FRDMEVM features

- Phoenix (3.81 mm) male connector for power supply input
- Phoenix (3.81 mm) male connectors for HVBUCK and HVLDO
- Phoenix (3.81 mm) male connectors for CAN communication
- Ignition key switch
- Embedded USB connection for easy connection to the NXP GUI (access to SPI/CAN bus, I/Os, RSTB, LIMP0, INTB, Debug, AMUX, regulators, registers, OTP emulation, and programming)
- LEDs that indicate signal or regulator status
- Support OTP fuse capabilities

### 4.1.2 Communication and I/Os between FS2400 and S32K144 MCU

The SPI bus, CAN Tx and Rx, along with the safety I/Os are connected to the S32K144 MCU.

This kit uses an S32K144 MCU to communicate with the NXP GUI. However, if the user wants to connect these I/Os to another MCU, this is possible. In this case, open the SW18 switch to disconnect the S32K144 MCU, see Figure 2, and connect the external MCU using J44, as shown in Figure 3. In addition to this change, make sure that the VDDIO voltage domain is the same on the MCU side and the SBC side.



#### KITFS2400FRDMEVM user manual



### 4.1.3 VDDIO selection

The VDDIO pin is powered through the VDDIO net and is used to supply FS2400 internal buffers and SPI communication.

The selection of VDDIO is made using several soldered resistors. By default on the board, VDDIO is supplied by external LDOs through the P\_VAR net.



#### Figure 4. VDDIO selection

VDDIO can also be powered from the V1 and V3 internal regulators or from the P5V0 net, which can be 5 V from a USB or 5 V coming from an external LDO. Figure 5 shows all the different ways the VDDIO can be supplied. The default configuration is represented by the blue jumpers.



Figure 5. VDDIO power supply selection

VDDIO is compatible with 1.8 V, 3.3 V, or 5 V. Therefore, VDDIO voltage is configurable between 1.8 V, 3.3 V, or 5 V using the J26 and J19 connectors (3.3 V by default). As J26 allows the user to choose between 5 V and P\_VAR, J19 allows the user to configure P\_VAR to 1.8 V or 3.3 V based on the jumper position. Figure 6 shows the schematics for P\_VAR voltage selection.

#### KITFS2400FRDMEVM user manual



The VDDIO\_SYS net is used to power nonapplicative components on the board, such as level shifters (to adjust voltage domain between FS2400 and S32K144) or LEDs. To get an accurate FS2400 current consumption measurement, remove the jumper 9-10 on J26.

## 4.1.4 S32K144 ADC

The S32K144 is configured to read some voltages from the FS2400 device using the circuits shown in Figure 7.



This allows measurements of the FS2400 characteristic voltages using the AMUX tab of the NXP GUI.

For an accurate FS2400 current consumption measurement, open SW20.

#### KITFS2400FRDMEVM user manual

## 4.2 Device OTP user configuration

NXP recommends learning about the OTP before operating the device. The FS2400 has a high level of flexibility because of its parameter configuration available in the OTP. The OTP affects the functionality of the device. In that sense, it is critical to understand how the OTP parameters are programmed, how to interact with the mirror registers and the FS2400 SoC.

The OTP-related operations can be performed only in OTP mode, which allows:

- Emulation: The product uses a given configuration as long as the power supply is not switched off
- **Programming:** The product uses the OTP-fused content that is valid even after the supply has been switched off/on

#### 4.2.1 OTP and mirror registers

The OTP configuration scheme is shown in Figure 8.

The device can be fused two times using mirror registers. The user can first load the mirror register content with the desired contents, then decide either to use the device in Emulation mode or to burn the next sector. The first sector burned is S1, the second is S1bis. The NXP GUI automatically manages the next sector to be burned. It is not possible to revert to the previous sector. When the user reaches the S1bis sector, there is no other possibility for burn. However, emulation of a different configuration in the mirror registers is still possible.



At boot, the content of the valid OTP fuse sector is loaded into the Mirror Register Sector 1. The content of the mirror registers is accessible from the NXP GUI by using specific SPI commands.

#### 4.2.2 OTP hardware implementation

To work in OTP emulation or OTP programming, start the device in OTP mode.

<u>Figure 9</u> shows the sequence to be followed to enter OTP mode. The voltage sequence on the kit is done using switches and a jumper installed on the board, while the OTP registers configuration is managed by the NXP GUI. This is described in detail in <u>Section 6.6</u> and <u>Section 6.7</u>.

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# UM12014

### KITFS2400FRDMEVM user manual



#### Figure 9. OTP mode entry

Figure 10 illustrates the hardware implementation.



## 4.3 Kit featured components

<u>Figure 11</u> identifies important components on the board and <u>Table 1</u> provides additional details about these components.

## KITFS2400FRDMEVM user manual



| Table 1. Evaluation board component | nts description |
|-------------------------------------|-----------------|
|-------------------------------------|-----------------|

| Number | Description   |
|--------|---|
| 1      | CAN Phoenix connector   |
| 2      | VBAT Phoenix connector  |
| 3      | <ul> <li>VBAT three position switch</li> <li>Position 1-2: Board supplied by USB</li> <li>Middle position: Board not supplied</li> <li>Position 2-3: Board supplied by Phoenix connector</li> </ul> |
| 4      | V1 power supply Phoenix connector   |
| 5      | USB connectors (JTAG for MCU flash; S32K144 for NXP GUI control)  |
| 6      | Optical probe connectors  |
| 7      | Communication debug connector (SPI and CAN)   |
| 8      | ALT_VBAT Phoenix connector (to supply MCU or VDDIO from a supply other than USB)  |
| 9      | FS2400 signals debug connector  |
| 10     | WAKE3/HID1 control switch   |
| 11     | WAKE2/HID0 control switch   |
| 12     | OTP mode control switch   |
| 13     | Jumper to set DBG pin voltage to 0 V (if opened) or to 4.7 V/7.95 V (if closed)   |
| 14     | Jumper to supply VBAT to the external LDO providing 5 V   |
| 15     | 5 V selection (USB or external LDO)   |
| 16     | P_VAR net voltage selection (1.8 V or 3.3 V)  |

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| Table 1. Evaluation board compensation accomptioncommed |                                   |  |
|---|-----------------------------------|--|
| Number  | Description                       |  |
| 17  | S32K144 supply selection          |  |
| 18  | V3 power supply Phoenix connector |  |

#### Table 1. Evaluation board components description...continued

### 4.3.1 FS2400: Fail-safe system basis chip with SMPS and LDO

### 4.3.1.1 General description

The FS2400 is a family of automotive safety SBC devices with multiple power supplies designed to support secure car access applications using ultra-wide band (UWB), near-field communication (NFC), and Bluetooth Low Energy devices while maintaining flexibility to fit other small applications requiring low power and CANFD communication.

This family of devices supports a wide range of applications, offering a choice in output voltage setting, physical interface, integrated system-level features to address low-power and noise-sensitive applications up to automotive safety integrity level (ASIL) B.

The FS2400 integrates a battery-connected switched mode regulator (V1) and a battery-connected linear regulator (V3) to supply the microcontroller, communication devices, and so on. V1 offers a high-performance switching regulator capable of operating in pulse frequency modulation (PFM) mode and forced pulse width modulation (FPWM) mode.

The FS2400 is developed in compliance with the ISO 26262 standard. The FS2400 includes enhanced safety features with fail-safe output, becoming part of a full safety-oriented system, covering up to ASIL B.

#### 4.3.1.2 Features

#### **Operating range**

- 40 V DC maximum input voltage
- Low-power off mode with low sleep current and multiple wake-up sources
- Low-power on mode with HVBUCK (V1) active, HVLDO (V3) selectable by OTP, and multiple wake-up sources

#### Power supplies

- V1: High-voltage synchronous buck converter with integrated FETs. Configurable output voltage (2.0 V to 5.0 V) and switching frequency, output DC current capability up to 400 mA and PFM mode for Low-power on mode operation
- V3: High-voltage LDO regulator for microcontroller I/O support with selectable output voltage between 3.3 V and 5.0 V and up to 150 mA current capability

#### System support

- One CAN FD 5M following IEC 62228-3 2019 edition
- Four wake-up inputs (40 V capable): WAKEx pins, HVIO pins, CANFD or SPI command
- Hardware ID detection capability
- One high-voltage I/O with wake-up capability (40 V capable)
- Device control via 32-bit SPI interface with CRC
- Integrated long duration timer (LDT) and analog multiplexer (AMUX)

#### **Functional safety**

- Developed following ISO 26262:2018 standard to fit for ASIL B applications
- Internal monitoring circuitry with its own reference

UM12014

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- Additional input for external voltage monitoring
- · Window or timeout watchdog function to monitor the MCU software failure
- Analog built-in self-test (ABIST) on demand
- Safety outputs (RSTB, LIMP0)
- · Safety input to monitor external IC state (ERRMON)

#### **Configuration and enablement**

- HVQFN32EP: QFN, 32 pins with exposed pad for optimized thermal management, wettable flanks, 5 mm x 5 mm x 0.85 mm, 0.5 mm pitch
- · Permanent device customization via OTP fuse memory
- OTP emulation mode for system development and evaluation

### 4.3.2 LED signaling

Figure 12 shows the LEDs provided as visual output devices for the evaluation board:



Figure 12. Evaluation board LED signaling location

#### Table 2. Evaluation board LED signaling description

| Label | Name     | Color | Description                      |
|-------|----------|-------|----------------------------------|
| D4    | RSTB     | Red   | RSTB asserted (logic level = 0)  |
| D6    | INTB     | Red   | INTB asserted (logic level = 0)  |
| D7    | V1       | Green | V1 Power supply on               |
| D9    | V3       | Green | V3 Power supply on               |
| D10   | VBAT     | Green | VBAT on                          |
| D14   | OTP mode | Blue  | OTP mode threshold crossed       |
| D15   | DBG mode | Green | DBG mode threshold crossed       |
| D21   | VDD MCU  | Green | MCU supplied                     |
| D26   | LIMP0    | Red   | LIMP0 asserted (logic level = 0) |

### KITFS2400FRDMEVM user manual

#### 4.3.3 Connectors

Figure 13 shows the location of the connectors on the board.



#### 4.3.3.1 VBAT connectors

VBAT connects to the board through the Phoenix connector (J6).

#### Table 3. Main VBAT Phoenix connector (J6)

| Schematic label | Signal name | Description                                  |
|-----------------|-------------|--|
| J6-1            | VBAT        | Main battery voltage supply input for FS2400 |
| J6-2            | GND         | Ground                                       |

#### Table 4. Alternative VBAT Phoenix connector (J35)

| Schematic label | Signal name | Description  |
|-----------------|-------------|--|
| J35-1           | VBAT_ALT    | Alternative battery voltage supply input for external LDOs and MCU |
| J35-2           | GND         | Ground   |

#### 4.3.3.2 Output power supply connectors

#### Table 5. V3 (HVLDO) connector (J7)

| Schematic label | Signal name | Description            |
|-----------------|-------------|------------------------|
| J7-1            | V3          | V3 power supply output |
| J7-2            | GND         | Ground                 |

## KITFS2400FRDMEVM user manual

| Schematic label | Signal name | Description            |
|-----------------|-------------|------------------------|
| J12-1           | V1_load     | V1 power supply output |
| J12-2           | GND         | Ground                 |

#### Table 6. V1 (HVBUCK) connector (J12)

### 4.3.3.3 CAN bus connector

| Table 7 | . V3 | CAN   | bus | connector  | (J16)                     |
|---------|------|-------|-----|------------|---------------------------|
|         |      | 0/111 | Nuo | 0011100101 | $(\mathbf{v},\mathbf{v})$ |

| Schematic label | Signal name | Description  |
|-----------------|-------------|--------------|
| J16-1           | CANH        | CAN bus high |
| J16-2           | CANL        | CAN bus low  |
| J16-3           | GND         | Ground       |

## 4.3.3.4 Debug connectors

#### Table 8. Communication debug connector (J44)

| Schematic label | Signal name | Description                  |
|-----------------|-------------|------------------------------|
| J44-1           | CSB         | SPI chip select (active low) |
| J44-2           | CANTXD      | CAN transmitter data         |
| J44-3           | SCK         | SPI clock                    |
| J44-4           | CANRXD      | CAN receiver data            |
| J44-5           | MISO        | SPI MISO                     |
| J44-6           | GND         | Ground                       |
| J44-7           | MOSI        | SPI MOSI                     |
| J44-8           | GND         | Ground                       |

#### Table 9. FS2400 signals debug connector (J4)

| Schematic label | Signal name   | Description   |
|-----------------|---------------|---|
| J4-1            | WAKE2_IN/HID0 | Global input wake pin or HW ID depending on configuration |
| J4-2            | WAKE2/HID0    | Wake pin or HW ID depending on configuration              |
| J4-3            | WAKE3_IN/HID1 | Global input wake pin or HW ID depending on configuration |
| J4-4            | WAKE3/HID1    | Wake pin or HW ID depending on configuration              |
| J4-5            | LIMP0_out     | Fail-safe pin (active low)                                |
| J4-6            | HVIO1_IN      | Global input HVIO1 pin                                    |
| J4-7            | RSTB          | Reset pin (active low)                                    |
| J4-8            | HVIO1         | High-voltage I/O pin                                      |
| J4-9            | VDDIO         | VDDIO pin   |
| J4-10           | GND           | Ground  |
| J4-11           | DEBUG         | Debug pin   |
| J4-12           | AMUX          | Analog multiplexer output                                 |

### KITFS2400FRDMEVM user manual

| Schematic label | Signal name | Description                 |
|-----------------|-------------|-----------------------------|
| J4-13           | VDIG        | VDIG pin                    |
| J4-14           | VMON_EXT    | VMON_EXT pin                |
| J4-15           | VBOS        | Best of supply              |
| J4-16           | P3V3        | External 3.3 V power supply |

#### Table 9. FS2400 signals debug connector (J4)...continued

## 4.3.3.5 S32K144 communication connectors

#### Table 10. USB connector (J48)

| Schematic label | Signal name | Description                                 |
|-----------------|-------------|---|
| J48             | NA          | USB connector to interface GUI with S32K144 |

#### Table 11. JTAG connector (J38)

| Schematic label | Signal name | Description                        |
|-----------------|-------------|------------------------------------|
| J38             | NA          | JTAG connector to flash S32K144 SW |

### 4.3.4 Test points

Figure 14 shows the test points that provide access to various signals to and from the boards.



#### Table 12. Evaluation board test points description

| Test point name | Signal name | Description            |
|-----------------|-------------|------------------------|
| TP1             | RSTB        | Reset pin (active low) |

| UM12014     |  |
|-------------|--|
| User manual |  |

KITFS2400FRDMEVM user manual

| Test point name | Signal name | Description            |
|-----------------|-------------|------------------------|
| TP2             | GND         | Ground                 |
| TP3             | CANL        | CAN bus low            |
| TP4             | V1_load     | V1 power supply output |
| TP5             | AMUX        | Analog multiplexer pin |
| TP6             | CANH        | CAN bus high           |
| TP9             | GND         | Ground                 |
| TP11            | INTB        | Interruption pin       |
| TP12            | GND         | Ground                 |
| TP13            | V3          | V3 power supply output |
| TP15            | LIMP0       | LIMP0 pin              |
| TP16            | GND         | Ground                 |
| TP17            | VSUP        | VSUP pin               |
| TP21            | GND         | Ground                 |
| TP23            | CANTXD      | CAN transmitter        |
| TP25            | GND         | Ground                 |
| TP26            | CANRXD      | CAN receiver           |

#### Table 12. Evaluation board test points description...continued

## 4.3.5 Jumpers



## KITFS2400FRDMEVM user manual

| Name                       | Function                            | Pin number | Jumper/pin function  |
|----------------------------|-------------------------------------|------------|--|
| J4                         | VMON_EXT easy evaluation            | 8–10       | 3.3 V to VMON_EXT resistor bridge input                              |
| 110                        | P_VAR output voltage selection      | 1-2        | P_VAR is 1.8 V   |
| 515                        |                                     | 2–3        | P_VAR is 3.3 V   |
| J30                        | Apply voltage to DBG pin            | 1–2        | Either 5 V (Debug mode) or 8 V (OTP mode) depending on SW12 position |
| J33                        | Power 5 V LDO from main VBAT supply | 1-2        | 5 V LDO input supplied by main VBAT                                  |
|                            | Select S32K144 supply               | 1-2        | S32K144 is supplied from an external 5V LDO                          |
| J36                        |                                     | 2–3        | S32K144 is supplied from an external P_VAR<br>LDO                    |
| 127                        | Salaat DE\/0 pat aupply             | 1-2        | P5V0 net is supplied by an external 5 V LDO                          |
| J37 Select P5V0 net supply |                                     | 2–3        | P5V0 net is supplied by 5 V from USB                                 |

#### Table 13. Evaluation board jumpers description

## 4.3.6 Switches



## Table 14 SW1 description

| Position   | Function | Description  |
|------------|----------|--|
| Up (2–3)   | VBAT on  | Powered by external supply through J6  |
| Mid        | VBAT off | —  |
| Down (1–2) | VBAT on  | Powered by board-embedded boost (8 V): Only for quick debug, don't load FS2400 using this supply |

### KITFS2400FRDMEVM user manual

#### Table 15. S2 description

| Position | Function              | Description                                      |
|----------|-----------------------|--|
| Right    | Corresponding LED on  | Each LED is controlled by an independent switch; |
| Left     | Corresponding LED off | consumption measurement                          |

#### Table 16. SW4 description

| Position | Function       | Description                 |
|----------|----------------|-----------------------------|
| Left     | WAKE2/HID0 off | WAKE2/HID0 pin tied to VSUP |
| Right    | WAKE2/HID0 on  | WAKE2/HID0 pin opened       |

#### Table 17. SW9 description

| Position | Function              | Description                                      |
|----------|-----------------------|--|
| Тор      | Corresponding LED on  | Each LED is controlled by an independent switch; |
| Bottom   | Corresponding LED off | consumption measurement                          |

#### Table 18. SW12 description

| Position | Function     | Description  |
|----------|--------------|--|
| Left     | OTP mode off | FS2400 starts in Debug (if J30 1-2 jumper is placed) or in Applicative mode (if J30 jumper is off)       |
| Right    | OTP mode on  | FS2400 starts in OTP mode (if J30 1-2 jumper is placed) or<br>in Applicative mode (if J30 jumper is off) |

#### Table 19. SW19 description

| Position | Function       | Description                 |
|----------|----------------|-----------------------------|
| Left     | WAKE3/HID1 off | WAKE3/HID1 pin tied to VSUP |
| Right    | WAKE3/HID1 on  | WAKE3/HID1 pin opened       |

## 4.4 Schematic, board layout and bill of materials

The schematic, board layout, and bill of materials for the KITFS2400FRDMEVM evaluation board are available at <a href="http://www.nxp.com/KITFS2400FRDMEVM">http://www.nxp.com/KITFS2400FRDMEVM</a>.

#### KITFS2400FRDMEVM user manual

#### Installing and configuring software and tools 5

The KITFS2400FRDMEVM is always delivered with the GUI firmware already flashed. If the MCU firmware is already flashed, the user may ignore this section. If it is specified that the user must update the firmware or it is malfunctioning, follow the instructions in Section 5.1.

## 5.1 Flashing or updating the GUI firmware

In the event the user must flash the MCU S32K144, this section explains how to proceed.

#### 5.1.1 Hardware setup

First, get a compatible debugger (the provided firmware is compatible with PEmicro debuggers):

U-MULTILINK NXP Semiconductors | Mouser France



#### Figure 17. Example debugger

Make sure to properly connect the debugger:



Figure 18. Proper connection

- 1. Connect the mini USB port to the computer to supply the microcontroller.
- 2. Connect the debugger to the J38 connector and be careful to connect the red wire to Pin 1.

KITFS2400FRDMEVM user manual

#### 5.1.2 Software setup

1. Download Design Studio : <u>https://www.nxp.com/webapp/swlicensing/sso/downloadSoftware.sp?catid=S32</u> DS-IDE-ARM-V2-X

| SOFTWARE   |            |
|--|------------|
| S32 Design Studio for ARM 2.2 - Windows/Linux  |            |
| S32 Design Studio for Arm v2.2 contains GNU Build Tools for Arm Embedded Processors, along with debugger options, fully integrated S32 SDK 3.0.2, includes S32DS E | various    |
| FLEXERA 1 KB S32DS-IDE-ARM-V2-X  | aaa-054137 |

- 2. Once downloaded, use the Design Studio installation guide included inside the package provided: "KITFS2400FRDMEVM\_HW\_Test\_Package\_W20.zip".
- 3. Launch Design Studio

| Eclipse Launcher         |   |                               |               |
|--------------------------|---|-------------------------------|---------------|
| elect a directory as w   | orkspace                                |                               |               |
| S32 Design Studio for AR | M uses the workspace directory to store | its preferences and developme | nt artifacts. |
|                          |   |                               |               |
| Workspace: Path page     | kage unzipped                           |                               | Browse        |
|                          |   |                               |               |
| Use this as the default  | and do not ask again                    |                               |               |
| Recent Workspaces        |   |                               |               |
| necent fromspaces        |   |                               |               |
|                          |   |                               |               |
|                          |   | OK                            | Cancel        |
|                          |   |                               | aaa-05        |

#### 4. Open the project provided

|            | Ealt Source Relactor Naviga    | te search Projec |
|------------|--------------------------------|------------------|
|            | New                            | Alt+Shift+N>     |
|            | Open File                      |                  |
|            | Open Projects from File System |                  |
|            | Close                          | Ctrl+W           |
|            | Close All                      | Ctrl+Shift+W     |
|            | Save                           | Ctrl+S           |
| <b>a</b> . | Save As                        |                  |
| 6          | Save All                       | Ctrl+Shift+S     |
|            | Revert                         |                  |
|            | Move                           |                  |
| 2          | Rename                         | F2               |
| 1          | Refresh                        | F5               |
|            | Convert Line Delimiters To     | >                |
| 8          | Print                          | Ctrl+P           |
|            | Switch Workspace               | >                |
|            | Restart                        |                  |
| 2          | Import                         |                  |
| 2          | Export                         |                  |
|            | Properties                     | Alt+Enter        |
|            | Exit                           |                  |

Go to the Directory and the location of the unzipped folder. Select the S32K144\_Flash folder:

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## UM12014

## KITFS2400FRDMEVM user manual

|                     | Path package unzipped               | \S32K144_Flash               | <ul> <li>Directory Archive</li> </ul> |
|---------------------|-------------------------------------|------------------------------|---------------------------------------|
| type filter text    |                                     |                              | Select All                            |
| Folder              | Flach                               | Import as<br>Eclipse project | Deselect All                          |
| Use installed proje | et configurators to:<br>ed projects |                              | Hide already open proje               |
| ✓ Detect and con    | figure project natures              |                              |                                       |
| Working Sets        |                                     |                              |                                       |
| Working Sets        | to working sets                     |                              | New                                   |

In the following window, click the button identified in the red box to access of the project view.

| File | e Edit | Source      | Refactor | Navigate | Search     |
|------|--------|-------------|----------|----------|------------|
| 8    | 🤝 G    | etting Star | ted 🔀    |          |            |
|      |        | START       | HERE     | EXTE     | INSION     |
|      |        | Start       | Here     |          |            |
|      |        | 1           |          |          | 15         |
|      |        |             |          |          |            |
|      |        |             | WF       |          | OM         |
|      |        |             |          | LU       |            |
|      |        |             |          |          |            |
|      |        |             |          |          |            |
|      |        |             |          |          |            |
|      |        |             |          |          |            |
|      |        |             |          |          | aaa-054141 |

The Project Explorer view appears:

### KITFS2400FRDMEVM user manual



5. Configure the debug parameters:

Go to the symbol inside the red box shown below and choose Debug Configurations.



The debugger configuration should be the same as below:

## **NXP Semiconductors**

## UM12014

## KITFS2400FRDMEVM user manual

| Create, manage, and run configurations<br>Plugin has not been registered. Some functionality may not be available.  |   |
|---|---|
| Image: Image              | Name         SISCH44, Flink, Debugg           Main         So Debugger         je Stantop           Software Registration         Extension           Please registration         Register now           PMisro         Interface           PMisro         Interface           PMisro         Interface           Vision         Vision           Processering         Seminor           Processering         Interface           Interface         USB Multink, USB Multink EV, Embedded OSBDM/OSTALG - USB Port           Port:         USB 1- Multink Universal Rev A (PESS5048)           Vision         Kertein  |
|   | Select Device         Vendor: NOP         Family: S3XXix         Target: S32K144/512M15           Care:         M4         Specify Network Card IP         Additional Options           Care:         M4         Specify Network Card IP         Additional Options           Care:         M4         Specify Network Card IP         Additional Options           Additional Options         Specify Network Card IP         Specify Network Card IP           Advanced Options         Hambers Indirace Power Costrol (Voltage ->> Power-Out Jack)         Proved power to target           Provide power to target         Regulator Output Voltage         Power Up Delay         250 ms.           Power oft target upon software out         Yes         Power Up Delay         1000 ms.   |
| liter matched 7 of 13 items   | Revet Appy  |
| 0   | Debug Close   |
| Im Market Constraints     Im Market Con | Name:       [SIX:14.14.fbi.h. Octory         Minit To Debugser:       [Sectory Fig: Sector)         Hardware Interface Rower Control (Voltage -> Sector-Out Ard)         Provide power to target       Regulator Output Voltage         Provide power to target       Regulator Output Voltage         Provide power to target       Regulator Output Voltage         Debugs 70:11       Provide power to target         Provide power to target       Regulator Output Voltage         Debug 70:11       Provide power to target         Coll Server 5 Settings       Server Port Number:         Class 5 Server 10:11       Server Port Number:         Convert Farmedrax       Server Port Number:         Convert 5 Server       Server Port Number:         Convert 5 Server       Server Port Number:         Convert 5 Server       Server Portanumer: |
| er matched 7 of 13 items  | Recent Apply  |
| Ð   | Debug Close   |
| Debug Configurations<br>ate, manage, and run configurations<br>Plugin has not been registered. Some functionality may not be available.<br>Plugin has not been registered. Some functionality may not be available.<br>Plugin has not been registered.<br>Plugin has not been registered been been been been been been been be  | Name: \$32X144,Flash,Debug  Name: \$32X144,Flash,Debug  Main "to Debugger _ie Startup Sg. Source _ Common 151 510 Support # OS Awareness  Other options Commands: _ est man inaccession-by-default off est top activity on sat top connect Simous 240  S100/Power Measurement Setting:  |
|   | Exception Catching Settings (Valid for ContextM cores only)  finable HardFault Catch  finable Exception Entry/Return Catch  finable Exception Entry/Return Catch  finable Exception Entry/Return Catch  |
|   | C Enable State information Euro Catch  Enable No-Coprocessor Catch  Enable No-Coprocessor Catch  Enable MemManage Catch  Enable MemManage Catch  Fance thread Sat update on suspend   |
| rmatched 7 of 13 items  |   |

## KITFS2400FRDMEVM user manual

| 3 R × 8 3 •  | Name S32K144 Rash Debug   |
|--|---|
| type filter text   | 🔝 Main 🔍 Debugger 🍺 Startup 🕼 Source 🛅 Common IIII SVD Support 🦊 OS Awareness   |
| C/C++ Application  | Project:  |
| GDB Hardware Debugging   | S32K144,Flash Br  |
| GOB PENtero Interface Debugging     S3X144_Plash_Debug     GoB SEGGER-Link Debugging     Lough Group | Specify the number of additional object files you wish to program 0 Generate Object File Fields   |
|  | CrC++ Applications<br>Path package unzipped<br>#s28xx-fw=P526-V81 hex<br>Variables Search Project Br  |
|  | CrC++ Applications<br>Path package unzipped<br>JrSInx-Hn-FSD4-V61 hex<br>Variables Search Project Br<br>Build (If required) before learching<br>Build Conferentiano Datus   |
|  | CrC++ Applications<br>Path package unzipped InStru-fru-FSB4-V81.hex<br>Build (or required) before learnching<br>Build Configuration (Debug<br>Configure Auto build<br>Dable ando build<br>Build Configure Settings. |

When the window below appears, click Yes.

| Confirm    | m Perspective Switch  | ×                                     |
|------------|---|---------------------------------------|
| <b>2</b> T | his kind of launch is configured to open the Debug perspective  | e when it suspends.                   |
| T          | his Debug perspective is designed to support application debu<br>riews for displaying the debug stack, variables and breakpoint n | gging. It incorporates<br>nanagement. |
| 0          | to you want to open this perspective now?   |                                       |
| Remer      | mber my decision  |                                       |
|            | Yes   | No                                    |
|            |   | aaa-054148                            |

If the debug configuration is properly set, the following <u>window</u> should appear:

| Order 1   | CONTRACTORS OF A REAL |
|---|-----------------------|
| B Chefore II<br>An welfares that nutrition.<br>For stopping the debugger mode   | 862                   |
| Recorder as the statistic.  | — j                   |
| E Low as a model.   | 1                     |
| For stopping the debugger mode  | 5.4 <u>9</u>          |
|   |                       |
| Aberbenel II 2 2 *** D Closede II 2 beis Strapter Augers Ster & Indexes O Secures () Versy  |                       |
| Program Conductor     Program     Pro |                       |
|   | aaa-05414             |

Click either **Stop** button to stop the debugger.

6. Go to the Run menu and click **Flash from file** as shown below:

KITFS2400FRDMEVM user manual



7. Select the file "fs23xx-fw-FS24-Vxx.hex" provided in the installation package.

| · · · · · · · · · · · · · · · · · · · | Name: 532K144,Flash, Debug   |
|---------------------------------------|--|
|                                       | 👔 Main 👋 Debugger 🐌 Statup 🦞 Source 🛅 Common 👭 OS Awareness 🔠 SVD Support  |
| GDB PEMicro Interface Debugging       | Project:   |
| C GDB SEGGER J-Link Debugging         | S32K144_Flash Browse   |
|                                       |  |
|                                       | C/C++ Application<br>Path package unzipped fr23x+-fr-F524-V[1].het<br>Variables_ Search Project. Browse  |
|                                       | C/C++ Application<br>Path package unzipped<br>fri23ox-for-FS24-VB1.hes<br>Build (# required) before lounching<br>Build (# required) before lounching   |
|                                       | C/C+- Application<br>Path package unzipped<br>fri2box-fre-PSId-V81.hes<br>Build (if required) before launching<br>Build (arriguestion: Debug   |
|                                       | C/C++ Application           Path package unzipped         fra2m-fra-FS24-V81.hes           Build (# required) before lounching         Variables         Search Project         Browse.           Build Configurations         Debug         Orable auto build         Orable auto build         Orable suto build         Example auto build         Configurations         Configurations <thconf< td=""></thconf<> |

Click **Flash** once the .hex file is selected.

At the end, if the microcontroller is correctly flashed, the following message appears:

```
Checksum Verification Successful. (Cumulative CRC-16=$2705)
Application verified in memory. No need to reprogram.
```

CMD>RE

```
Initializing.
Target has been RESET and is active.
```

aaa-054152

8. Install the NXP GUI.

9. Connect the board to the computer via the mini USB port. Click **Start** (at the upper left of the interface). If the NXP user interface recognizes the microcontroller, the version of the firmware will appear at the bottom of the window.

## 5.2 Installing NXP GUI software package

To install the FS2400 NXP GUI, download or obtain the NXP GUI package, unzip the package and double click the NXP\_GUI\_version-Setup.exe and follow the instructions. Proceed with the following pop-up windows to install the application on a Windows PC:

| Name                      | ~   | Status  |  |
|---------------------------|---|---|--|
| RXP_GUI_Dev-3.1.305-Setup |   | $\odot$   |  |
|                           |   | aaa-054159  |  |
| Figure 19. NXP GUI setup  | NOV.6311.02 Day     N | Image: Section 1.05 Strategy       - · · · · · · · · · · · · · · · · · · ·  |  |
|                           | Add Schulz Schule     Aussen of Schule  | Verified Lind Index     Our bound to the All Lind Index     O |  |

Select the following options before completing the installation of the setup:

- Run NXP\_GUI
- Show Readme

Click Finish to complete the installation

### KITFS2400FRDMEVM user manual



When the installation is finished, search for "NXPGUI" in the windows search bar. Click to launch.

## 5.3 Launching the FS24 NXP GUI

When the KITFS2400FRDMEVM kit is set up and the GUI installed, follow the steps below to launch the GUI:

1. Click the **Windows** icon (bottom left corner) and locate "NXPGUI" in the Windows All Apps bar, then click the NXPGUI icon to launch the GUI.



aaa-054163

When the GUI opens, the first window to appear is the Kit Selection window. In the Kit Selection window, select the settings shown <u>below</u>. When finished selecting the settings, click **OK**. To avoid the Kit Selection window on every launch, check the box "Use this configuration and do not ask again".

KITFS2400FRDMEVM user manual

| Kit and Dev   | ices   |                                 |   |    |
|---|--|---------------------------------|---|----|
| PF5300  |  | 3.1.244                         | 10/27/2021                                  | _  |
| <ul> <li>KITPF510x</li> </ul>   |  |                                 |   |    |
| PF5103  |  | 3.1.303                         | 6/27/2022                                   |    |
| PF0300  |  | 3.1.303                         | 6/27/2022                                   | -1 |
| <ul> <li>KITPF5030</li> </ul>   |  |                                 |   |    |
| PF5030  |  | 3.1.290                         | 5/23/2022                                   | -1 |
| * KHES23  |  |                                 |   |    |
| 500000  |  | 2 4 200                         | 7 (1 5 (2022)                               |    |
| FS2300  |  | 3.1.309                         | 7/15/2022                                   |    |
| FS2300<br>FS2320  | / 1: Select F  | 3.1.309<br>S2400 dev            | 7/15/2022<br>7/15/2022<br>/ice              |    |
| FS2300<br>FS2320<br>• KITFS24   | 1: Select F  | 3.1.309<br>-52400 dev           | 7/15/2022<br>7/15/2022<br>Vice              |    |
| FS2300<br>FS2320<br>• KITFS24<br>FS2400   | 1: Select F  | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>Vice<br>7/19/2022 |    |
| FS2300<br>FS2320<br>* KITFS24<br>FS2400 *   | 1: Select F  | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>Vice<br>7/19/2022 | Ŧ  |
| FS2300<br>FS2320<br>• KITFS24<br>FS2400<br>• KITFS24  | 1: Select F  | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>/ice<br>7/19/2022 |    |
| FS2300<br>FS2320<br>KITFS24<br>FS2400<br>A kit for NXP F<br>Advanced S                              | 1: Select F<br>MIC evaluation<br>ettings                   | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>/ice<br>7/19/2022 | •  |
| FS2300<br>FS2320<br>KITFS24<br>FS2400<br>A kit for NXP F<br>Advanced S<br>Feature Set               | 1: Select F<br>MIC evaluation<br>ettings<br>SPI            | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>/ice<br>7/19/2022 | -  |
| FS2300<br>FS2320<br>KITFS24<br>FS2400<br>A kit for NXP F<br>Advanced S<br>Feature Set<br>Target MCU | 1: Select F<br>MIC evaluation<br>ettings<br>SPI<br>S32K144 | 3.1.309<br>52400 dev<br>3.1.311 | 7/15/2022<br>7/15/2022<br>7/19/2022         | -  |

aaa-054164

*Note:* The Kit Selection window can be enabled/disabled through the File main menu item once the GUI is launched by checking/unchecking the "Do not display GUI Kit Selection at Start" box.

#### Configuring the hardware and software 6

## 6.1 Setting up the KITFS2400FRDMEVM

The procedure for setting up the KITFS2400FRDMEVM board is as follows:

- 1. Make sure the board has the jumpers and switches configured in their default positions. The default debug configuration enables the board to be fully controlled by the S32K144 MCU (via SPI) and the GUI. Section 4.3 shows the default jumpers and switches configurations.
- 2. Connect the power supply to J6 (Phoenix connector 3.81 mm). The power supply should be set to a nominal value of 14 V.
- 3. Make sure that the USB cable between the board and the PC is securely connected. This connection is critical because the USB port serves as a communication channel between the PC and the S32K144 MCU onboard and provides voltages and references to some onboard circuits.



4. Place SW1 in top position (2-3)

## 6.2 Connecting the KITFS2400FRDMEVM to the GUI

The procedure for connecting the KITFS2400FRDMEVM to the GUI is as follows:

- 1. To launch the NXP GUI application, see Section 5.3.
- 2. In the USB and Device Status bar at the bottom of the GUI window, the State message should display "DISCONNECTED" when the USB cable is plugged in but communication has not yet been established between the S32K144 MCU and the FS2400 SBC.

| USB cable plugged in, communication | State: DISCONNECTED |
|-------------------------------------|---------------------|
| not established yet                 | aaa-049530          |

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|-------------|--|--------------------------------------|
| User manual | Rev. 1 — 1 February 2024   |                                      |
|             |  | 30 / 79                              |

#### KITFS2400FRDMEVM user manual

| USB cable not plugged in  | State: NOT DETECTED aaa-049531   |
|---|--|
| 1. To establish communication betw take control, click <b>Start</b> in the Co | een the S32K144 MCU and the FS2400 SBC and allow the GUI to<br>nnection Toolbar in the top left corner of the GUI window. Once the |

USB cable plugged in, communication established

*Note:* If *Start* does not light and stays gray, the FTDI driver might be missing. To confirm this, access the Windows Device Manager and look for FT230X Basic UART Device.

communication is established, the State message becomes "CONNECTED".



**Note:** To solve this issue, the user should look for the FTDI USB Serial Port driver corresponding to the PC and install it. For Dell computers, the package can be downloaded <u>here</u>.

## 6.3 Operation modes

The KITFS2400FRDMEVM provides three distinct operation modes with direct impact on the device's functionalities. Understanding these modes helps use the GUI properly.

**Note:** When using the KITFS2400FRDMEVM for the first time, it is recommended to start the device in Debug mode. Indeed, Debug mode disables the watchdog, making engineering and debugging easier. To start the device in Debug mode, the hardware should be configured accordingly, see <u>Section 6.3.1.2</u>. Once the watchdog is set to infinite window, Debug mode can be exited from the GUI.

The voltage level on the FS2400 Debug pin is one condition for entering a given operation mode.

Figure 23 gives an overview of the device modes and actions to perform in the GUI or on the EVB to enter or exit modes.

## KITFS2400FRDMEVM user manual



## 6.3.1 Debug mode

### 6.3.1.1 Debug mode definition

The device starts in Debug mode when the hardware is configured accordingly. This mode requires the Debug pin to be connected to VBOS through a diode to enter Debug mode automatically at startup. On the board, jumper J30 on.

Debug mode works in parallel with Normal mode or Test mode. When Debug mode is active, the FS24 will run with limited functionalities as some safety functions are disabled.

**Note:** RSTB 8 s timer is disabled, Watchdog is configured with infinite timeout and window fully opened (equivalent to disabled), CAN transceiver is set in Active mode by default.

Debug mode is helpful during software development if the device has those functions enabled by OTP.

### 6.3.1.2 Debug mode activation

- 1. Set the default jumper configuration, see <u>Section 6.1</u>.
- 2. Verify that jumper J30 is on (allows to apply 5 V or 8 V on the pin).
- 3. Plug the USB cable between the PC and the board. Blue LED D14 for OTP 8 V generation is off. Green LED D15 for debug is on.
- 4. Apply power supply VBAT.
- 5. Open the GUI and start the connection.
- 6. Check that the GUI has detected Debug mode in the USB and device status bar:



7. The equipment is now set to Debug mode.

#### 6.3.1.3 Debug mode deactivation

To get out of Debug mode, send a DEBUG EXIT SPI request by either using the ACCESS>Main Tab>Device State>"Exit Debug Mode" checkbox, or using the DBG\_EXIT bit from the M\_SYS1\_CFG register of the Register Map tab.

**Note:** Removing the Debug Selection jumper J30 will not automatically deactivate Debug mode, as DBG 5 V on FS2400 Debug pin is only a condition for Debug mode entry, not a condition to remain in Debug mode.

#### 6.3.2 Test mode

#### 6.3.2.1 Test mode definition

Test mode allows the user to write in the Mirror registers to configure or reconfigure the device for customer evaluation and to burn OTP fuses.

# **Note:** In case of a POR, the Mirror registers will be reset to the default OTP configuration (empty if OTP not burned).

Test mode requires jumper J30 on and switch SW12 on (OTP mode is therefore enabled when Test mode is activated) and valid Test mode keys to be sent by SPI. Permanently fusing all the data stored in Mirror registers to the OTP sectors necessitates an extra key command, available in the PROG tool. The OTP fuse burning process is explained in <u>Section 6.7</u>.

When the Mirror registers configuration is done, the user can move to User mode to power up the device with the given configuration.

#### 6.3.2.2 Test mode activation: hardware and software

In order to start the device with a configuration loaded in the Mirror registers, follow the instructions below:

- 1. Set the default jumper configuration, see <u>Section 6.1</u>.
- 2. Set switch SW12 on (apply 8 V on the debug pin).
- 3. Verify that jumper J30 is on (allows the user to apply 5 V or 8 V on the pin).
- 4. Plug the USB cable between the PC and the board. Blue LED D14 for OTP 8 V generation and green LED D15 for debug is on.
- 5. Apply power supply VBAT. This action loads the Mirror registers with burned OTP configuration if present.
- 6. Open the GUI and start the connection.
- 7. Select "test-mode" from Connection Toolbar:



#### 6.3.2.3 Test mode deactivation

Once activated, Test mode can be deactivated using the GUI by selecting User mode from the Connection Toolbar.

**Note:** Only switching off "OTP Mode Enable SW12" without changing mode in GUI will not automatically deactivate Test mode, as OTP 8 V on FS2400 debug pin is only a condition for Test mode entry/exit and not a condition to remain in Test mode.

#### 6.3.2.4 Test mode operation

Operating in Test mode allows the user to create and test a preliminary version of a desired configuration in the Mirror registers, prior to submitting the configuration to the OTP fuse burning process.

There are two ways to load Mirror registers:

- With a TBB script via SCRIPT tool (see Section 6.6.1),
- With the MIRROR tool (see <u>Section 6.6.2</u>).

These methods are functional with an empty or burned part.

#### 6.3.3 User mode

#### 6.3.3.1 User mode definition

The User mode operation is used for final product test in an automotive environment.

User mode entry at power-on reset requires 0 V on the FS2400 debug pin.

When User mode is active, the device cannot access mirror registers content or perform OTP-related operations (emulation or programming).

#### 6.3.3.2 User mode activation

<u>Section 6.3.3.2.1</u> shows how to activate User mode:

#### 6.3.3.2.1 With the GUI using mode selection in Connection Toolbar

- 1. Set the default jumper configuration, see <u>Section 6.1</u>.
- 2. Plug the USB cable between the PC and the board.
- 3. Apply power supply VBAT.
- 4. Open the GUI and start the connection.
- 5. Check that the GUI started in User mode in the Connection Toolbar:



6. The equipment is set to User mode.

## 6.4 Generate a TBB script

A TBB file is needed to burn an OTP fuse or to emulate an OTP configuration by filling the Mirror registers through the Script tool.

To generate a TBB file, use the following procedure:

1. Go to the OTP tool from the Tools Access Bar:

| HEX HEX   |  | Lastered to see the  |  |
|---|--|--|--|
| Syst Save CFG ators Functional Safety Program ID Calculato                      | f.   | Load CFG Save CFG NXP  | Only - Customer Details  |
| Load CFG 2his: Impo   | art OTP configuration from a n   | reviously saved CEG file                                     | - Company Name*  |
| HSD Default CFG  MOD_EN_0 20131 HTPC  | are officialing and don notificial p   | reviously saved er o me                                      | Location   |
| HSD13_EN_OTP MOD_CONF_OTP Triangular  | modulation is selected   | nt OTB configuration to CEC file                             | Contact Name*  |
| LDTIM_EN_OTP  | Export curren  | in ore configuration to crd file                             | Contact e-mail*  |
|   |  |  | Phone Number*  |
| en OTP tool   |  |  | Address#1*   |
| 2: Chappe OTR can   | figuration fitting   | 3: Provide Customer and                                      | Program details  |
| SPLEN_OTP 2: CHOOSE OTP CON   | liguration nitting   |  |  |
| 12CDEVADDR_OTP 12C address is 0x20 • your applicati                             | on's needs   |  | Zip Code*  |
| AUL_CLK_IMHZ_ON No force  |  |  | Country  |
| ALL_CLK_20MHZ_ON No force   | X  |  | Other Info   |
| WAKEs Configuration   | LVIOs Confi  | iguration  | Program Details  |
| WK1PUPD_OTP WAKE1 internal pull down and pull up are configured as cell reper * | LVI03_SLOT_OTP LVI03 polarity is change  | ed in slot 0   | Program Name*  |
| WK2PUPD_OTP WAKE2 internal pull down and pull up are configured as cell reper - | LVIO3_HS_EN_OTP  |  | Application*   |
|   | LVIO3_LS_EN_OTP  |  | Production Date  |
|   | A REAL PROPERTY OF A REAL PROPER |  | Sample Date*   |
|   | LVIO3PUPD_OTP LVIO3 Internal pull down   | n and pull up are configured as cell repeat 💌                |  |
|   | LVIO3PUPD_OTP LVIO3 internal pull down<br>LVIO3_OUT_DFT_OTP  | n and pull up are configured as cell repeat 💌                | Other Info   |
|   | LVIO3PUPD_OTP LVIO3 Internal pull dows<br>LVIO3_OUT_DFT_OTP<br>LVIO4_SLOT_OTP LVIO4 polarity is change   | n and pull up are configured as cell repeat *                | Other Info Device Type F52320-QM   |
|   | LVIO3PUPD_OTP LVIO3 internal pull down<br>LVIO3_OUT_DFT_OTP<br>LVIO4_SLOT_OTP LVIO4_Polarity is change<br>LVIO4_HS_EN_OTP  | n and pull up are configured as cell repeat * ed in slot 0 * | Other Info<br>Device Type <u>F52320-QM</u><br>OTP ID A0<br>OTP Revision A          |
|   | LVIO3PUPD_OTP LVIO3 internal pull dow<br>LVIO3_OUT_DFT_OTP<br>LVIO4_SLOT_OTP LVIO4_HS_EN_OTP<br>LVIO4_LS_EN_OTP  | n and pull up are configured as cell repeat *                | Other Info Device Type F52320 QM OTP ID A0 OTP Revision Part Number PF52320AMMA0ES |

- 2. Configure the OTP parameters to fit the application's needs or import an OTP configuration from a previously saved CFG file.
- 3. Enter customer and program details in the window on the right side.
- 4. When done, export the OTP configuration directly to a TBB script by clicking **Export** in the Framework settings bar and choosing **TBB**:
- 5. Select the desired folder to save the TBB script:



6. TBB is now generated and saved, see Section 6.9.

## 6.5 First-start procedure: configure Watchdog as Infinite Time Out

This procedure is to be used as a first-start and allows the user to enter system-level Normal mode execution with the Watchdog configured with window fully opened (equivalent to disabled).

- 1. Set up and connect the KITFS2400FRDMEVM to the GUI using Section 6.1 and Section 6.2.
- If the FS2400 part is empty or if the configuration loaded from OTP should be modified:
   a. Enter Test mode using Section 6.3.2.2.
  - b. Load a configuration in the MIRROR tool, then write the configuration in the Mirror registers.
  - c. Select User mode from Connection Toolbar to exit Test mode.
- 3. Exit the OTP mode by switching OFF SW12.
- 4. Go to ACCESS Main Tab and click Read all to check that the device is in Normal and Debug mode.
- 5. Go to ACCESS WatchDog and configure watchdog with Infinite Time Out.

### KITFS2400FRDMEVM user manual

- 6. Exit Debug mode from ACCESS Main Tab in Device State box.
- 7. Under the WatchDog tab beside Good watchdog refresh, write "WD Answer Good" to get out of INIT state.

Figure 24 shows the sequence of steps (from step A to step I) to perform in ACCESS tool:

| Ø DiagSafety   | Ø Main Tab (  | () Regulators                      | 4 Interrupts   | and AMUX   | General IOs  | Physical Layers            |                       |
|--|---|------------------------------------|--|--|--|----------------------------|-----------------------|
|  |   |                                    |  |  |  | Read All                   | Write                 |
|  |   |                                    | General Flags  |  | Dev  | ice State                  |                       |
| Register Co  | ontent  | I2C/SPI Commu                      | nication Error No error                                  |  | Normal Mode  | Device is in Normal mode 🚽 |                       |
| Modulation is  | disabled  | Regulator Event                    | t Vx even<br>VSUP en                                     | ror reported   | Go to Normal Mode  |                            |                       |
| Triangular mo  | odulation is selected   | Wake-up Event                      | No even  | nt   | Exit OTP Mode  | Device is not in OTP mode  |                       |
|  |   | IO Event                           | No even  | nt   | Debug Mode   | Device is in Debug mode    | -                     |
|  |   | WD Even G                          | . Exit Debug   | -mode -  | Exit Debug Mode  |                            |                       |
|  |   | i o arene                          |  |  | LPON Mode  | Device is Not in LPON mode | •                     |
|  |   |                                    | Read   |  | Go to LPON   |                            |                       |
|  |   | -                                  | POD Elser  |  | INIT mode  | Device is in INIT mode     |                       |
|  |   | BAT Fail No b                      | pattery failure event                                    |  | Go to INIT   |                            |                       |
|  |   | POR Event No P                     | POR event  |  | Request Soft POR   |                            |                       |
|  |   | VBUS_UV NO E                       | event detected   |  |  |                            |                       |
|  |   | D. Rea                             | id and check   | current r  | modes  | Read                       |                       |
|  |   |                                    |  | ]  |  |                            |                       |
|  | Ε.  | Go to Wat                          | tchDog   |  |  |                            |                       |
| 801 Degister Man   | INTT Safety   | Ø watchDog                         | 1 DianSafety   | Main Tab   | () Regulators  | -                          |                       |
| nor register riop  | o har buicty  | 0 1101010-09                       |  | The second secon |  |                            | WHAT AT A T A         |
|  |   |                                    |  |  | C Regulators   | 7 anterrupta               | uthi AMUX             |
| WatchDog   |   | Watel days to                      |  |  | O Regulators   | 7 anteropta                | ullul AMUX            |
| WatchDog<br>Watchdog Type  | Watchdog Window is  | Watchdog Na<br>s disabled (Watchdo | anagement<br>Ig Time out)                                |  | ▼ N/N  | 7 anterrupts               | idbi AMUX             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh   | Watchdog Window is  | Watchdog Ma<br>s disabled (Watchdo | anagement<br>Ig Time out)<br>H. Send a G                 | Good WD  | <ul> <li>N/V</li> <li>Answer</li> </ul>                        | Last Recorded Error        | ann AMUX<br>Count: N/ |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh   | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog Ha<br>s disabled (Watchdo | anagement<br>Ig Time out)<br>H. Send a C                 | Good WD  | N/V<br>Answer  | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh   | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog Ha<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G                 | Good WD  | • N/V<br>Answer  | Last Recorded Error        | count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh   | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog Ha                        | anagement<br>ig Time out)<br>H. Send a C                 | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: NA             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMPO release  | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog PI<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G                 | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release script  | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog HJ<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G                 | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release script  | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog Hi<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G                 | Good WD .  | Answer   | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script   | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad   | Watchdog PL<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a C                 | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: NA             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wa  | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Comm<br>LIMPO Release scrip  | Watchdog Hu<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G                 | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wa  | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Comm<br>LIMPO Release scrip  | Watchdog Hi<br>s disabled (Watchdo | anagement<br>g Time out)<br>H. Send a G                  | Good WD .  | N/V<br>Answer  | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wat<br>Full error counter<br>WD Window Period           | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Comm<br>LIMPO Release scrip<br>Atchdog with<br>INFINITE Time Out,                          | Watchdog Hi<br>s disabled (Watchdo | anagement<br>og Time out)<br>H. Send a G<br>Fime Out     | Good WD .  | N/V<br>Answer<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V        | Last Recorded Error        | Count: NA             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wa<br>Full error counter<br>WD Window Period            | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Comm<br>LIMPO Release scrip<br>Atchdog with<br>INFINITE Time Out,                          | Watchdog Hu<br>s disabled (Watchdo | anagement<br>g Time out)<br>H. Send a G<br>Fime Out      | Good WD .  | N/V<br>Answer<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V        | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wa<br>Full error counter<br>WD Window Period            | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMP0 Release Comm<br>LIMP0 Release scrip<br>Atchdog with<br>INFINITE Time Out,                          | Watchdog Hi<br>s disabled (Watchdo | anagement<br>g Time out)<br>H. Send a G<br>Fime Out      | Good WD .  | N/V<br>Answer<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V               | Last Recorded Error        | Count: N/             |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wat<br>Fult error counter<br>WD Window Period           | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Comm<br>LIMPO Release scrip<br>Atchdog with<br>INFINITE Time Out, V<br>WD stays enabled in | Watchdog Hi<br>s disabled (Watchdo | anagement<br>g Time out)<br>H. Send a G<br>Fime Out      | Good WD .  | N/V<br>Answer<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V | Last Recorded Error        | Count: N/V            |
| WatchDog<br>Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh<br>LIMP0 release<br>LIMP0 release script<br>WD_TOKEN<br>F. Configure Wa<br>Fluit error counter<br>WD Window Period<br>Disable WD in LPON mode | Watchdog Window is<br>WD Answer Good<br>WD Answer Bad<br>LIMPO Release Com<br>LIMPO Release scrip<br>Atchdog with<br>INFINITE Time Out,<br>WD stays enabled in    | Watchdog Hu<br>s disabled (Watchdo | anagement<br>g Time out)<br>H. Send a G<br>Fime Out<br>d | Good WD  | N/V<br>Answer<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V | Last Recorded Error        | Count: N/V            |
#### KITFS2400FRDMEVM user manual

# 6.6 Operate OTP emulation by loading configuration in the Mirror registers

Mirror registers are where the OTP-fused registers are loaded at each FS2400 boot up. OTP emulation mode allows the user to directly write to Mirror registers to emulate any OTP-fused configuration. Mirror registers can be read/written multiple times, whereas OTP registers can only be burned once.

Mirror registers can be read and written in Test mode only, see <u>Section 6.3.2</u>.

In case of a power-on reset, the Mirror registers are reset to the default OTP configuration (default if OTP sectors are not burned).

The MIRROR tool provides access to all Mirror registers with a similar disposition to the OTP tool.

#### 6.6.1 Modify the Mirror registers with a TBB script

From an active Test mode environment, the Mirror registers can be configured using the SCRIPT tool, see <u>Section 7.5.4</u>.

1. In the SCRIPT tool, click **OPEN** in the Script bar to load the TBB script file into the Script Command Window.

*Note:* The script file must have an FS240x prefix, with x being the part version.



2. To execute the TBB script, click **RUN** in the bar at the bottom of the Script Command Window:



3. Deactivate Test mode to start the device with a given configuration and load the SPI functional registers with the Mirror registers content.

As a cross-check, for example, open the ACCESS tool and check the fields in the Regulators tab. If the operation completed without a problem, all fields should display the expected configuration.

#### 6.6.2 Modify the Mirror registers with the MIRROR tool

To configure the Mirror registers, the MIRROR tool can be used to write/read directly into Mirror registers, see <u>Section 7.5.5</u>). This requires Test mode to be activated.

1. Open the MIRROR tool from the Tool Access Bar:

| FS2    | 400 Stop test-mode ¥ Poling SI  | PI Freq (KHz): 2000 🐨 | Enable WatchDog Refresh   | Period 1 ms       | *              | NE  | 3   |
|--------|---|-----------------------|---|-------------------|----------------|---|-----|
| 0      | Log Window  | System Configurat     | tion Regulators   | Functional Safety | Program I      | ID Read All Write All OTP Import Export   |     |
| ACCESS | Filter Messages         Cold         Rev           F52400 [M, SY51_CFG:0x06]R:0x2008         F52400 [M, SY51_CFG:0x06]R:0x2008         F52400 [M, SY51_CFG:0x06]R:0x2008           F52400 [FS_WDW_CFG:0x08]R:0x2008         F52400 [FS_WDW_CFG:0x08]R:0x2008         F52400 [FS_WDW_CFG:0x08]R:0x2008 | CAN_EN_OTP            | System Configura  | ation             | N/V<br>N/V     | Read current mirror registers content   | 1.4 |
| Ope    | F52400 (F5_WD_IOKENC0.39)(50:5852<br>F52400 (F5 I WD_CFG:0x37)(7:60:5080<br>en Mirror tool   6(7:60:28d1<br>  W6x0000   | VSUP_UVTH_OTP         | VSUP_UVTH low threshold select<br>V1UVLP threshold is typical 1.8 V | ted (4.7 V)       | ▼ N/V<br>▼ N/V | Write current configuration to mirror registers<br>Import registers configuration from CFG file |     |
|        | FS2400 [M_TM_ENTRY:0x7]W:0x387<br>FS2400 [M_TM_ENTRY:0x7]W:0xb8ee<br>FS2400 [M_TM_ENTRY:0x7]W:0x087<br>FS2400 [M_TM_STATUS1:0x60]R:0x0022<br>FS2400 [M_TM_STATUS1:0x60]R:0x0022   |                       | Write   | lead              |                | Export current mirror registers configuration to CFG file                                       |     |

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- Configure Mirror registers to fit the application or load an existing configuration by importing a CFG file. Note: It is possible to load a configuration from a previously made configuration saved as a CFG file using Import from CFG at the top right of the Mirror tool.
- 3. Once done, click **Write All**. As a cross-check, click **Read all**. If the operation completed without a problem, all fields should display the expected configuration.

4. Exit OTP mode to start the device.

Note: It is possible to save a current mirror registers configuration as a CFG file using Export from CFG at the top right of the Mirror tool.

### 6.7 Programming an OTP operation

The PROG tool is used to permanently burn the FS2400 fuses with the customer's OTP configuration from a TBB script file. See Section 7.5.3 for further information.

The TBB script can be generated from an OTP configuration. See Section 6.4.

Note: The TBB file must have FS240x as a prefix, with x corresponding to part version.

An FS2400 device can be burned just twice.

Requirement: Make sure that the soldered device has not yet been burned before starting the burning process.

Follow this procedure to verify that the part is empty:

- 1. Set the default jumper configuration, see Section 6.1.
- 2. Set switch SW12 on (applies 8 V to debug pin).
- 3. Verify that jumper J30 is on (enables Debug mode/OTP mode entry).
- 4. Plug the USB cable between the PC and the board. Blue LED D14 for OTP 8 V generation and green LED D15 for debug is on.
- 5. Apply power supply VBAT (this action loads the mirror registers with burned OTP configuration if present).
- 6. Open the GUI and start the connection.
- 7. Switch to Test mode.
- 8. Open the PROG tool from the Tools Access Bar.
- 9. Check the flags in the Sector Flags section of the Fuse Box Status window, see Section 7.5.3.3 for further information.

The device is empty and can still be programmed two times when Sector 1 and 1bis flags are set to 0 (blue).

| Sector 1 Flags | Sector 1bis Flags |
|----------------|-------------------|
| used 🦲         | Fused             |
| Read           |                   |

Sector flags are set to 1 (orange) once the given sector is burned.

|  | g Window   | 0   | X    | Device Progra                         | mming   |               |      |
|--|--|---|------|---------------------------------------|---|---------------|------|
| Fit  | ter Messages 💌   | SAVE CLEAR  | UN I |                                       | Description Conflorentian                                   | omutede       |      |
| COG to<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS<br>FS | ) [M_SYS1_CF6:0x06)[R:0x<br>] [FS_WDW_CF6:0x38][R:0<br>] [FS_WD_TOKEN:0x38][R:0<br>] [FS_WD_TOKEN:0x38][R:0<br>2400 [FS_1_VD_CF6:0x37][R:0<br>2400 [FS_1_SYS1_CF6:0x37][R:0<br>2400 [M_TM_ENTRY:0x77][W:0<br>2400 [M_TM_ENTRY:0x77][W:0<br>2400 [M_TM_STATUS1:0x:60][R<br>2400 [M_TM_STATUS1:0x:60][R] | 2008<br>ko120<br>0x5ab2<br>x5080<br>0x28d1<br>0x0000<br>0xd5a7<br>0x068e<br>0x0f37<br>:0x0022<br>:0x0022<br>:0x0022 |      | Select TBB File<br>TBB File<br>Status | Browse<br>Not Selected<br>Not Ready<br>Select TBB script fi | OTP Mode Exit | rite |



# KITFS2400FRDMEVM user manual

| M_SYS1_CFG:0x06]R:   | SAVE CLEAR   |   |  |  |   |
|--|--|---|--|--|---|
| [M_SYST_CFG:0x00]K:  | 0.0000   | and a second  | Device Pro   | gramming Configuration   | OTP Mode  |
| [M_SYS1_CFG:0x06]R:<br>[FS_WDW_CFG:0x38]R<br>[FS_WD_TOKEN:0x39]I                       | 0x2008<br>0x2008<br>50x0120<br>R:0x5ab2  | 1   | Select TBB File  | Browse   | OTP Mode Exit   |
| [FS_I_WD_CFG:0x37]R  | 0x5080   |   | TBB File   | FS2400_MA2_TB8_Rev_A_202207  | TBB script file name is displaye  |
| [FS][FSSM_CFG:0x36]<br>[M_TM_ENTRY:0x7f]W<br>[M_TM_ENTRY:0x7f]W                        | /:0x0000<br>/:0xd5a7   |   | Status   | Ready  | Status is displayed   |
| [M_TM_ENTRY:0x7f]W<br>[M_TM_ENTRY:0x7f]W<br>[M_TM_STATUS1:0x60]<br>[M_TM_STATUS1:0x60] | /:0xb8ee<br>/:0x0f37<br>]R:0x0022<br>]R:0x0022   |   | Progra   | am Reset   |   |
|  | [FS_WD_TOKEN:0x39]<br>[FS_UD_TOKEN:0x39]<br>[FS_I_WD_CFG:0x37]R<br>[FS_I_FSSM_CFG:0x36]<br>[M_TM_ENTRY:0x7F]W<br>[M_TM_ENTRY:0x7F]W<br>[M_TM_ENTRY:0x7F]W<br>[M_TM_STATUS1:0x60]<br>[M_TM_STATUS1:0x60]<br>[M_TM_STATUS1:0x60] | Instruction         Instruction           IFS_WD_TOKEN:0x39]R:0x65ab2         IFS_I_WD_CF6:0x39]R:0x5ab2           IFS_I_FSSM_CF6:0x36]R:0x28d1         Immediate           M_TM_ENTRY:0x7T[W:0x0000         Immediate           M_TM_ENTRY:0x7T[W:0x0000         Immediate           M_TM_ENTRY:0x7T[W:0x0000         Immediate           M_TM_ENTRY:0x7T[W:0x0000         Immediate           M_TM_ENTRY:0x7T[W:0x0000         Immediate           M_TM_STATUS1:0x00]R:0x0022         Immediate           M_TM_STATUS1:0x60]R:0x0022         Immediate           M_TM_STATUS1:0x60]R:0x0022         Immediate | [F3_WD_TOLGN39]R0x5ab2<br>[F5_WD_TOLGN39]R0x5ab2<br>[F5_UD_CF6:0x39]R0x5ab2<br>[F5_UD_CF6:0x39]R0x5ab2<br>[M_TM_ENTRV:0x7]W:0x0000<br>[M_TM_ENTRV:0x7]W:0x063a7<br>[M_TM_ENTRV:0x7]W:0x063a7<br>[M_TM_ENTRV:0x7]W:0x0637<br>[M_TM_STATUS1:0x60]R0:0x0022<br>[M_TM_STATUS1:0x60]R0:0x0022<br>[M_TM_STATUS1:0x60]R0:0x0022 | The UNITY Construction         Select 100 File           TFS_UVD_CICKEN.cbs309.cbx3ab2         TBB File           [FS_UVD_CICKEN.cbs309.cbx3ab2         TBB File           [FS_UVD_CICKEN.cbs309.cbx3ab2         TBB File           [M_TM_ENTRY:dbx711W:cbx0000         Status           [M_TM_ENTRY:dbx711W:cbx3637         Status           [M_TM_ENTRY:dbx711W:cbx0637         Progr.           [M_TM_STATUS:dbx60]Rcbx0022         Progr.           [M_TM_STATUS:dbx60]Rcbx0022         Progr. | TM_UTV_CTRUSSIONCLO         Display the second |

#### 11. Click Program.

12. One pop-up window will appear asking for confirmation of burning. Confirm to launch the burning process.

At the end of OTP programming, change the jumper configuration and restart the device completely before going to User mode.

A good check is to use the AMUX panel in the ACCESS tool to see if all regulator voltages are correct.

### 6.8 Save a routine from Log window then Run it as a Script

The Log window shows the requests and answers transiting between the S32K144 MCU and the FS24 device. The commands are sent using SPI protocol depending on the user's choice.

Requests are sent to the FS24 after a user action on the graphical interface and answers are received by the MCU, then displayed to the user in the graphical interface. These exchanges are stored in the Log file.

When the user needs to operate the same routine multiple times on the device (for example, to automate loading the Mirror registers, as shown in the example <u>below</u>), it is possible to record the actions from the Log file, then run the routine later as a Script.

#### KITFS2400FRDMEVM user manual



In the .txt file, it is possible to add:

- · A delay between successive commands using DELAY:xx command with xx the delay in milliseconds
- A pause in the script (for example to have time to change hardware configuration between two commands) using PAUSE command
- A comment in the script using // at line start

#### 6.9 TBB script example

This TBB script corresponds to FS2400 HVBUCK ASIL B version an is given as an example code.

UM12014

40 / 79

#### KITFS2400FRDMEVM user manual

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UM12014 User manual

# 7 Tool interface (GUI) description

### 7.1 Framework window

The Framework window consists of the following sections:

- **Connection Toolbar:** Used to start communication with the device, enter or exit User/Debug/Test modes, fix SPI frequency and main address, enable watchdog.
- Framework settings: Manages file import/export and Framework configuration.
- Window log: Reports USB and Device communication events.
- **USB and Device status:** Indicates if USB or Device is connected or disconnected, shows communication protocol (SPI), shows firmware and GUI version, displays current device mode.
- Tools access bar: Provides quick access to the FS24 evaluation tools and features.
- Tab content: Shows the content of each tool or tab. There may be several tabs, boxes, or windows inside.



Figure 25. Framework window

If Log window, Tools Access Bar, or Connection Toolbar do not appear on the screen, right-click on the Framework settings bar. This action displays three selection boxes (checked if display is active):

- Log window
- Tabs correspond to the Tools Access Bar
- ConnectionMgr



### 7.2 Framework settings

<u>The Framework settings section</u> appears at the top left corner of the Framework window. It consists of four items:

- File
- View
- Import/Export
- Help

|                               | File | View        | Imp | port/Expo | rt Help |
|-------------------------------|------|-------------|-----|-----------|---------|
|                               | F    | <b>S240</b> | 0   | Start     | user-mc |
| Figure 26. Framework settings |      |             |     |           |         |

#### 7.2.1 File menu item

The file menu item is used to set the current GUI as default or exit the application.

|                          | File View Import/Export Help Use Default Configuration Exit |  |
|--------------------------|---|--|
| Figure 27 File monu item | aaa-054167  |  |

- Use Default Configuration: Check the box to always open the NXP GUI as the current GUI version. Uncheck the box to display the product selection box at the NXP GUI startup.
- Exit: Exits the NXP GUI application.

#### 7.2.2 View menu item

The View menu item contains the following options:

- Display
- Show
- Naming Conventions
  - Display: Allows the user to enable or disable the Connection Toolbar (enabled by default).

| File V | /iew Import/Export Help |                       |                     |            |  |  |
|--------|-------------------------|-----------------------|---------------------|------------|--|--|
|        | Display                 | ✓ Connection Tool Bar |                     |            |  |  |
| FS     | Show                    | •                     | Polling SPI Fred (K |            |  |  |
| 10     | Naming Conventions      | •                     |                     | Register   |  |  |
|        |                         |                       |                     | aaa-054168 |  |  |

- Show: Allows the user access to various sections of the GUI and displays the Log window.

### KITFS2400FRDMEVM user manual



Naming Conventions: Allows the user to select Friendly or Register name display for the OTP tool. This
option is enabled only when the OTP tool is active.

| View | Import/Export Help | ) |          |            |
|------|--------------------|---|----------|------------|
| D    | lisplay            | + |          |            |
| S    | how                |   | Polling  | <b>P</b> 1 |
| N    | laming Conventions |   | Friendly |            |
| F    | ilter Messages     | * | Register |            |
|      |                    |   | aaa-054  | 170        |

- The naming convention options are:
  - **Friendly:** Register names are displayed as user-friendly names in the OTP tool.
  - Register: Register names are displayed by their technical names in the OTP tool.

#### 7.2.3 Import/Export menu item

The Import/Export menu item allows the user to manage the files needed for Mirror emulation, for the PROG tool, and for GUI configuration. This menu item is only active when the OTP tool has been selected. See <u>Section 7.5.2</u> for details.



aaa-054171

- **TBB:** Exports the OTP configuration into a TBB script file that can be used to load the Mirror registers using the SCRIPT tool. The same file can be used by the PROG tool to burn OTP fuses.
- HEX: Outputs the OTP configuration in I-HEX (Intel Hex) or S-HEX (Simple Hex) script file format.
- Save CFG: Used to save the current configuration as a CFG file.
- Load CFG: Used to load a previously saved configuration from a CFG file.
- Default CFG: Used to load a predefined OTP configuration in QM or ASIL B to use as a starting point.

#### 7.2.4 Help menu item

The Help menu item contains links to additional information, allows the user to display GUI and firmware version numbers, and contains a glossary for acronyms.

- Documentation: Lists online NXP documentation related the FS2400 GUI.
- About: Displays the version number of the GUI currently installed.
- Glossary: Lists expanded names for acronyms used in the GUI.

|                           | File Vie | w Import/Export | Help                   |  |
|---------------------------|----------|-----------------|------------------------|--|
|                           | FS24     | 00 Start us     | Documentation<br>About |  |
|                           |          | System Confi    | Revision History       |  |
|                           | ACCESS   |                 | Glossary               |  |
|                           |          |                 | aaa-054172             |  |
| Figure 28. Help menu item |          |                 |                        |  |

### 7.3 Connection Toolbar

The Connection Toolbar menu is directly below the Framework settings menu at the top left of the Framework window.

| FS2400 | Start | user-mode | Pollind | SPI Freq (KHz): 2000 - | Enable WatchDog Refresh | Period 1 ms |            |
|--------|-------|-----------|---------|------------------------|-------------------------|-------------|------------|
|        |       |           |         |                        |                         |             | aaa-054173 |

**Note:** The Connection Toolbar is not displayed if not selected in the Frameworks settings>View>Display menu item.

The Connection Toolbar allows the user to start or stop communication with the device, enter or exit User/ Debug/Test modes, fix the SPI frequency (depending on the chosen MCU communication protocol at start), and enable watchdog.

The Connection Toolbar consists of the following items:

- Start/Stop: Open or close communication with the device
- Mode: Select between Debug mode, Test mode, and User mode
- **Polling:** When on, the GUI detects a mode change by polling mode status
- SPI Frequency: Set the SPI frequency in kHz
- Enable Watchdog Refresh: Enable/disable the Watchdog refresh
- Period: Set the Watchdog period

#### 7.3.1 Device connection

The device connection boxes appear first in the Connection Toolbar menu.

| Device conne | ection              |                        |                 |                         |             |            |
|--------------|---------------------|------------------------|-----------------|-------------------------|-------------|------------|
| FS2320 Start | user-mode 💌 Pollind | I2C Frequency(KHz):400 | Main ADD: 0x2 * | Enable WatchDog Refresh | Period 1 ms |            |
|              |                     |                        |                 |                         |             | aaa-054174 |

When the S32K144 MCU is not connected through the USB port, the State indicator in the USB and Status bar shows "NOT DETECTED", the FS2400 header text appears red, and the **Start** button is not available.

| MCU: | S32K144 | State: | NOT DETECTED | FS2400 | Start | user-mode * |
|------|---------|--------|--------------|--------|-------|-------------|
|      |         |        |              |        |       | aaa-054176  |

UM12014

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### KITFS2400FRDMEVM user manual

After the USB cable is connected, the State indicator displays "DISCONNECTED" and the **Start** button becomes available.

# MCU: S32K144 State: DISCONNECTED FS2400 Start user-mode =

Click Start to start communication with the FS2400.

At this point, the State indicator displays "CONNECTED" and the FS2400 header text changes from red to green.

| MCU: | S32K144 | State: | CONNECTED | FS2400 | Stop | user-mode 🔻 |
|------|---------|--------|-----------|--------|------|-------------|
|      |         |        |           |        |      | aaa-054177  |

Usually, once connected, the next step is to load an OTP configuration and write it in the Mirror registers.

#### 7.3.2 SPI communication configuration

The FS2400 supports SPI communication protocol. SPI configuration settings for the MCU side appear second in the Connection Toolbar, allowing the user to choose the protocol frequency and device address, if applicable.

| FS2400 Stop user-mode  Polling | SPI Freq (KHz): 600 🔻 | Enable WatchDog Refresh | Period 1 ms | •          |
|--------------------------------|-----------------------|-------------------------|-------------|------------|
|                                | SPI configuration     |                         |             | aaa-054178 |

#### 7.3.3 Watchdog management

The FS2400 provides watchdog monitoring with keys as a functional safety feature. The watchdog feature can be disabled only in the QM version. The watchdog is always enabled for an ASIL B part.

#### 7.3.3.1 Watchdog enablement and configuration on FS2400 side

On the FS2400 side, the watchdog can be disabled by OTP. Watchdog monitoring default configuration is done via SPI via the ACCESS tool.

### KITFS2400FRDMEVM user manual

| System Configuration UMP0_EN_OTP UDTIM_EN_OTP ERRMON_EN_OTP FIRST_FAULT_EN_OTP FIRST_FAULT_EN_OTP FIRST_SUBS_DIS_OTP RSTB 8s timer is enabled RSTB_DUR_OTP 10 ms WD_INF_OTP Watchdog Is enabled Configuration via SPI (ACCESS tool): Ctchdog configuration via SPI (ACCESS tool): Ctchdog configuration via SPI (ACCESS tool): Ctchdog configuration via SPI Freq (012) COC Enable Watchdog Refresh Period Comparison of the second of  | 1 | System Configuration                   | Regulators F  | unctional Safety   | Program ID  | Calculator           |            |            |   |
|---|---|--|---------------|--|---|----------------------|------------|------------|---|
| LIMP0_EN_OTP<br>LDTIM_EN_OTP<br>ERRMON_EN_OTP<br>FIRST_FAULT_EN_OTP<br>FIRST_FAULT_EN_OTP<br>RSTB 8s timer is enabled<br>RSTB_DUR_OTP<br>10 ms<br>WD_INF_OTP<br>Watchdog is enabled<br>Chdog configuration via SPI (ACCESS tool):<br>PGUI (DEV_build) - F52400 - 3.1.364<br>Triew Import/Export Help<br>Configuration via SPI freq (Kitz): 2000 Enable WatchDog Refresh Period Imm<br>Watchdog VatchDog @ DiagSafety @ Main Tab<br>Fiter Messages<br>Copen WatchDog tab<br>Watchdog Vindow is deabled (Vatchdog Time out) N/V<br>Bad watchdog refresh<br>LIMP0 release command<br>LIMP0 |   |  | Syste         | m Configuration  |   |                      |            |            |   |
| LDTIM_EN_OTP<br>ERRMON_EN_OTP<br>FIRST_FAULT_EN_OTP<br>FIRST_FAULT_EN_OTP<br>RSTB 8s timer is enabled<br>RSTB_DUR_OTP<br>10 ms<br>WD_INF_OTP<br>Watchdog is enabled<br>Chdog configuration via SPI (ACCESS tool):<br>PGUI (DEV_build) - Fs2400 - 3.1.364<br>Tere Import/Export Help<br>Filer Messages<br>Common Help<br>Filer Messages<br>Common Help<br>Filer Messages<br>Enable Watchdog Ranagement<br>2: Open WatchDog tab<br>Bad watchdog refresh<br>UMPO release common<br>LIMPO release common<br>LIMPO release common<br>UMD Answer Good<br>UMD Answer Good<br>UMD Orelease script<br>UMD Newer Bad<br>LIMPO release script<br>VO_TOKEN<br>Filer WatchDog parameters<br>T<br>VO_TOKEN<br>T<br>VO_TOKEN<br>T<br>Copen ACCESS tool<br>Write<br>Read<br>Error Counters Limit  |   | LIMP0_EN_OTP                           |               |  |   |                      |            |            |   |
| ERRMON_EN_OTP<br>FIRST_FAULT_EN_OTP<br>DoNot GoTo FS at first fault<br>RSTBBS_DIS_OTP<br>RSTB 8s timer is enabled<br>RSTB_DUR_OTP<br>10 ms<br>WD_INF_OTP<br>Watchdog is enabled<br>Chdog configuration via SPI (ACCESS tool):<br>2 GUI (DEV_build) - F52400 - 3.1.364<br>iew import/Export Help<br>Help<br>Help<br>Help<br>Help<br>Cog Window<br>Filter Messages<br>Vatchdog Management<br>2: Open WatchDog tab<br>Bad watchdog refresh<br>UNPO release cript<br>WD_TOKEN<br>Enter WatchDog parameters<br>T<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V   |   | LDTIM_EN_OTP                           |               |  |   |                      |            |            |   |
| FIRST_FAULT_EN_OTP       DoNot GoTo FS at first fault         RSTB8S_DIS_OTP       RSTB 8s timer is enabled         RSTB_DUR_OTP       10 ms         WD_INF_OTP       Watchdog is enabled         vUl (DEV_build) - FS2400 - 3.1.364         iew       Import/Export         Log Window       Period         Vatchdog Stat       Period         Vatchdog Window       WatchDog         Vatchdog Vindow       WatchDog         Vatchdog Vindow       WatchDog         Vatchdog Vindow       N/V         Vatchdog refresh       U/V Answer Good         Bad watchdog refresh       U/PO Release Command         UMPO release script       N/V         WD_TOKEN       N/V         WD Window Period       N/V         VO Answer Bad       N/V         UMPO release script       N/V         N/V       N/V         WD_TOKEN       N/V         VD Window Period       N/V         VM Window Period       N/V         UNV On LPON       Wo stays enabled in LPON <td></td> <td>ERRMON_EN_OTP</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>  |   | ERRMON_EN_OTP                          |               |  |   |                      |            |            |   |
| RSTB85_DIS_OTP RSTB 8s timer is enabled<br>RSTB_DUR_OTP 10 ms<br>WD_INF_OTP 10 ms<br>wtdthdog is enabled<br>rout(DEV_build) - FS2400 - 3.1.364<br>iew Import/Export Help<br>400 Start ser-mode Point SPI Freq (1912): 200 Enable WatchDog Refresh Period Ims<br>Log Window Point SPI Freq (1912): 200 Enable WatchDog Refresh Period Ims<br>Watchdog VatchDog © DiagSafety Ø Main Tab<br>Watchdog VatchDog © DiagSafety Ø Main Tab<br>Watchdog VatchDog Tab<br>Watchdog VatchDog Tab<br>Watchdog Vatchdog Time out) N/V<br>Filter Messages WatchDog tab<br>WD_Answer Good<br>UMP0 release script UMP0 Release Script<br>UMP0 Release Script<br>WD_TOKEN<br>Enter WatchDog parameters T<br>N/V<br>WD Window Period DiagNered N/V<br>Diable WD on LPON WD stays enabled in LPON N/V<br>WD table WD on LPON Workers Limit  |   | FIRST_FAULT_EN_OTP                     | DoNot GoTo    | FS at first fault  |   | +                    | ]          |            |   |
| RSTB_DUR_OTP       10 ms         WD_INF_OTP       Watchdog is enabled         cchdog configuration via SPI (ACCESS tool):         rgUI (DEV_build) - FS2400 - 3.1.364         iew import/Export         400       Start         Log Window       Period         Vert Import/Export         Help         400       Start         Vert Import/Export       Help         Vert Import/Export       Help         400       Start         Vert Import/Export       Help         Vert Import/Export       Help         400       Start         Vert Import/Export       Help         Vert Import/Export       Help         Vert Import/Export       WatchDog         Vert Import       WatchDog         Vert Import       WatchDog         Vert Import       WatchDog         Vert Import       WatchDog Vert Import         Vert Import Import </td <td></td> <td>RSTB8S_DIS_OTP</td> <td>RSTB 8s time</td> <td>r is enabled</td> <td></td> <td>•</td> <td>]</td> <td></td> <td></td>   |   | RSTB8S_DIS_OTP                         | RSTB 8s time  | r is enabled   |   | •                    | ]          |            |   |
| WD_INF_OTP Watchdog is enabled  Chdog configuration via SPI (ACCESS tool): COULDEV_build) - F52400 - 3.1.364  We Import/Export Help  Log Window Poling SPI Freq (KHz): COOP Enable WatchDog Refresh Period Ims Fitter Messages Watchdog Tene WatchDog Color Poling SPI Freq (KHz): COOP Enable Watchdog WatchDog @ DiagSafety @ Main Tab Watchdog Window is disabled (Watchdog Tene out)  Watchdog Window is disabled (Watchdog Time out)  WD Answer Good Bad watchdog refresh UMPO release script WD TOREN  Enter WatchDog parameters T WD TOREN  LIMPO Release script WD Window Feriod UMPO Release script WD Window Feriod UMPO Release script WD Window Feriod UMPO Release script WD Window Feriod USable WD on LPON WD stays enabled in LPON NV   |   | RSTB_DUR_OTP                           | 10 ms         |  |   | •                    | 1          |            |   |
| chdog configuration via SPI (ACCESS tool):<br>2 GU (DEV_build) - F52400 - 3.1.364<br>iew Import/Export Help<br>400 Start vermed Poing SPI Freq (KHz): 200 Enable WatchDog Refresh Period Ims V<br>Fiter Messages V WatchDog VatchDog VatchDog O DiagSafety O Main Tab<br>Fiter Messages V WatchDog tab<br>NV Watchdog Window is disabled (Watchdog Time out) V N/V<br>2: Open WatchDog tab<br>Bad watchdog refresh WD Answer Bad<br>LIMP0 release script LIMP0 Release Command<br>LIMP0 release script UMP0 Release Script N/V<br>NV<br>NV<br>NV<br>NV<br>NV<br>NV<br>NV<br>NV<br>NV<br>N   |   | WD_INF_OTP                             | Watchdog is a | enabled  |   |                      | 1          |            |   |
| LIMP0 release script<br>WD_TOKEN<br>T<br>Enter WatchDog parameters<br>T<br>VD_TOKEN<br>T<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>WD Window Period<br>Disable WD on LPON<br>MD stays enabled in LPON<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/  |   | Log Window<br>Filter Messages *<br>2:0 | pen Wato      | WatchDog<br>WatchDog<br>ChDog tab<br>Bad watchdog refresh<br>LIMP0 release | Vatchdog Wind<br>Watchdog Wind<br>WD Answer G<br>WD Answer F<br>LIMP0 Release C | WatchDog             | DiagSafety | ♥ Main Tab | Ċ |
| Interval       N/V         N/V       N/V         WD Window Period       INFINITE Time Out, Window fully opened       N/V         Disable WD on LPON       WD stays enabled in LPON       N/V         1: Open ACCESS tool       Write       Read         Error Counters Limit  |   | Enter WatchDo                          | a param       | LIMP0 release script<br>WD_TOKEN   | LIMP0 Release   | script               |            | N/V<br>N/V |   |
| WD Window Period       INFINITE Time Out, Window fully opened       • N/V         Disable WD on LPON       WD stays enabled in LPON       • N/V         1: Open ACCESS tool       Write       Read         Error Counters Limit   | • |  |               |  |   |                      |            | N/V<br>N/V |   |
| Disable WD on LPON WD stays enabled in LPON N/V  1: Open ACCESS tool  Write Read  Error Counters Limit  |   |  |               | WD Window Period   | INFINITE Time C   | Out, Window fully op | ened       | ▼ N/V      |   |
| 1: Open ACCESS tool Write Read Error Counters Limit   |   |  |               | Disable WD on LPON   | WD stays enable   | ed in LPON           |            | ▼ N/V      |   |
| Error Counters Limit  | I |  | CCESS too     |  |   | Write Re             | ad         |            |   |
|   | ] | 1: Open AG                             |               |  |   |                      |            |            |   |
| WD_ERR_LIMIT N/V  | ] | 1: Open Ad                             |               |  |   | Fror Counters Li     | nit        |            |   |

# 7.3.3.2 Watchdog configuration on MCU side

On the S32K144 MCU side, actions are configured with the watchdog management boxes in the Connection Toolbar menu.

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UM12014
User manual
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KITFS2400FRDMEVM user manual

|        |      |             |         |                      |   | Watchdog managem        | ent    |      |          |
|--------|------|-------------|---------|----------------------|---|-------------------------|--------|------|----------|
| FS2400 | Stop | test-mode 💌 | Polling | SPI Freq (KHz): 2000 | ¥ | Enable WatchDog Refresh | Period | 1 ms | •        |
|        |      |             |         |                      |   |                         |        |      | aaa-0541 |

Figure 30. Watchdog enablement boxes

The mechanism is fully operational when both SBC and MCU actions are enabled and have matching configurations.

The steps for watchdog enablement are described in Figure 31:

|                |  |                         | 1: Select Watc          | hdog period        |                |
|----------------|--|-------------------------|-------------------------|--------------------|----------------|
|                |  | 2: Check Enable W       | atchdog Refresh box     |                    |                |
| FS2            | 400 Stop test-mode <b>v</b> Polling S  | PI Freq (KHz): 2000 🔻 🛛 | Enable WatchDog Refresh | Period 256 ms (det | fault value) 🔻 |
| 0              | Log Window   | 1001 Register Map       | INIT Safety             | Ø WatchDog         | 🛛 DiagSa       |
| ACCESS         | FS2400 [FS_WDW_CFG:0x38]W:0x0120<br>FS2400 [ WD Refresh 1 Enabled Simple 256ms | Regulators              |                         |                    |                |
| 3:             | : Corresponding Log message is displaye  | d                       |                         | V1 HVBUCK          | aaa-054180     |
| 3:<br>Figure ( | : Corresponding Log message is displaye  | d                       |                         | VI HVBDCK          | aaa-05         |

- 1. Select the watchdog period via the Period selection box in the Connection Toolbar.
- 2. Enable WatchDog Refresh by checking corresponding box to enable watchdog monitoring on the MCU side.
- 3. A message is displayed in the Log window with the selected period and type values.

If the Period selection box is unavailable, verify that Enable WatchDog Refresh is unchecked.

| F52    | 400       | Stop    | test-mode      | •    | Polling | PI Freq (KHz) | ): 2000      | Enable WatchDog Refresh | Period 256 ms (def | ault value) 🔻 |
|--------|-----------|---------|----------------|------|---------|---------------|--------------|-------------------------|--------------------|---------------|
| 0      | Log Win   | dow     |                |      | 0 🗙     | 0101          |              | M BUT Color             | Ø water            |               |
| ACCESS | Filter Me | ssages  | *              | SAVE |         | 1001 H        | Register Map | V INIT Safety           | VivatchDog         | V Diags       |
| ACCESS | FS2400 [  | WD Refr | resh ] Disable | ed   |         | Regula        | ators        |                         |                    |               |
| Appe   | ars if us | er und  | hecks Ena      | able | Watchdo | g Refresh     |              |                         |                    |               |

Figure 32. Enable WatchDog Refresh unchecked

### 7.4 USB and Device status bar

The USB and Device status bar is at the bottom of the Framework window. This toolbar gives information about the GUI and the firmware version, about the USB connection status, and about FS24 active modes.



### KITFS2400FRDMEVM user manual

The Tools access bar appears in a vertical row along the left side of the Framework window. The

bar provides access to tools that implement various GUI functions. The Tools access bar consists

• OTP: Used to create and save OTP configuration files, with customer and program details

• ACCESS: Provides access to I<sup>2</sup>C/SPI registers via Register Map or thematic tabs

• POWER: Provides a power dissipation analysis of the FS2400

· PROG: Used to manage OTP fuse-burning process

SCRIPT: Used to create, open, save, and run scripts
MIRROR: Provides access to Mirror registers

• MCU PINS: Used to read and set MCU I/O pins

· CAN: Used to manage CAN communication

### 7.5 Tools access bar

of nine items:



aaa-054182

#### **7.5.1 POWER**

The POWER tool is used to compute the power dissipation of the device in a given application.

<u>Figure 34</u> shows how to use the POWER tool. Input values are selected on the interface from keyboard inputs or selection boxes. Results are shown in the green boxes. Power dissipation graphs can be displayed from the interface using **System Power Dissipation** or **IC Power Dissipation** buttons. **Load Power Config** and **Save Power Config** buttons are used to resume the power dissipation calculation later, by saving and loading a text file.

#### KITFS2400FRDMEVM user manual

| POWER        | in the first of the second   | 61-  | Import values fro   | om a pre-defined   | Resume the calculation later |
|--------------|--|--|---|--|------------------------------|
|              | A configuration from a CFG<br>System Power Dissipation IC Power Dissipation<br>INPUT PARAMETERS<br>VIN DC 14.0 ‡ V<br>Rthja 40 ‡ ¢C/W<br>SYSTEM POWER DISSIPATION<br>PDIS, EXT 0.0201644 W<br>PDIS, EXT 0.0201644 W<br>PDIS, SYS 0.632946 W<br>Ta,init,max 125,489 °C<br>VBOS<br>PDIS 0.14 W | File           NUT         3.300         ↓ V           10UT         0.400         ↓ A           fswitch         2200         ↓ H×z           10UT         0.400         ↓ A           fswitch         2200         ↓ H×z           10LT         0.400         ↓ A           fswitch         200         ↓ H×z           10.0         ♥ ns         ns           Cn, ESR         10.0         ♥ ns2           Cout,ESR         3.10         ♥ m2           Lool         4.70         ↓ H           Lool,DCR         120.00         ♥ m2           IOUT,TOT         0.4         A           POUT         1.32         W           POIS, ECT         0.203644         W           POIS, EXT         0.021644         W           Fedramen         26.30         \$ | Import Values fri<br>confi<br>Umport OTP Config<br>VOUT 5.0 V<br>IOUT 0.000 C A<br>IOUT, TOT 0.0111667 A<br>POUT 0.0558333 W<br>PDIS 0.1005 W | ma pre-defined<br>ig file<br>mort Mirror Config<br>CAN Supplied to V3 T<br>Bus Load 50.0 Enable or disable the CAN<br>Node Contribution 50.0 0 0 0 0<br>CAN is short-Circuit I<br>IAvg 11.1667 mA<br>POIS 0.0335 W | Resume the calculation later |
| Figure 34 Po | ower tool  | Cincersy Processo 78   | J   |  |                              |

### 7.5.2 OTP

The OTP tool allows the user to enter an OTP configuration. The OTP configuration can be saved as a CFG file or exported as a TBB file:

- **CFG file**: Used by the GUI to log all of the configuration information. The CFG file can be used to save an OTP configuration or load Mirror registers with the MIRROR tool in OTP mode.
- **TBB file**: Can be created with a .txt extension. The TBB file can be used to load the Mirror registers with the SCRIPT tool in OTP mode or to burn OTP fuses using the PROG tool.

The OTP configuration is not addressed here. For information about OTP configuration, refer to <u>the FS2400</u> <u>data sheet</u>.

The OTP tool's main panel is divided into two windows:

- OTP Parameters Setting window
- OTP Details window

#### KITFS2400FRDMEVM user manual



Figure 35. OTP Tool tabs

#### 7.5.2.1 OTP Parameters Setting window

The OTP Parameters Setting section is organized into four tabs.

#### 7.5.2.1.1 System Configuration tab

The System Configuration tab provides a means of setting miscellaneous FS2400 system configuration parameters, including clock, I/Os, and power-up sequence configuration. The tab displays the set power-up sequence as a graph in the Sequence Diagram box.

#### KITFS2400FRDMEVM user manual



### 7.5.2.1.2 Regulators tab

The Regulators tab allows the user to set OTP parameters for V1 - HVBUCK and V3 - HVLDO. The tab displays a simplified diagram of the selected configuration as a visual cross-check.

| Block Diagram        | ety Program ID Calculato  | VI HVBUCK Configuration   | Load Cro Jave Cro Juxe Only   |
|----------------------|---|---|---|
| Block Diagram        | BUCK_SRHSOFF_OTP<br>BUCK_SRHSON_OTP<br>BUCK_CCOMP_OTP<br>BUCK_CCOMP_OTP<br>BUCK_SC_OTP<br>BUCK_AVG_OC_PWM_OTP<br>BUCK_PK_OC_PWM_OTP<br>BUCK_PFM_TON_OTP<br>BUCK_PFM_TOFF_OTP<br>VV1_BUCK_OTP<br>VV1_LP_BUCK_OTP<br>BUCK_LP_DVS_OTP<br>BUCK_RRV_LV_OTP<br>VBOS2V1_SW_LP_EN_OTP | VI HVBUCK Configuration HS falling slew rate is 20 ns HS raising slew rate is 20 ns I2 pf I300 KOhm Slope Comp. for V1 = 5V, L = 15uH Average current detection threshold is 200 r Overcurrent (peak) threshold is 400 mA Overcurrent (peak) threshold is 400 mA TON time in PFM isns (to be computed) TOFF time in PFM is 605 ns 3.2 V 3.2 V I22.5 mV/us LS reverse recovery delay is 5 ns | •<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>• |
| V3 Configuration     | BUCK_UC_DGLT_UTP  | overcurrent deglitcher is 250 µs  |   |
| VV3_OTP V3 = 3.3 V · |   |   |   |
|                      |   |   | aaa-05420   |

Figure 37. Regulators tab

### 7.5.2.1.3 Functional Safety tab

The Functional Safety tab allows the user to set OTP safety-related parameters, such as voltage monitoring, LIMP function, and other safety-related system configurations. This tab displays a different window depending on the device's safety level (QM or ASIL B) selected in the Program Details panel, see <u>Section 7.5.2.2</u>.

#### KITFS2400FRDMEVM user manual

| System Configuration | Regulators | Function      | nal Safety     | Program ID   | Calculato       | r .                                |                                  |
|----------------------|------------|---------------|----------------|--------------|-----------------|------------------------------------|----------------------------------|
|                      | 5          | System Config | juration       |              |                 |                                    |                                  |
| LIMP0_EN_OTP         |            |               |                |              |                 |                                    |                                  |
| LDTIM_EN_OTP         |            |               |                |              |                 |                                    |                                  |
| ERRMON_EN_OTP        |            |               |                |              |                 |                                    |                                  |
| FIRST_FAULT_EN_OTP   | DoNot Go   | oTo FS at fi  | rst fault      |              |                 |                                    |                                  |
| RSTB8S_DIS_OTP       | RSTB 8s    | timer is ena  | bled           |              |                 | •                                  |                                  |
| RSTB_DUR_OTP         | 10 ms      |               |                |              |                 | *                                  |                                  |
| WD_DIS_OTP           | Watchdog   | g is enabled  |                |              |                 | •                                  |                                  |
| FS_DUR_CFG_OTP       | FS state o | duration is t | 100 ms         |              |                 | •                                  |                                  |
| FS_LPOFF_OTP         | Automati   | c restart aft | er FS state    |              |                 | •                                  |                                  |
| INIT_CRC_DIS_OTP     | CRC is en  | abled         |                |              |                 | •                                  |                                  |
| CRC_INV_OTP          | CRC calcu  | ulation resu  | lt is not inve | rted         |                 | •                                  |                                  |
| CRC_DBG_DIS_OTP      | SPI CRC    | is enabled v  | while DEBUG    | mode         |                 | •                                  |                                  |
| CRC_DIS_OTP          | SPI CRC    | is enabled    |                |              |                 | •                                  |                                  |
| V1_UV_PW_EN_OTP      | Monitor d  | lisabled      |                |              |                 | •                                  |                                  |
| CONF_OV_V1_OTP       | The V1 is  | disabled in   | case of OV     |              |                 | •                                  |                                  |
| CONF_OV_V3_OTP       | The V3 is  | disabled in   | case of OV     |              |                 | •                                  |                                  |
|                      |            |               |                |              | Voltage I       | Ionitoring                         |                                  |
| EN                   | l Voltage  | UV TH         | OV TH          | UV Deglitche | r OV Deglitcher | UV RSTB Impact                     | OV RSTB Impact                   |
| VMON0 RES            | 1.0 V      | 64 % *        | 102.5 %        | 5 us 👻       | 25 us -         | VMON EXT UV Does not assert RSTB * | VMON_EXT_OV Does not assert RSTB |
| VMON1_DAC Enab       | led        | 64 % 🔻        | 102.5 %        | 5 us 💌       | 25 us 🔻         | V1 UV does not assert RSTB         | V1 OV Does not assert RSTB       |
| VMON3 DAC Enab       | led        | 64 % *        | 102.5 %        | 5 us *       | 25 us *         | V3 UV does not assert RSTB         | V3 OV does not assert RSTB       |

Figure 38. Functional Safety tab

#### 7.5.2.1.4 Program ID tab

The Program ID tab displays the OTP ID. Only NXP users can create a new OTP ID.

|                           | System Configuration | Regulators | Functional Safety | Program ID | Calculator |
|---------------------------|----------------------|------------|-------------------|------------|------------|
|                           | Program ID           |            |                   |            |            |
|                           | PROG_IDL_OTP         |            |                   |            |            |
|                           | PROG_IDH_OTP 0       |            |                   |            |            |
|                           |                      |            |                   |            | aaa-054210 |
| Figure 39. Program ID tab |                      |            |                   |            |            |

### 7.5.2.2 OTP Details window

The OTP Details window collects and stores information about the customer and OTP version. All the information entered in this section will be part of the CFG and TBB files.

This section is organized into two boxes:

• <u>Customer Details box:</u> Collects and displays customer contact information.

#### KITFS2400FRDMEVM user manual



• **<u>Program Details box</u>**: In addition to comments related to the application, this window allows the user to:

- Select the device type (see Section 7.5.2.1.3 for details)
- Display the OTP ID (set by NXP only)
- Display the build part number (set by NXP only)
- Select the target market, either automotive or industrial

| Balancia de Calencia de Calenc | ls                         |   |
|--|----------------------------|---|
| Program Name*  | [Program Name]             |   |
| Application*   | [Application Description]  |   |
| Production Date  | [Targeted Production Date] |   |
| Sample Date*   | [Require Sample Date]      |   |
| Other Info   |                            |   |
| Device Type  | FS2320-QM                  | Ŧ |
| OTP ID   | AO                         |   |
| OTP Revision<br>Part Number  | A<br>PFS2320AMMA0ES        |   |
|  | Automotive                 | * |

#### 7.5.3 PROG

The PROG tool provides access to device programming configuration and tools for the OTP burning process.

Note: A device can be programmed once. Use this tab cautiously.

The programming tool is available only in Test mode.

The programming screen consists of three sections:

- Device Programming Configuration window
- OTP Mode window
- Fuse Box Status window

#### KITFS2400FRDMEVM user manual



### 7.5.3.1 Device Programming Configuration window

This window allows the user to program an OTP operation from a saved TBB file.

To program the part by fusing OTP, see <u>Section 6.7</u>.

#### 7.5.3.2 OTP mode window

The OTP mode window allows the user to:

- Exit OTP mode by sending a SPI frame.
- Verify that the FS2400 device is in OTP mode (OTP 8 V applied to DBG pin).

#### 7.5.3.3 Fuse Box Status window

The Fuse Box Status window gives status about which sector has already been fused/burned. In the indicator boxes, blue indicates low state, orange indicates high state.

- Sector 0 is NXP proprietary.
- Sector 1 is customer OTP configuration.
- Sector 1bis is a duplicate of Sector 1 for second-time programming.

Notice that an empty part has the following fuse box status, see Figure 43:

|                            | Fuse Bo        | x Status          |
|----------------------------|----------------|-------------------|
|                            | Sector 1 Flags | Sector 1bis Flags |
|                            | Fused          | Fused             |
|                            |                |                   |
|                            | Re             | ead               |
| Eigure 42 Euce Box Status  |                |                   |
| Figure 43. Fuse box Status |                |                   |

#### 7.5.4 SCRIPT

The Script editor allows the user to create new script sequences or to send existing sequences to the device. Commands include reading/writing individually to a register, to a digital pin, or to an analog pin. This tool allows the emulation of an OTP configuration.

The Script editor window consists of four sections:

- Log Window
- Script Commands
- Script Window and its Script bar
- Script Results Window

| ACCESS     | Alias FS2400 💌           |                 |                |
|------------|--------------------------|-----------------|----------------|
| SCRIPTI    | Digital Pins             | 1               |                |
| 0          | Analog Pins              |                 |                |
| (A)        | Register Script Commands | Script Commands | Script Results |
| Log Window | Mode     Panel           | Window          | Window         |
|            | > Control                |                 |                |
| ROMER      | Generator                |                 |                |
| 0          |                          |                 |                |
|            |                          |                 |                |
|            |                          |                 | tu tu dan dan  |
| *          | <b>J</b>                 | Could have      |                |

#### 7.5.4.1 Log window

The Log window lists events as they occur in real time when the script is executing. The Filter Messages box allows the user to limit log messages to certain events (Register Read, Register Write, Pin Read, Pin Write). The Log window menu bar also contains buttons for saving the log contents to a file, clearing the log, or running the script in the Script Command window.

#### 7.5.4.2 Script Commands panel

The Script Commands panel allows the user to enter commands into the Script Command window by clicking on the appropriate command. Facilitated command entry ensures error-free syntax in the command. The commands are organized into functional categories. Opening a panel tab and selecting a specific pin or register makes the associated command appear in the Script Command window.

#### KITFS2400FRDMEVM user manual



- **Digital Pins:** Used to enter a script command to read or writing the value of the selected digital pin
- **Analog Pins:** Used to enter a script command to read the value of the selected analog pin
- **Registers:** Used to enter a script command to read or write functional and safety registers
- Mode: Used to enter a script command setting the desired mode
- **Control:** Used to enter a script command to pause script execution (execution halts when the Pause command is encountered and a pop-up window appears prompting to continue execution), to delay script execution (default is 300 ms, editable to any ms value in the Script Commands window), or to exit script execution
- **Generator:** Used to clear the content of the Script Commands window and enter a pre-prepared script sequence

*Note:* The HELP button in the Script bar gives information about Commands.

|  | -   |   |  | -   |                  |    |
|--|---|---|--|---|------------------|----|
|  | SAVE  | OPEN  | CLEAR HEL  | P   |                  |    |
| Script Editor : Help W   | indew   |   |  |   | - 0 )            | ×  |
| nis help page d  | escribes command  | s available in th   | Get h  |   | Comman           | hd |
| ET_DPIN : set  | s value of a select   | ed digical pin.   |  |   |                  |    |
| ET_DPIN : get<br>ET_APIN : get<br>ET_MODE : se<br>ELAY : introdu<br>AUSE : to paus<br>XIT : to stop th<br>ommand form<br>he following tat                                      | is value of a select<br>is value of a select<br>is device mode. Li<br>ce delay of certain<br>e commands exec<br>ne execution of com<br>nat<br>le describes comm                               | ted analog pin. Return<br>st of modes depends<br>milli seconds betwee<br>ution until the promp<br>mmands at any point<br>mand parameters. All   | ned value is in mV.<br>on a device.<br>en two successive so<br>t is closed.<br>of time.<br>paramaters are man  | cript commands.   |                  |    |
| ET_DPIN : gel<br>ET_APIN : gel<br>ET_MODE : se<br>VELAY : introdu<br>AUSE : to paus<br>XIT : to stop th<br>command form<br>he following tat                                    | is value of a select<br>is value of a select<br>ts device mode. Li<br>ce delay of certain<br>e commands exec<br>e execution of com<br>nat<br>le describes comm<br>1st<br>parameter            | eed onjuta pan.<br>eed analog pin. Return<br>st of modes depends<br>milli seconds betwee<br>ution until the promp<br>mmands at any point of<br>nand parameters. All<br>2nd<br>parameter | ved value is in mV.<br>on a device.<br>In two successive so<br>t is closed.<br>of time.<br>paramaters are man<br>3rd<br>parameter  | aript commands.<br>adatory.<br>4th<br>parameter             | Sth<br>parameter |    |
| ET_DPIN : get<br>ET_APIN : get<br>ET_MODE : se<br>ELAY : introdu<br>AUSE : to paus<br>XIT : to stop th<br>ommand form<br>he following tat<br>Commands<br>SET_REG               | is value of a select<br>is value of a select<br>is device mode. Li<br>ce delay of certain<br>e commands exec<br>se execution of con<br>nat<br>le describes comm<br>ist<br>parameter<br>Device | ed organ pin. Return<br>st of modes depends<br>milli seconds betwee<br>ution until the promp<br>mmands at any point -<br>and parameters. All<br>2nd<br>parameter<br>Reg. set            | ned value is in mV.<br>on a device.<br>In two successive so<br>It is closed.<br>of time.<br><b>3rd</b><br><b>parameter</b><br>Reg. name /<br>Reg. name /<br>Reg. address                                   | ript commands.<br>datory.<br>4th<br>parameter<br>Reg. value | Sth<br>parameter |    |
| ET_OPIN : get<br>ET_APIN : get<br>ET_MODE : se<br>ELAY : introduct<br>AUSE : to paus<br>XIT : to stop th<br>command form<br>he following tat<br>Commands<br>SET_REG<br>GET_REG | is value of a select<br>ts device mode. Li<br>edelay of certain<br>se commands exec<br>e execution of cor<br>nat<br>le describes comm<br>fat<br>parameter<br>Device<br>Device                 | ed organ pin. Return<br>st of modes depends<br>milli seconds betwee<br>ution until the promp<br>mmands at any point -<br>2nd<br>parameter<br>Reg. set<br>Reg. set                       | ned value is in mV.<br>on a device.<br>In two successive so<br>is closed.<br>of time.<br>aramaters are mar<br><b>3rd</b><br><b>parameter</b><br>Reg. name /<br>Reg. address<br>Reg. name /<br>Reg. address | Adatory.  | Sth<br>parameter |    |

All menu items work in a similar way. Figure 45 shows a typical process using the Registers menu tab.

Clicking on the Register tab brings up the parameters panel shown in <u>Figure 45</u>. A Write operation to the CTRL1 register in the functional register group has been selected. The value 0x00 is selected as the value to be written to the register. Hitting **Enter** with the cursor in the value field enters the command in the Script Commands window.

To apply the commands on the Script Commands window, click **RUN** in the Script bar. The sent commands are displayed in the Log window and command results are displayed in the Script Results window.

KITFS2400FRDMEVM user manual

| Log Window                | 88   |   | FC2 400  | Script C  | mmands   | Window  |  |                          |                 |             | Script  | Results Wind | ow   |                   |
|---------------------------|--|---|--|-----------|--|---|--|--------------------------|-----------------|-------------|---------|--------------|--|-------------------|
| Filter Messager           | s 👻 🛄 🕅 R.M.   | Device  | FS2400   | Same      |  |   |  |                          |                 |             | Script  | cesures mine | ~~   |                   |
| 6: Run c<br>and log<br>Lo | Refrent Diabled<br>MPD RELocation Saon<br>command is sent<br>g is displayed in<br>g Window | Alas<br>Digital Pr<br>Analog<br>Analog<br>Registers<br>Operation:<br>Reg Group:<br>Reg Name/4<br>Value: | FS2400<br>1: Select Registeres<br>white<br>asfety<br>rs_LIMPO_RE<br>0x5401 | SET_R     | EG:FS240<br>4:<br>c<br>: Enter<br>: Once<br>on | o:SAFET<br>Hitting<br>ommai<br>Con<br>parami<br>filled, f | :FS_LIMPO<br>Enter a<br>id to Scr<br>imands<br>eters<br>it Enter<br>rd | _REL:0x5<br>dds /<br>ipt | 401             |             | OK : 1  | Registe      | 7: FS_LIMPO_REL : 0x5401<br>7: Command result is<br>displayed in Script<br>Results |                   |
| a .                       |  | # Mode  |  |           | 5: To a  | pply, cli   | ck on RL   | IN                       |                 |             |         |              |  |                   |
| •                         |  | Control   |  | -         | -  |   | -  |                          |                 |             |         |              |  |                   |
|                           |  | > Generato  | ·  | RUN       | 0.00   | SAVE  | OPIN   | CLAR                     | неть            |             | SAVE    | UT OPIN      |  | a                 |
| 32K144 State:             | CONNECTED Protocol: SPI  | Firmware:   | 0.5 Device Mode: test  | -mode Col | or legend                                      | High  | Low  |                          | SM CURRENT STAT | E: OTP_MODE | Debug-M | de: Act      | ive  | 7/28/2022 18:08:3 |

### 7.5.4.3 Script Commands window

The Script Commands window is the area where existing script files can be loaded in Test mode only and where script commands are entered, edited, and executed.

Another use of the SCRIPT tool is to replay a Log command sequence, for example to run a routine. This specific use is detailed in <u>Section 6.8</u>.

The Script bar at the bottom of the window contains the following six buttons:

- RUN: Initiates execution of the script sequence in the Script Commands window.
- LOOP: Executes the script as a loop. Click LOOP, then click RUN.
- SAVE: Saves the content of the Script Commands window as a .txt file that can be subsequently reloaded.
- **OPEN:** Clears the current content and loads a previously saved script into the Script Commands window.

**Note:** The loaded file has to be a TBB file with .txt extension.

- CLEAR: Clears the current content of the Script Commands window.
- HELP: Shows a list of all script editor commands with their formats.



### 7.5.4.4 Script Results window

The Script Results window displays the results of an executed script. The menu bar at the bottom of the window contains three buttons, shown in Figure 47:

- SAVE: Saves the content of the Script Results window as a .txt file that can be subsequently reloaded.
- **OPEN:** Clears the current content and loads a previously saved results file into the Script Results window.
- CLEAR: Clears the current content of the Script Results window.

#### KITFS2400FRDMEVM user manual



#### 7.5.5 MIRROR

The MIRROR tool provides access to all Mirror registers. Mirror registers are an emulation of OTP registers. Mirror registers can be read/written multiple times, whereas OTP registers can be burned once. In case of a power-on reset, the Mirror registers will be reset to the default OTP configuration (empty if OTP sectors not burned). Mirror registers can be read and written in Test mode only, see <u>Section 6.3.2</u>.

**Note:** The MIRROR tool display tabs are similar to the corresponding OTP tool tab. To avoid confusion, the tab header background colors are different. The MIRROR tool tab header is red. The OTP tool tab headers are blue.

The MIRROR tool is available only in Test mode. Test mode loads required keys in order to have full access to Mirror registers. This tool allows the user to:

- Read and write Mirror register values.
- Load a CFG file into the Mirror registers and debug the configuration. For further details on CFG file preparation and parameters configuration, see <u>Section 7.5.2</u>.

| Filter Messages 🔹 🛃 🛃 | System Configuration Regulators Functional Safety Program ID<br>System Configuration | Man All Mile All Old Thinks                          |
|-----------------------|--|--|
|                       | CAN_EN_OTP   | Mc Read current mirror registers content             |
| Address               | SLOT_BYP_OTP N/V   | Write current configuration to Mirror registers      |
| WIIFFOF               | VIDVE TH OTP VIDVE brechold is brical 1.8.V * NV                                     |  |
| tool                  |  | Import registers configuration from CFG file         |
|                       | Write Read   | Export current Mirror registers configuration to CFG |
|                       | Write setting  |  |
|                       | HVIOL Configuration  |  |
|                       | HVIO1PUPD OTP HVIO1 internal pull down and pull up are disabled * N/V                |  |
|                       | HVIO1_OUT_DFLT_OTP HVIO1 default state is ON (asserted) - N/V                        |  |
|                       | HVIO1_OUT_EN_OTP N/V   |  |
|                       | HVI01_SLOT_POL_OTP N/V   |  |
|                       |  |  |
|                       | Write Read   |  |
|                       | Write Read   |  |

# 7.5.6 ACCESS

The ACCESS tool is the central tool for an evaluation session. This tool gives access to GUI functions that configure, monitor, and control the FS2400 device during the evaluation session.

The ACCESS tool provides access to all SPI registers, displayed either ... :

- ... in a Register Map format with direct access to register bits.
- ... in thematic tabs with a graphical and more readable view.

#### KITFS2400FRDMEVM user manual

| Log Window        | 88             | 🗰 Register Map | INIT Safety | Ø WatchDog  | ☑ DiagSafety  | 🛛 Main Tab | O Regulators | 4 Interrupts | HIN AMUX | General IOs | Physical Layers | CRC Calculat |
|-------------------|----------------|----------------|-------------|-------------|---------------|------------|--------------|--------------|----------|-------------|-----------------|--------------|
| Filter Messages * | SAVE CLEAR RUN | Functional     |             | M_DE        | EV_CFG (0x00) | Read       | 0x0000       |              |          |             |                 |              |
|                   |                | Safetu         |             |             |               |            |              | 10           |          |             |                 |              |
|                   |                | Register i     | map (direc  | t access to | o registers   | )          | Ther         | natic tabs   | (select  | ion boxes   | acting on re    | gisters)     |

The Tab Content contains 11 tabs:

- Register Map
- INIT Safety
- WatchDog
- DiagSafety
- Main Tab
- Regulators
- Interrupts
- AMUX
- General IOs
- Physical Layers
- CRC Calculator

#### 7.5.6.1 Register Map

The Register Map tab allows the user to read or write the FS2400 SPI registers, bit per bit.

**Note:** SPI registers are responsible for FS2400 flexible device configuration, in opposition to OTP configuration, which is permanent once fuses are burned. The two levels of configuration work together and should be defined accordingly.

The Register Map is composed of three sub-tabs:

- Functional: Access to SPI functional registers for system configuration.
- Safety: Access to SPI registers relative to Safety functions.
- Write\_INIT\_Safety: Access to SPI registers relative to Safety behavior configuration.

| og Window Øb         | Register Map                | INIT Safety | 🛛 WatchDog 🖓 Dia      | gSafety 🛛 Main        | Tab O Regulat         | tors 4 Interrupts | i iii Amux     | General IOs            | Physical Layers | CRC Calcula |
|----------------------|-----------------------------|-------------|-----------------------|-----------------------|-----------------------|-------------------|----------------|------------------------|-----------------|-------------|
| liter Messages 🔹 就 👔 | Functional                  | 1           | M_DEV_CFG (0x         | 00) Read              | 0x0000                |                   |                |                        |                 |             |
| Register Map menu    | Safety<br>Write_INIT_Safety |             | _ Register nar        | ne and nu             | mber 🔐                | One SPI r         | egister        | RESERVED               | Bit name        | RESERVED    |
|                      |                             |             | RESERVED              | ABIST_EN              | RESERVED              | RESERVED          | LIMPO_BN       | VOMON_EN               | RESERVED        | RESERVED    |
|                      |                             |             | M_DEV_PROG_I          | 0 (0x01) R            | ead 0x2000            |                   |                |                        |                 |             |
| Log Window           |                             |             | FULL_LAYE<br>R_REV[2] | FULL_LAYE<br>R_REV[1] | FULL_LAYE<br>R_REV[0] | METAL_LAYE        | METAL_LAYE     | METAL_LAYE<br>R_REV[0] | RESERVED        | RESERVED    |
|                      |                             |             | PROG_IDH[3]           | PROG_IDH[2]           | PROG_IDH[1]           | Registers<br>wind | Content<br>dow | PROG_IDL(2)            | PROG_IDU[1]     | PROG_JDL[0] |
| ACCESS               |                             |             | M_GEN_FLAG (0         | (02) Read             | 0x0000                |                   |                |                        |                 |             |
| tool                 |                             |             | RESERVED              | RESERVED              | RESERVED              | RESERVED          | RESERVED       | RESERVED               | RESERVED        | RESERVED    |
|                      |                             |             | RESERVED              | WD_G                  | PHYG                  | WUG               | tog            | СОМБ                   | VSUPG           | VXG         |
|                      |                             |             |                       |                       | - (22)                | David Hits        | Descri         |                        |                 |             |

aaa-054183

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#### KITFS2400FRDMEVM user manual

#### 7.5.6.1.1 Modifying one bit by clicking on the bit name

In the Registers Content window, the user can access the FS2400 SPI registers. Two types of registers exist:

- Read Only registers (for example, Status) appear with a Read button only.
- Read/Write registers (for example, System configuration, Regulators Control, and so on) appear with both a **Read** button and a **Write** button.

When the bit is writable, the user can click on a bit name to change its value.



aaa-054184

#### 7.5.6.1.2 Accessing one register content using Read and Write buttons

In the Registers Content window, the user can read and/or write the FS2400 SPI registers using the **Read** and **Write** buttons.

To read the content of one register:

- Click **Read** next to the corresponding register name.
- The current register content is displayed as an hexadecimal value next to the **Read** button.

To write the content of one register:

- Enter the desired register value as an hexadecimal value in the box next to the Write button.
- · Click Write next to the corresponding register name.
- As a cross-check, the current register value can be accessed by clicking Read.

| 03) Read             | 0x0000   | Z  |  |  |  |   |
|----------------------|--|--|--|--|--|---|
| Click <b>Read</b> to | get IAL_S  | INIT_S   | WK3_S  | WK2_S  | RESERVED   | RESERVED  |
| rent register        | value<br>RVED  | RESERVED   | V1_MODE  | V1_S   | RESERVED   | V3_S  |
| x05) Read            | 0x5000   | Write 0x00   | 000  |  |  |   |
| BAT_FAIL             | RESERVED   | POR  | RESERVED   | RESERVED   | GO2INIT  | GO2NORMAL   |
| GO2LPOFF             | INT_TO_WUEN  |  | Nrite register t   | then =   | MOD_CONF   | MOD_EN  |
|                      | 03) Read<br>Click Read to<br>rrent register<br>x05) Read<br>BAT_FAIL<br>GO2LPOFF | 03) Read 0x0000<br>Click Read to get ALS<br>rent register value<br>x05) Read 0x5000<br>BAT_FAIL RESERVED<br>GO2LPOFF INT_TO_WUEN | 03) Read 0x0000 Click Read to get IAL_S INIT_S<br>rrent register value RESERVED RESERVED<br>x05) Read 0x5000 Write 0x00<br>BAT_FAIL RESERVED POR<br>GO2LPOFF INT_TO_WUEN INTB_ V | 03)       Read       0x0000       INIT_S       WK3_S         Click Read to get       IAL_S       INIT_S       WK3_S         rrent register value       NLS_RVED       RESERVED       V1_MODE         x05)       Read       0x5000       Write       0x0000       Image: Constraint of the second o | 03)       Read       0x0000       INIT_S       WK3_S       WK2_S         Click Read to get       IAL_S       INIT_S       WK3_S       WK2_S         rrent register value       NLL_RVED       RESERVED       V1_MODE       V1_S         x05)       Read       0x5000       Write       0x0000       INT_S         BAT_FAIL       RESERVED       POR       RESERVED       RESERVED         GO2LPOFF       INT_TO_WUEN       INTB_       Write register to apply | 03)       Read       0x0000       INIT_S       WK3_S       WK2_S       RESERVED         Click Read to get       IAL_S       INIT_S       WK3_S       WK2_S       RESERVED         rent register value       NUL_RVED       RESERVED       V1_MODE       V1_S       RESERVED         x05)       Read       0x5000       Write       0x0000       Image: Click Reserved       GO2INIT         BAT_FAIL       RESERVED       POR       RESERVED       RESERVED       GO2INIT         GO2LPOFF       INT_TO_WUEN       INTB_       Write register to apply       MOD_CONF |

aaa-054185

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#### 7.5.6.1.3 Accessing one register content using Bit-Map Dialog

In the Registers Content window, the user can read and/or write the FS2400 SPI registers using the Bit-Map Dialog window.

Clicking the **Green Pencil** next to the corresponding register name opens the Bit-Map Dialog window and shows the name and description of all of the register's bitfields.

The Bit-Map Dialog window:

- ... allows the user to change bit values from selection boxes on the left side.
- ... displays the current register content on the right side.

| M_SYS_CFG (0)     | x05) Read 0                                       | 0x5000 Write                 | 0x50   | 00                | Bit-N<br>fo                          | <b>1ap Dialog</b> : Ope<br>r <i>user friendl</i> y re | ns a pop-<br>gister bit | up win<br>s editin | dow<br>g           |
|-------------------|---|------------------------------|--------|-------------------|--------------------------------------|---|-------------------------|--------------------|--------------------|
| RESERVED          | BAT_FAIL  | RESERVED                     |        | POR               | RESER                                |   |                         |                    | 2NORMAL            |
| GO2LPON           | GO2LPOFF  | INT_TO_WUEN                  | IN     | TB_REQ            | INTB_DUR                             | RESERVED  | MOD_0                   | CONF               | MOD_EN             |
| M_SYS_CFG         | (0x05) Bit-Map Dialo                              | 9                            |        | and a Committee   |                                      | ,   | ×                       |                    |                    |
| Set Value (Edita  | able)   |                              |        | Register Conte    | ent                                  |   |                         | зүр                | TSLOT_DO<br>WN_CFG |
| BAT_FAIL:<br>POR: | Battery failure event of<br>Digital POR event occ | occurred<br>urred            | *<br>* | BAT_FAIL:<br>POR: | Battery failure e<br>Digital POR eve | event occurred<br>ent occurred                        |                         | ат                 | OTP_MODE           |
| GO2INIT:          | No action   |                              | -      | INT_TO_WUEN:      | Interrupt Time                       | Out wake up capabilit                                 | y is disabled           | -                  |                    |
| GO2NORMAL:        | No action   |                              | *      | INTB_DUR:         | INTB pulse = 2                       | 25 us   |                         |                    | <u></u>            |
| GO2LPON:          | No action   |                              | *      | MOD_CONF:         | Triangular mod                       | dulation is selected                                  |                         | SON[1]             | BUCK SRHSONIC      |
| GO2LPOFF:         | No action   |                              | *      | MOD_EN:           | Spread spectrum                      | m is disabled   |                         |                    |                    |
| INT_TO_WUEN:      | Interrupt Time Out w                              | ake up capability is disable | ed 🔻   |                   |                                      |   |                         |                    | VODIC              |
| INTB_REQ:         | No effect   |                              |        |                   |                                      |   |                         | 1                  | VSDIS              |
| INTB_DUR:         | INTB pulse = 25 us                                |                              | *      |                   |                                      |   |                         |                    |                    |
| MOD_CONF:         | Triangular modulation                             | n is selected                | *      |                   |                                      |   |                         | -                  |                    |
| MOD_EN:           | Spread spectrum is di                             | sabled                       | *      |                   |                                      |   |                         | I                  | VIUV I             |
|                   | 0   | 0 0                          |        |                   |                                      |   | 0                       | P                  | aaa-(              |

#### 7.5.6.1.4 Modifying multiple registers using the lower Read/Write/Reset bar

In the Registers Content window, the user can read, write, and/or reset multiple FS2400 SPI registers at a time using the lower Read/Write/Reset bar.

- Checkboxes allow the user to select the registers to be read or written.
- Ticking the Select All checkbox allows the user to simultaneously act on all the registers in the Register subtab.
- Read, Write, and Reset buttons allow the user to act on the previously selected registers. Reset switches all the bits in the register to 0.

Navigating in the Registers content in the Register sub-tab can be facilitated using the selection box in the bar.

|                       | RESERVED        | BAT_FAIL     | RESERVED     | POR                      | RESERVED            | RESERVED     | GO2INIT               | GO2NORMAL          |
|-----------------------|-----------------|--------------|--------------|--------------------------|---------------------|--------------|-----------------------|--------------------|
|                       | GO2LPON         | GO2LPOFF     | INT_TO_WUEN  | INTB_REQ                 | INTB_DUR            | RESERVED     | MOD_CONF              | MOD_EN             |
| select the register   | M_SYS1_CFG (0   | x06) Read (  | 0x0000 Write | e 0x0000                 |                     | ,            |                       |                    |
|                       | RESERVED        | RESERVED     | RESERVED     | VBOS2V1_SW_<br>ALWAYS_EN | RESERVED            | LOAD_OTP_BYP | SLOT_BYP              | TSLOT_DO<br>WN_CFG |
|                       | RESERVED        | SOFTPOR_REQ  | RESERVED     | Read, Write or Re        | set all selected re | egisters VED | OTP_EXIT              | OTP_MODE           |
| elect all registers - | Select All M_DE | V_CFG (0x00) | • (42) Read  | Write Reset              |                     | type reg     | gister name to search | <u>`</u>           |

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#### KITFS2400FRDMEVM user manual

#### 7.5.6.2 INIT Safety

The INIT Safety tab allows the user to read or write the parameters related to the Safety functions initialization and Safety behavior configuration.

- Fault Impact: Used to choose Safety pin assertion response to each fault source.
- Safety Behavior Config: Used to initialize and configure Safety-related functions such as error counters limits, RSTB, ERRMON.
- **INIT CRC:** Used to compute the INIT CRC (FS\_CRC register) based on the connected device INIT registers (FS\_I\_XXX) content. When no board is connected, the tab data is used.

| Log Window      | INIT Safe          | ety menu   | murr cafaty   | Quanton  | Constatute.                           | Q Mars Tab   | () Burndahara  | G totomote   | E mar  | Write/Read all | the values from this tab   |                   |
|-----------------|--------------------|--|---|--|---------------------------------------|--|--|--|--|----------------|--|-------------------|
| Filter Messages | * SWE LOTAN L R.M. | INIT Safety  | P Diel Direcy   | O Habbody  | C Daysarety                           | C Patr (ap   | C Regulators   | 7 anemajos   | ABB HIMUN  |                |  | Read All          |
|                 |                    | Fault source RSTB<br>VMON0_UV<br>VMON0_DV<br>VMON1_UV<br>VMON1_UV<br>VMON1_UV<br>Impact  | Fault Impo<br>Fault In<br>Wind  | at<br>alt source RSTB<br>colog_uv<br>pact<br>cow<br>T_CRC<br>P0_SC<br>V    |                                       |  |  |  |  |                |  |                   |
|                 |                    |  | Write   | Read   |                                       |  |  |  |  |                |  |                   |
|                 |                    | Watchdog error limit (<br>Watchdog refresh limit (<br>Fault error limit (  | 8<br>6<br>Max Value = 6<br>Write  | Error Co   | ounters Limit                         | Error cou<br>config                                      | • NV<br>nters limit<br>uration   | Enable<br>Disable<br>RSTB P<br>Disable                             | RST8 request by<br>ext. RST8 Monit<br>ulse Duration<br>8s timer  | MCU            | Hiscellaneeus RSTB configur  | ۹/۷<br>۹/۷<br>۹/۷ |
|                 |                    | Watchdog error limit<br>Watchdog refresh limit<br>Pault error limit<br>DRRMON PS Reaction<br>BRMON Admoniedge Ti<br>BRMON Admoniedge Ti<br>BRMON Fault Polarity<br>LIMPD GPO | 8 6 Max Value = 6 Write RSTb and L1 Conservation Low level is a Write Write | Emory Co<br>ERRHOW<br>MPD only a assert<br>a fault after a neg<br>afety pn | configuration<br>ed low in case of fa | Error cou<br>config<br>uit detected on BPJ<br>Ex<br>m mc | <ul> <li>NV</li> <li>Inters limit<br/>uration</li> <li>INV</li> <li>Internal IC<br/>enitoring</li> <li>NV</li> </ul> | Enable<br>Desble<br>RSTS P<br>Deable<br>Disable<br>Disable<br>Comp | RSTB request by<br>ext. RSTB Mont<br>ulse Duration<br>Be timer<br>RC Status<br>Autor 0<br>International Content<br>and the INT CRC 0 | 100            | Hiscolaneous<br>RSTB configur<br>avrr Cisc<br>INIT CRC calculation<br>tool | ation v           |

#### 7.5.6.3 Watchdog

The Watchdog tab allows the user to read or write the parameters related to the Watchdog management and to observe watchdog error and refresh counters evolution.

- Watchdog Management: Used to manage watchdog operation by sending SPI commands and set watchdog functional parameters, such as window timing.
- Watchdog Counters Config: Used to read the previously set watchdog error and refresh counters' limit values and fault error limit action. See <u>Section 7.5.6.2</u> tab for parameter settings.
- Watchdog Error and Refresh Counts diagrams: Used to display the diagrams of watchdog error count and watchdog refresh count relative to the previously set watchdog error and refresh counters limit values.

### KITFS2400FRDMEVM user manual

|   | A Managamant   |                                  |   |  |
|---|--|----------------------------------|---|--|
| Watchdog Type<br>Good watchdog refresh<br>Bad watchdog refresh  | Watchdog W Y N/V<br>WD Answer Good<br>WD Answer Bad  | Last Recorded Error Count: N/V   |   |  |
| LIMPO release LIMPO Watchdog WD_T W Watch Watchw renew wannee Fault error counter WD Window Period WD Window Recovery ite/Read values Witte | LIMPO Release Command<br>Management<br>indow<br>NV<br>INFINITE TI = N/V<br>INFINITE TI = N/V<br>S from the box | Last Recorded Refresh Count: N/V | Watchdog Error and<br>Refresh Counts diagrams |  |
| Watch<br>Watch Watchd<br>Fault c  | og Counters  |                                  |   |  |

### 7.5.6.4 DiagSafety

The DiagSafety tab allows the user to carry out the safety diagnosis by reading or clearing the interrupt and status bits of the Safety pins and the UV/OV monitoring bits. The DiagSafety tab also allows the user to launch ABIST and to observe the ABIST status.

- Safety IO Diagnosis: Used to request and observe the diagnosis on the Safety I/O pins.
- ABIST Diagnosis: Used to operate ABIST on the device.
- ERRMON Diagnosis: Used to carry out the diagnosis on external IC error monitoring.
- **UV/OV Diagnosis:** Used to carry out the diagnosis on UV/OV monitoring by exploiting interrupt flags and realtime status bits.
- General Flags: Displays general error flags states.

#### KITFS2400FRDMEVM user manual

| Safe 10  | Diag Sa           | fety                       | OV/UV Status   |
|--|-------------------|----------------------------|--|
| IMPD Driver     N/V       IMPD Sense     N/V       IMPD Diag     N/V       IMPO Request     N/V       External Reset     N/V       ISTB Driver     Safety IO       ISTB Event     Diagnosis       ISTB Reque     window       Write     Read | ERRMON Admowledge | RRMON<br>liagnosis<br>Read | VOMON UV N/V<br>VOMON OV N/V<br>VIMON UV LPON N/V<br>VIMON UV UV/OV Diagnosis<br>VIMON UV Window<br>VIMON OV N/V<br>Write Read |
| ABIST Re   | quest and Status  |                            | General Flags  |
| 1. Always Clear ABIST Result Before Launch   | 3.1               | aunch ABIST                | Falsafi  |
| Clear ABIST Write  | Launch Full ABIST | Write                      |  |

### 7.5.6.5 Main Tab

The Main Tab allows the user to manage the Clock Modulation, to monitor occurrence of events, to operate the Device States, and to configure the FS2400 LDT.

- Clock Management: Used to enable and set 20 MHz clock modulation.
- General Flags: Used to monitor occurrence of events on FS2400 functions.
- Device State: Used to monitor the device's state and request mode change.
- LDT Configurations: Used to enable, configure, and monitor the LDT.
- LDT Calibration: Used to emulate the LDT calibration procedure using the device's embedded S32K144. The MCU software timing measurements are not accurate, the GUI tool is only intended to help understand the procedure.
- **Miscellaneous management:** Used to monitor FS2400 external events and FS2400 VBOS, or to monitor time slots' attribution and interrupt settings.

#### KITFS2400FRDMEVM user manual

| Clock Manag  | ement   |  | LDT  | Configurations   |  |
|--|---|--|--|--|--|
| Iock Modulation enable<br>Iock Modulation CFG                  | Register Content<br>N/V<br>DW<br>Read   | LDT Mode(time base)<br>LDT After Run Value<br>LDT Wake-up Value<br>LDT Function<br>LP Mode Transition(for F2 and F3)<br>LDT Status<br>LDT Enable<br>LDT timer selection<br>LDT WU Enable | Select Value<br>LDT is set to long<br>0x0<br>0x0<br>Function1 is<br>Go to LPOFI<br>Target value of W<br>No wake up and | g count (1 s) DT Config window Vake-up LDT timer can I no Interrupt the Bred | Register Content           N/V           N/V |
| LDT Calibration  | Miscel  | laneous<br>Value Depictor Conton   | SDI Commun   | General Flags  | Device State   |
| xpect 0x8718<br>(ctual LDT Calibration N/V<br>N/V<br>Calibrate | BAT Fail<br>POR Event<br>VBOS_UV<br>VBOS to V1 switch Statue<br>Bypass OTP Loadin Misco<br>Bypass Slot 2<br>Power Down Time Manager<br>INTB Assertion Request<br>Enable Interrupt TO Wake-up<br>INTB Pulse Duration INTB p<br>Write | ellaneous<br>nent window<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V<br>N/V   | Regulator Ev<br>VSUF<br>Wakk Ger<br>Physi<br>IO E  | neral Flags<br>window<br>N/V<br>Read   | Go to Normal Mode OTP Mode N/V<br>Exit OTP Mode Det<br>Exit Device State<br>LPC window<br>Go to LPOFF<br>Go to INIT<br>Request Soft POR<br>Write Read  |

### 7.5.6.6 Regulators

The Regulators tab allows the user to control and read status related to HVBUCK (V1) and HVLDO3 (V3).

The Regulators tab gives access to the real-time status of each regulator for monitoring, and allows the user to control the regulators' enablement in specific modes.

- V1 HVBUCK: Used to configure V1 slew rate and DVS parameters
- Regulators Control: Used to control the regulators' enablement and V1 DVS transition

|  | V1 HVBUCK  |  | Regulators Control  |
|--|--|--|---|
| V1 HS Rising SR<br>V1 HS Falling SR<br>V1 DVS output voltage<br>V1 DVS Slew Rate | Select Value HS raising slew rate is 20 ns HS falling slew rate is 20 ns U1 HVBUCK 22.50 mV/i Write Read | Register Content           • N/V           • N/V           • N/V           • N/V           • N/V | V1 Enable in Normal Mode<br>V1 Disable in Normal Mode<br>V3 Enable<br>V3 Disabl<br>Keep V3 ( Regulators Control<br>V1 voltag<br>V1 Go to DV5 value<br>V1 Go to DFLT value |

Figure 50. Regulators menu

#### 7.5.6.7 Interrupts

The Interrupts tab is arranged as an Interrupt board with two thematic sub-menus displaying one Interrupt box per function:

#### KITFS2400FRDMEVM user manual

- Main Interrupt Configuration: Used to monitor events related to FS2400 functional features, such as regulators' temperature and current, SPI communication, physical layers, timers, high-side drivers, I/Os, and wake-up events.
- FailSafe Interrupt Configuration: Used to monitor safety-related events, such as undervoltage/overvoltage, or open load on regulators, and events on safety pins or functions.



#### Figure 51. Interrupts menu

| Masking an Interrupt inhibits its treatment   |
|---|
| Clearing an Interrupt<br>takes down the flag<br>Watz Steet<br>Blue: Clear activated<br>Blue: Clear not activated<br>Wite Rest Path<br>Displays the<br>real-time status<br>Orange: High<br>Blue: Low<br>Wite Rest Path<br>Displays mask status |
| Write to Clear         Read current Clear         Poll continuously           Mask an interrupt         and Mask status         Clear and Mask status   |
| aaa-054194  |

#### Figure 52. Masking an interrupt

In each Interrupt box, the interruption can be:

- Read: Click Read on each box to read the current status or Read all to update the whole Interrupt board.
- Polled: Click Poll on each box to read the current status periodically.
- Cleared: Select each check box from the Clear column, then click Write to apply.
- Masked: Select each check box from the Mask column, then click Write to apply.

### 7.5.6.8 AMUX

The AMUX tab allows the user to measure voltage levels and temperature values of multiple key locations from onboard sensors. The AMUX feature uses one ADC to successively measure multiple points. The AMUX feature must be enabled beforehand by clicking **Enable AMUX**.

- **AMUX Measurements:** Displays the measurements of voltage levels and temperature values from sensors placed at key locations. Click **Read** to obtain or refresh the measurements.
- **ADC Measurements:** Displays the measurements of valuable voltage levels sensed by an ADC outside of the AMUX feature. Click **Read** to obtain or refresh the measurements.
- Voltage Measurement graph: Displays the selected voltage measurement values as a function of time in a graph.
- **Temperature Measurement graph:** Displays the selected temperature measurement values as a function of time in a graph.



Figure 53. AMUX menu

Measurement graphs can be edited using the editing bar, as shown in Figure 54:

#### KITFS2400FRDMEVM user manual

| Select m               | easura | ble  | value      |                     |                    |               |                  |              |                           |                                    |              |
|------------------------|--------|------|------------|---------------------|--------------------|---------------|------------------|--------------|---------------------------|------------------------------------|--------------|
|                        | Add n  | neas | urable v   | alue to             | graph              |               |                  |              |                           |                                    |              |
|                        |        | Ren  | nove me    | asurat              | ole value          | e from graph  |                  |              |                           |                                    |              |
| Voltrge Heasurement    | + 1    | ~    | Clear      | Pol                 | Y min 0            | Y max 1000    | Auto Scale Y X n | nin 0        | X max 30                  | Auto Scale X                       | <b>*</b>     |
| 1000 1                 |        |      | Po<br>Empt | ll the v<br>y the g | values to<br>graph | o update graj | bh               | Autos        | caling is C<br>n/max valu | )<br>DFF, please e<br>ues on X and | nter<br>I Y  |
| Temperature Measurem   | ent    |      |            |                     |                    |               |                  |              |                           |                                    |              |
| Die Temperature Sensor | •      | ÷    | ] • ]      | Clear               | Pol                | Y min         | max 🖄            | Auto Scale Y | X min                     | X max                              | Auto Scale X |
| 20 <sub>1</sub>        |        |      |            |                     |                    |               |                  |              | Autosca                   | ling is ON                         | aaa-054197   |
| Figure 54. M           | easure | men  | t graphs   | ;                   |                    |               |                  |              |                           |                                    |              |

#### 7.5.6.9 General IOs

The General IOs tab allows the user to control I/Os levels, to configure Wake-Up/Interrupt features, and to operate the Cyclic Sense feature depending on the I/Os desired configuration. A pre-configuration of the I/Os is done by OTP.

- IO Control: Used to control I/O output levels when configured as outputs
- IO Wake-up Config: Used to set I/O wake-up parameters when the Wake-Up feature is enabled on I/O
- IO Wake-up/Interrupt Enable: Used to enable Wake-Up and/or Interrupt feature on I/O
- **HW ID Config:** Used to enable and disable pullup and pulldown current sources on FS2400 WAKE2/HID0 and WAKE3/HID1 pins

| General IOs          |  |   |   |                                 | Read   | All Write Al |
|----------------------|--|---|---|---------------------------------|--|--------------|
| 10 Control           | IO Wake-up Config  |   | IO Wake-up/Interrupt F  | nable                           | HW ID Config   |              |
| IO Control<br>window | WAKE2 Configuration Light level wake up is configured<br>WAKE2 Deglitcher Time 15 us<br>WAKE3 Configuration<br>WAKE3 Configuration<br>HVI01 Configuration<br>HVI01 Configuration<br>HVI01 Deglitcher Tir<br>Write Read | <ul> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> </ul> | WARE2 IN IO Wake-Up/<br>WARE3 IN IO Wake-Up/<br>HVT01 IN Interrupt<br>Enable window<br>Withe Read | Register Content<br>N<br>N<br>N | Chable WAK2 PD<br>Disable WAK2 PD<br>E HW ID<br>E control<br>Disable WAK3 PD<br>Enable WAK3 PU<br>Disable WAK3 PU<br>Write |              |

#### 7.5.6.10 Physical Layers

The Physical Layers tab allows the user to configure the CAN and LIN transceivers and monitor status.

CAN Config/Status: Used to configure the CAN transceiver and monitor status. The CAN setting on the MCU side and the CAN frames sending is done using <u>the CAN</u> tool.

#### KITFS2400FRDMEVM user manual

| I Register Map   | ☺ INIT Safety                    | Ø WatchDog                                      | Ø DiagSafety        | ⊘ Main Tab   | O Regula | <b>Physical Layers menu</b> | I IOs 🛛 Physical Layers |
|--|----------------------------------|---|---------------------|--|----------|-----------------------------|-------------------------|
| Physical Layers  |                                  |   |                     |  |          |                             | Read All Write A        |
|  |                                  | CAN Config/                                     | Status              |  |          |                             |                         |
| CAN Mode<br>CAN FS Disable<br>CAN WU Enable<br>CAN Active Mode 9<br>CAN TXD Dominar<br>CAN TSD | Trans<br>No w<br>Status<br>nt TO | ceiver offline<br>ake up and no intr<br>CAN Con | errupt<br>fig/Statu | <ul> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> <li>N/V</li> </ul> | Conten   |                             |                         |
| CAN SM State<br>Bypass CANRXD as   | ssert low Trxd_                  | wu_timeout not                                  | bypassed            | • N/V  |          |                             |                         |

Figure 56. Physical Layers tab

### 7.5.6.11 CRC Calculator

The CRC Calculator tab allows the user to compute the FS2400 SPI CRC using the specified polynomial. It can be useful while debugging SPI communication.

|                           | CRC Calculator  |
|---------------------------|---|
|                           | CRC Calculation (SPI)   |
|                           | Operation: <ul> <li>Read</li> <li>Write</li> </ul> Register Address:             0x00               Data:             0x0000               CRC:             N/V               Polynomial:             [x^8 + x^4 + x^3 + x^2 + 1]               Calculate |
|                           | aaa-054200  |
| Figure 57. CRC Calculator |   |

#### 7.5.7 MCU PINS

The MCU PINS tool provides a means of reading and setting the S32K144 input and for signals RSTB and LIMP0.

The MCU PINS panel consists of three sections:

- Log window: Maintains a running log of events initiated during the current session. A drop-down menu in the upper left allows the log to be filtered by register read, register write, pin read, and pin write. Buttons in the upper right allow the Log window contents to be saved, cleared, or run.
- MCU Input Pins Reading window: Allows the listed MCU pins to be read or polled during a selected time duration.

### KITFS2400FRDMEVM user manual

|              | FS2400 Start user-mode - Pollind | SPI Freq (KHz): 2000 - Enable WatchDog Refresh Deriod 1 ms                |  |
|--------------|----------------------------------|---|--|
|              | Log Window                       | LIMPO_MCU : Du MCU Input Pins<br>Reading Window oll Read<br>RSTB_MCU : Du |  |
|              | CAN                              | aaa-054213  |  |
| Figure 58. M | CU PINS tool                     |   |  |

### 7.5.8 CAN

The CAN tool provides a means to use the CAN transceiver on FS2400. The CAN tool allows the user to configure the CAN bus on the MCU side and send edited frames on the bus sporadically or periodically with a selectable repeat rate. CAN logs are displayed.

The CAN tool panel consists of four sections:

- MCU CAN Configuration window: Used to configure the CAN on the MCU S32K144 side by choosing the baud rate, CAN ID, and payload size
- CAN Send window: Used to send CAN frames sporadically or periodically with a selectable repeat rate from 200 ms to 100 s
- CAN Frames Editing window: Used to edit the frames to be sent on the CAN bus (up to 64 bytes) with hexadecimal values. CAN frames can be imported and saved as CSV files
- CAN Log window: Displays exchanged frames
- CAN Tool state machine: Helps with understanding how to use the CAN tool


KITFS2400FRDMEVM user manual

### 8 References

1. FS2400 — product information on FS2400, Safety system basis chip, fit for ASIL B

#### KITFS2400FRDMEVM user manual

## 9 Revision history

| Document ID   | Release date     | Description     |
|---------------|------------------|-----------------|
| UM12014 v.1.0 | 01 February 2024 | Initial version |

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UM12014

#### KITFS2400FRDMEVM user manual

### Tables

| Tab. 1.  | Evaluation board components description11     |  |
|----------|---|--|
| Tab. 2.  | Evaluation board LED signaling description 13 |  |
| Tab. 3.  | Main VBAT Phoenix connector (J6) 14           |  |
| Tab. 4.  | Alternative VBAT Phoenix connector (J35)14    |  |
| Tab. 5.  | V3 (HVLDO) connector (J7)14                   |  |
| Tab. 6.  | V1 (HVBUCK) connector (J12) 15                |  |
| Tab. 7.  | V3 CAN bus connector (J16) 15                 |  |
| Tab. 8.  | Communication debug connector (J44)15         |  |
| Tab. 9.  | FS2400 signals debug connector (J4)15         |  |
| Tab. 10. | USB connector (J48)16                         |  |
|          |   |  |

| Tab. 11. | JTAG connector (J38)                     | 16 |
|----------|--|----|
| Tab. 12. | Evaluation board test points description | 16 |
| Tab. 13. | Evaluation board jumpers description     | 18 |
| Tab. 14. | SW1 description                          | 18 |
| Tab. 15. | S2 description                           | 19 |
| Tab. 16. | SW4 description                          | 19 |
| Tab. 17. | SW9 description                          | 19 |
| Tab. 18. | SW12 description                         | 19 |
| Tab. 19. | SW19 description                         | 19 |
| Tab. 20. | Revision history                         | 74 |
|          | -  |    |

#### KITFS2400FRDMEVM user manual

## Figures

| Fig. 1.  | KITFS2400FRDMEVM evaluation board1        |
|----------|---|
| Fig. 2.  | Communication/I/Os connection to          |
| -        | S32K1446                                  |
| Fig. 3.  | J44 Communication/I/Os connection7        |
| Fig. 4.  | VDDIO selection7                          |
| Fig. 5.  | VDDIO power supply selection7             |
| Fig. 6.  | P_VAR supply configuration8               |
| Fig. 7.  | List of analog inputs8                    |
| Fig. 8.  | OTP block diagram9                        |
| Fig. 9.  | OTP mode entry10                          |
| Fig. 10. | OTP hardware implementation10             |
| Fig. 11. | Evaluation board featured components      |
|          | location11                                |
| Fig. 12. | Evaluation board LED signaling location13 |
| Fig. 13. | Evaluation board connectors14             |
| Fig. 14. | Evaluation board test points16            |
| Fig. 15. | Evaluation board jumpers location (with   |
|          | default position)17                       |
| Fig. 16. | Switches location                         |
| Fig. 17. | Example debugger20                        |
| Fig. 18. | Proper connection20                       |
| Fig. 19. | NXP GUI setup27                           |
| Fig. 20. | NXP GUI setup configuration27             |
| Fig. 21. | NXP GUI setup completion28                |
| Fig. 22. | Power supply                              |
| Fig. 23. | Device modes                              |
| Fig. 24. | ACCESS tool                               |
| Fig. 25. | Framework window42                        |
| Fig. 26. | Framework settings43                      |
| Fig. 27. | File menu item43                          |
| Fig. 28. | Help menu item45                          |

| Fig. 29.      | Watchdog enablement/disablement/  |    |
|---------------|-----------------------------------|----|
| <b>-</b> : 00 | configuration                     |    |
| Fig. 30.      | Watchdog enablement boxes         |    |
| Fig. 31.      | Watchdog enablement               |    |
| Fig. 32.      | Enable WatchDog Refresh unchecked |    |
| Fig. 33.      | USB and Device status bar         |    |
| Fig. 34.      | Power tool                        | 50 |
| Fig. 35.      | OTP Tool tabs                     | 51 |
| Fig. 36.      | Power cequence configuration      |    |
| Fig. 37.      | Regulators tab                    | 52 |
| Fig. 38.      | Functional Safety tab             | 53 |
| Fig. 39.      | Program ID tab                    | 53 |
| Fig. 40.      | Customer Details box              | 54 |
| Fig. 41.      | Program Details box               | 54 |
| Fig. 42.      | PROG tool                         | 55 |
| Fig. 43.      | Fuse Box Status                   | 55 |
| Fig. 44.      | SCRIPT tool                       | 56 |
| Fig. 45.      | Parameters panel                  | 58 |
| Fig. 46.      | Script Commands window            | 58 |
| Fig. 47.      | Script Results window             | 59 |
| Fig. 48.      | MIRROR tool                       | 59 |
| Fig. 49.      | Main Tab                          | 66 |
| Fig. 50.      | Regulators menu                   | 66 |
| Fig. 51.      | Interrupts menu                   | 67 |
| Fig. 52.      | Masking an interrupt              | 67 |
| Fig. 53.      | AMUX menu                         | 68 |
| Fig. 54.      | Measurement graphs                | 69 |
| Fig. 55.      | General IOs tab                   | 69 |
| Fia. 56.      | Physical Lavers tab               | 70 |
| Fig. 57.      | CRC Calculator                    |    |
| Fig. 58       | MCU PINS tool                     | 71 |
| Fig. 59       | CAN tool                          |    |
|               |                                   |    |

77 / 79

#### KITFS2400FRDMEVM user manual

#### Contents

| 1          | Introduction                              | 6.      |
|------------|---|---------|
| 2          | on the NYP web site                       | ю.<br>6 |
| 21         | Collaborate in the NXP community 4        | 6       |
| 3          | Getting ready 5                           | 6.      |
| 31         | Kit contents 5                            | 6.4     |
| 3.2        | Additional hardware 5                     | 6.      |
| 3.3        | Windows PC workstation 5                  | 0.      |
| 3.4        | Software 5                                | 6.      |
| 4          | Getting to know the hardware6             |         |
| 4.1        | Kit overview6                             | 6.0     |
| 4.1.1      | KITFS2400FRDMEVM features6                | 6.0     |
| 4.1.2      | Communication and I/Os between FS2400     |         |
|            | and S32K144 MCU6                          | 6.      |
| 4.1.3      | VDDIO selection7                          | 6.8     |
| 4.1.4      | S32K144 ADC8                              |         |
| 4.2        | Device OTP user configuration9            | 6.9     |
| 4.2.1      | OTP and mirror registers9                 | 7       |
| 4.2.2      | OTP hardware implementation9              | 7.      |
| 4.3        | Kit featured components10                 | 7.2     |
| 4.3.1      | FS2400: Fail-safe system basis chip with  | 7.3     |
|            | SMPS and LDO12                            | 7.3     |
| 4.3.1.1    | General description12                     | 7.3     |
| 4.3.1.2    | Features12                                | 7.3     |
| 4.3.2      | LED signaling13                           | 7.3     |
| 4.3.3      | Connectors14                              | 7.3     |
| 4.3.3.1    | VBAT connectors14                         | 7.3     |
| 4.3.3.2    | Output power supply connectors            | 7.3     |
| 4.3.3.3    | CAN bus connector                         | 7.:     |
| 4.3.3.4    | Debug connectors                          | _       |
| 4.3.3.5    | S32K144 communication connectors          | 7.3     |
| 4.3.4      | lest points                               | 1.4     |
| 4.3.5      | Jumpers                                   | 7.5     |
| 4.3.6      | Switches                                  | 7.3     |
| 4.4        | Schematic, board layout and bill of       | 7.3     |
| 5          | Installing and configuring coffware and   | 7.3     |
| 5          | tools 20                                  | 7.      |
| 51         | Elashing or undating the GLII firmware 20 | 7.      |
| 511        | Hardware setup 20                         | 7.      |
| 512        | Software setup                            | 7       |
| 5.2        | Installing NXP GLII software package 27   | 7       |
| 5.3        | Launching the ES24 NXP GUI 28             | 7       |
| 6          | Configuring the hardware and software 30  | 7       |
| <b>6</b> 1 | Setting up the KITES2400ERDMEVM 30        | 7       |
| 6.2        | Connecting the KITFS2400FRDMEVM to        | 7.      |
| 0.2        | the GUI                                   | 7.      |
| 6.3        | Operation modes                           | 7.      |
| 6.3.1      | Debug mode                                | 7.      |
| 6.3.1.1    | Debug mode definition                     | 7.      |
| 6.3.1.2    | Debug mode activation                     | 7.      |
| 6.3.1.3    | Debug mode deactivation                   | 7.      |
| 6.3.2      | Test mode                                 | 7.      |
| 6.3.2.1    | Test mode definition                      | 7.      |
| 6.3.2.2    | Test mode activation: hardware and        | 7.      |
|            | software                                  | 7.      |

| 6222         | Test mode desstivation                        | 24       |
|--------------|---|----------|
| 0.3.2.3      | Test mode operation                           | 34       |
| 633          | Lesi mode operation                           | 34       |
| 6221         | User mode definition                          | 24       |
| 6333         | User mode activation                          | 2/       |
| 0.3.3.Z      |   | 25       |
| 0.4          | Generate a TDB Script                         |          |
| 0.5          | as Infinite Time Out                          | 35       |
| 66           | Operate OTP emulation by leading              | 35       |
| 0.0          | configuration in the Mirror registers         | 37       |
| 661          | Modify the Mirror registers with a TRR script | 37       |
| 662          | Modify the Mirror registers with the          | 57       |
| 0.0.2        |   | 37       |
| 67           | Programming an OTP operation                  | 38       |
| 6.8          | Save a routine from Log window then Run it    | 50       |
| 0.0          | as a Script                                   | 30       |
| 60           | TBB script example                            | 10       |
| 7            | Tool interface (GUI) description              | 40       |
| 71           | Framowork window                              | 42       |
| 7.1          | Framework softings                            | 42<br>12 |
| 7.Z          | File monu item                                | 43       |
| 7.2.1        | View monulitom                                | 4J       |
| 7.2.2        | Import/Export monu itom                       | 43       |
| 7.2.3        | Holp monu item                                | 44       |
| 7.2.4<br>7.2 | Connection Teelber                            | 45       |
| 7.3          |   | 40       |
| 7.3.1        | Device connection configuration               | 45       |
| 7.3.2        | SPI communication configuration               | 40       |
| 7.3.3        | Watchdog management and configuration on      | 40       |
| 1.3.3.1      | FS2400 side                                   | 46       |
| 7.3.3.2      | Watchdog configuration on MCU side            | 47       |
| 7.4          | USB and Device status bar                     | 48       |
| 7.5          | Tools access bar                              | 49       |
| 7.5.1        | POWER   | 49       |
| 7.5.2        | OTP   | 50       |
| 7.5.2.1      | OTP Parameters Setting window                 | 51       |
| 7.5.2.2      | OTP Details window                            | 53       |
| 7.5.3        | PROG  | 54       |
| 7.5.3.1      | Device Programming Configuration window       | 55       |
| 7.5.3.2      | OTP mode window                               | 55       |
| 7.5.3.3      | Fuse Box Status window                        | 55       |
| 7.5.4        | SCRIPT  | 56       |
| 7.5.4.1      | Log window                                    | 56       |
| 7.5.4.2      | Script Commands panel                         | 56       |
| 7.5.4.3      | Script Commands window                        | 58       |
| 7.5.4.4      | Script Results window                         | 58       |
| 7.5.5        | MIRROR  | 59       |
| 7.5.6        | ACCESS  | 59       |
| 7.5.6.1      | Register Map                                  | 60       |
| 7.5.6.2      | INIT Safety                                   | 63       |
| 7.5.6.3      | Watchdog                                      | 63       |
| 7.5.6.4      | DiagSafety                                    | 64       |
| 7.5.6.5      | Main Tab                                      | 65       |
| 7.5.6.6      | Regulators                                    | 66       |
| 7.5.6.7      | Interrupts                                    | 66       |
| 7.5.6.8      | AMUX  | 68       |

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#### KITFS2400FRDMEVM user manual

| General IOs       | 69   |
|-------------------|--|
| Physical Layers   | 69   |
| CRC Calculator    | 70   |
| MCU PINS          | 70   |
| CAN               | 72   |
| References        | 73   |
| Revision history  | 74   |
| Legal information | 75   |
|                   | General IOs<br>Physical Layers<br>CRC Calculator<br>MCU PINS<br>CAN<br>References<br>Revision history<br>Legal information |

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